

HD74LV1G00A

2-input NAND Gate

R04DS0017EJ0900 Rev.9.00 Jan 10, 2014

Description

The HD74LV1G00A has two-input NAND gate in a 5 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

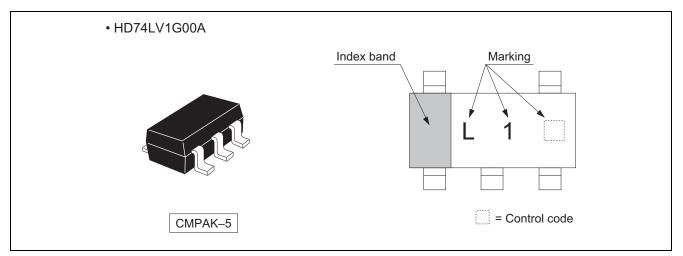
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV00A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current $\pm 6 \text{ mA}$ (@V_{CC} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G00ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G00AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

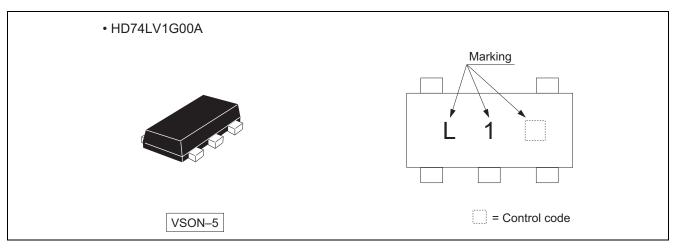
Note: Please consult the sales office for the above package availability.

Outline and Article Indication





Outline and Article Indication



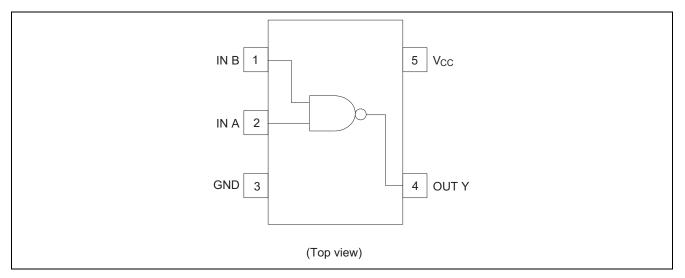
Function Table

Inp	Inputs					
A	В	Output Y				
L	L	н				
L	Н	н				
Н	L	н				
Н	Н	L				

H : High level

L : Low level

Pin Arrangement





Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{cc}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	M	–0.5 to V _{CC} + 0.5	- V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	V	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	I _{OK}	±50	mA	$V_0 < 0 \text{ or } V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_{O} = 0$ to V_{CC}
Continuous current through V_{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) *3	PT	200	mW	
Storage temperature	Tstg	–65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Мах	Unit	Conditions
Supply voltage range	V _{cc}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		—	1		$V_{CC} = 1.65$ to 1.95 V
		—	2	- mA	$V_{CC} = 2.3$ to 2.7 V
	I _{OL}	—	6		$V_{CC} = 3.0$ to 3.6 V
		—	12		$V_{CC} = 4.5$ to 5.5 V
Output current	I _{он}	—	-1		V _{CC} = 1.65 to 1.95 V
			-2		$V_{CC} = 2.3$ to 2.7 V
			-6		$V_{CC} = 3.0$ to 3.6 V
		—	-12		$V_{CC} = 4.5$ to 5.5 V
		0	300		$V_{CC} = 1.65$ to 1.95 V
Input transition rise or fell rate	$\Delta t / \Delta v$	0	200	ns / V	$V_{CC} = 2.3$ to 2.7 V
Input transition rise or fall rate	Δι / Δν	0	100	115 / V	$V_{CC} = 3.0$ to 3.6 V
		0	20		$V_{CC} = 4.5$ to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.



Electrical Characteristics

• Ta = -40 to $85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	V	2.3 to 2.7	V _{CC} ×0.7	—	—		
	VIH	3.0 to 3.6	V _{CC} ×0.7	—	—		
		4.5 to 5.5	V _{CC} ×0.7	—	—	V	
Input voltage		1.65 to 1.95	—	—	V _{CC} ×0.25	v	
	V	2.3 to 2.7	—	—	V _{CC} ×0.3		
	V _{IL}	3.0 to 3.6	—	_	V _{CC} ×0.3		
		4.5 to 5.5	—	_	V _{CC} ×0.3		
		1.8	—	0.25	—		
	V	2.5	—	0.30	—	V	$V_{T}^{+} - V_{T}^{-}$
Hysteresis voltage	V _H	3.3	—	0.35	—	v	$v_{T} - v_{T}$
		5.0	—	0.45	—		
		Min to Max	V _{CC} -0.1	_	—		I _{OH} =50 μA
		1.65	1.4	—	—		$I_{OH} = -1 \text{ mA}$
	V _{OH}	2.3	2.0	_	—		I _{OH} = -2 mA
		3.0	2.48	_	—		I _{OH} =6 mA
		4.5	3.8	_	—	V	I _{OH} = -12 mA
Output voltage		Min to Max	—	_	0.1	v	I _{OL} = 50 μA
		1.65	—	_	0.3		$I_{OL} = 1 \text{ mA}$
	V _{OL}	2.3	—	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	—	_	0.44		$I_{OL} = 6 \text{ mA}$
		4.5	—	_	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_		±1	μA	$V_{IN} = 5.5 V \text{ or GND}$
Quiescent supply current	I _{CC}	5.5	—	_	10	μΑ	$V_{IN} = V_{CC}$ or GND, $I_O = 0$
Output leakage current	I _{OFF}	0	—	_	5	μA	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	CIN	3.3		2.5		pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 V$

ltom	Symbol		Ta = 25°C		Ta = -40) to 85°C	l lmit	Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	12.7	23.1	1.0	25.5		C _L = 15 pF	A or D	V
delay time	t _{PHL}	_	18.7	33.4	1.0	37.0	ns	$C_L = 50 \text{ pF}$	A or B	ſ

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

ltom	Symbol		Ta = 25°C		Ta = -40	to 85°C	11:4	Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		7.1	12.9	1.0	15.0	20	$C_L = 15 \text{ pF}$	A or D	V
delay time	t _{PHL}	_	9.6	16.6	1.0	20.0	ns	$C_L = 50 \text{ pF}$	A or B	ř

$\bullet \quad V_{CC}=3.3\pm0.3~V$

ltom	Symbol		Ta = 25°C		Ta = -40) to 85°C	l lmit	Test	FROM	то
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	5.0	7.9	1.0	9.5		$C_L = 15 \text{ pF}$	A or D	V
delay time	t _{PHL}		6.9	11.4	1.0	13.0	ns	$C_L = 50 \text{ pF}$	A or B	ř

• VCC = 5.0 ± 0.5 V

ltom	Symphol		Ta = 25°C		Ta = -40	to 85°C	l lm it	Test	FROM	то
ltem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		3.6	5.5	1.0	6.5	20	$C_L = 15 \text{ pF}$	A	V
delay time	t _{PHL}	—	4.9	7.5	1.0	8.5	ns	$C_L = 50 \text{ pF}$	A or B	ř

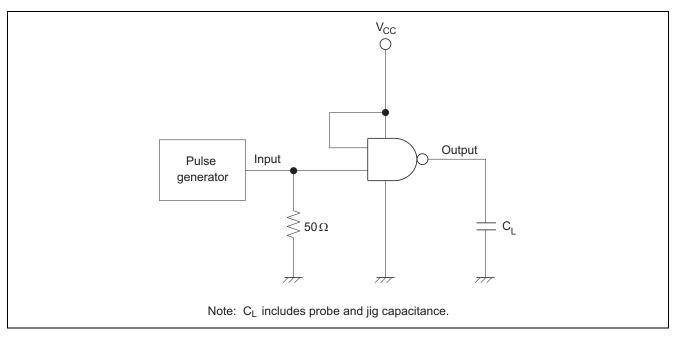
Operating Characteristics

• $C_L = 50 \ pF$

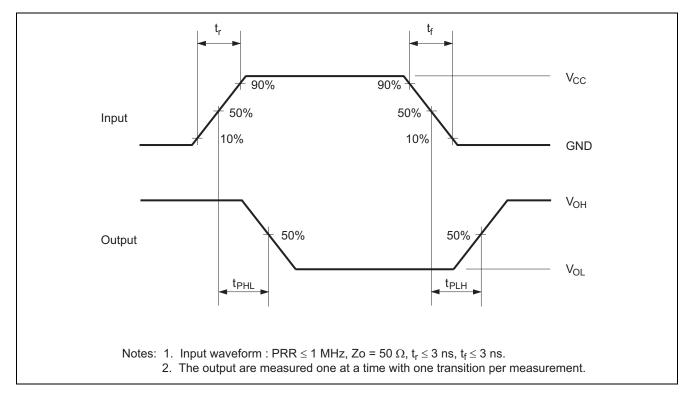
ltom	Symbol	Vcc		Ta = 25°C		11:0:4	Test Conditions	
Item	Symbol	(V)	Min	Тур	Max	Unit		
Power dissipation	<u> </u>	3.3	-	9.5	_	~ F	f 10 MUL	
capacitance	C _{PD}	5.0		11.0		рF	f = 10 MHz	



Test Circuit



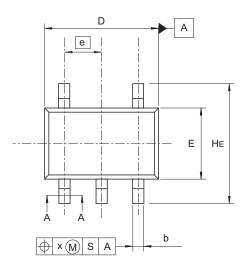
Waveforms

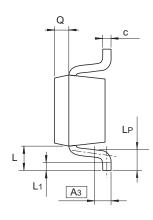


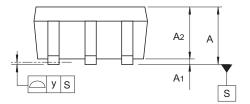


Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-88A	PTSP0005ZC-A	CMPAK-5 / CMPAK-5V	0.006









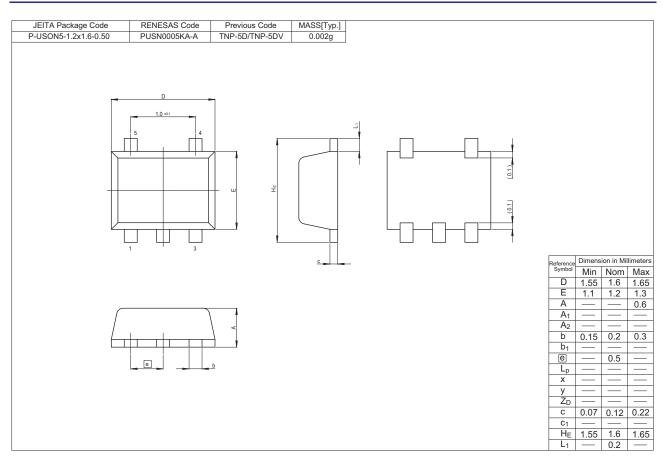
A-A Section

Reference	Dimensi	ons in mi	llimeters
Symbol	Min	Nom	Max
Α	0.8		1.1
A ₁	0		0.1
A ₂	0.8	0.9	1.0
A ₃		0.25	
b	0.15	0.22	0.3
С	0.1	0.13	0.15
D	1.8	2.0	2.2
E	1.15	1.25	1.35
е		0.65	
HE	1.8	2.1	2.4
L	0.3		0.7
L ₁	0.1		0.5
LP	0.2		0.6
Х			0.05
У			0.05
Q		0.25	

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HD74LV1G00A





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