

HAT2215R

Silicon N Channel Power MOSFET
High Speed Power Switching

REJ03G0486-0301

Rev.3.01

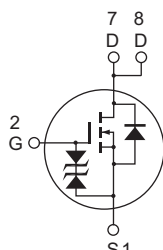
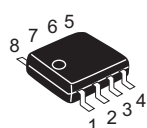
Jan 20, 2017

Features

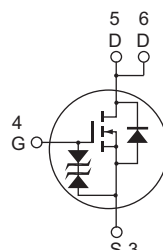
- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting

Outline

RENESAS Package code: PRSP0008DD-D
(Package name: SOP-8<FP-8DAV>)



MOS1



MOS2

1, 3 Source
2, 4 Gate
5, 6, 7, 8 Drain

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DS}	80	V
Gate to source voltage	V_{GS}	± 20	V
Drain current	I_D	3.4	A
Drain peak current	$I_{D(pulse)}$ ^{Note1}	20.4	A
Reverse drain current	I_{DR}	3.4	A
Channel dissipation	P_{ch} ^{Note2}	1.5	W
Channel dissipation	P_{ch} ^{Note3}	2.2	W
Channel temperature	T_{ch}	150	°C
Storage temperature	T_{stg}	-55 to +150	°C

Notes: 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$

2. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), $PW \leq 10 s$

3. 2 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), $PW \leq 10 s$

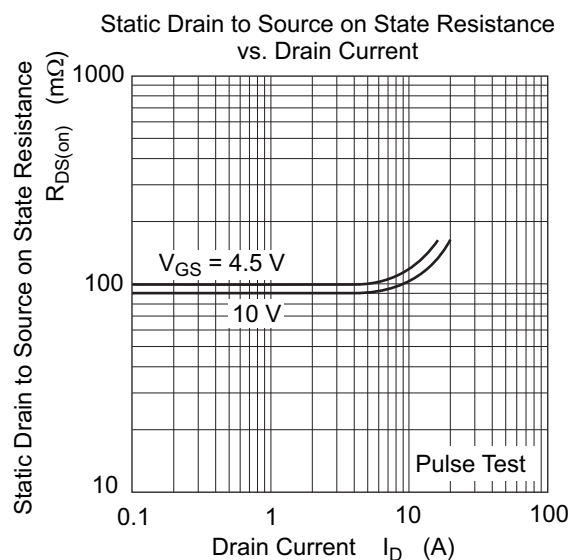
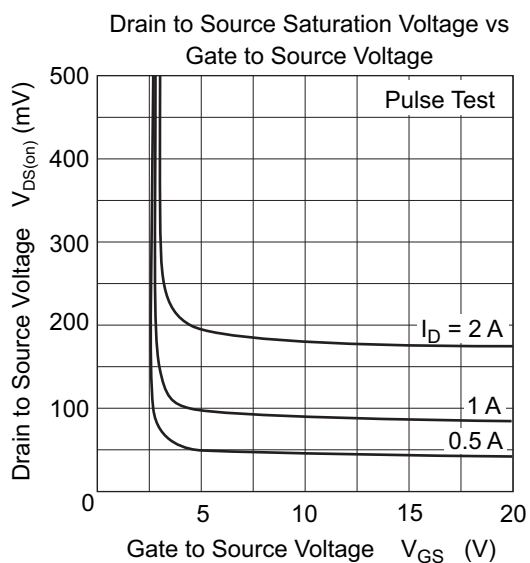
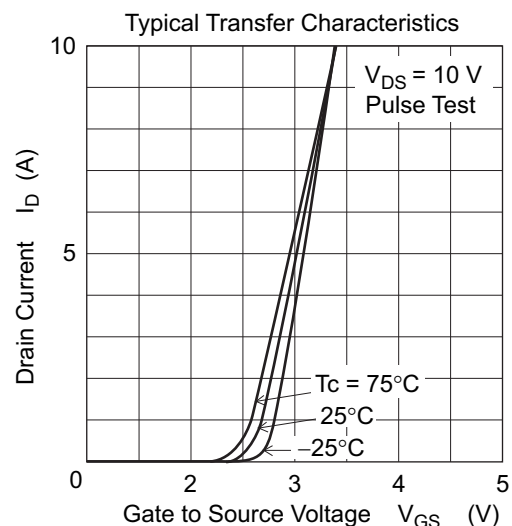
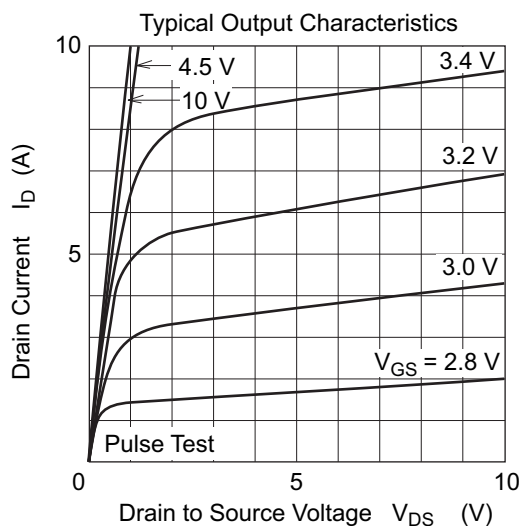
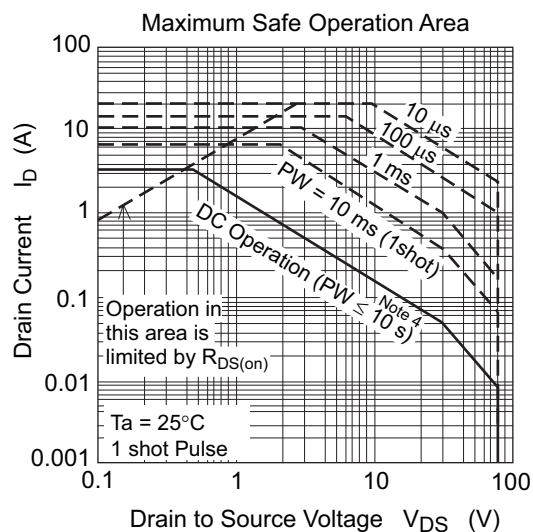
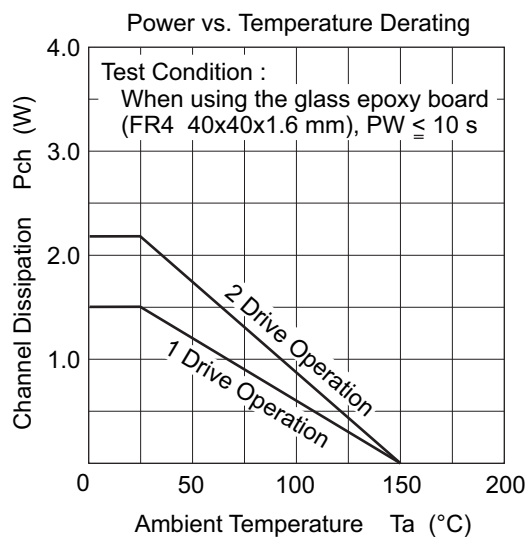
Electrical Characteristics

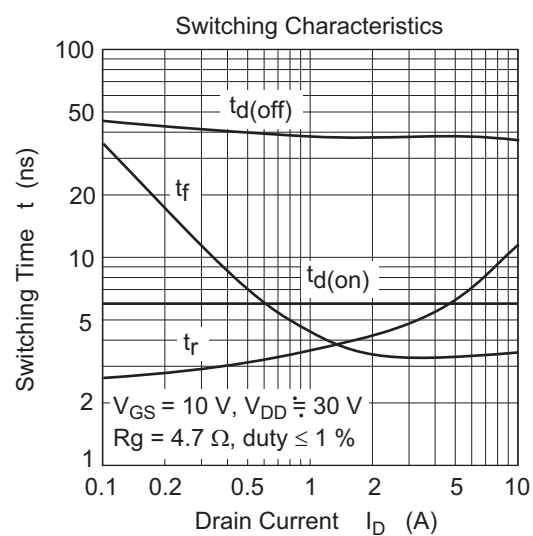
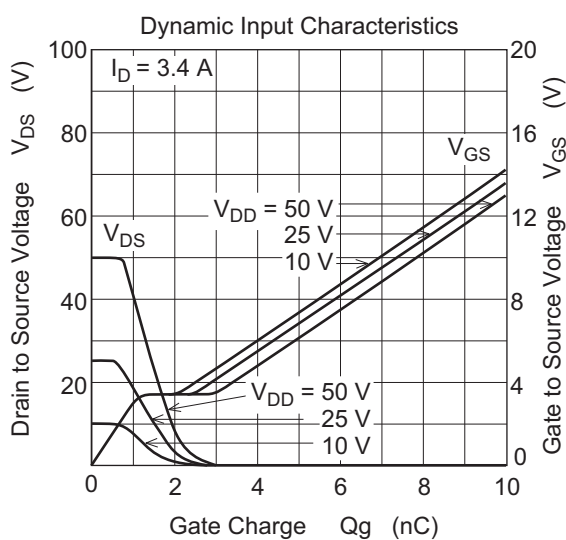
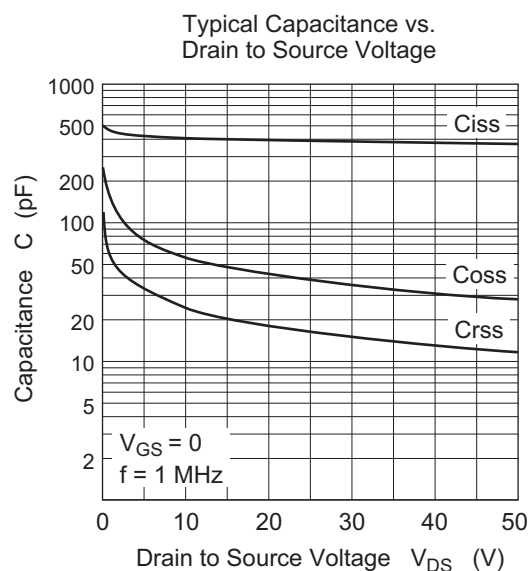
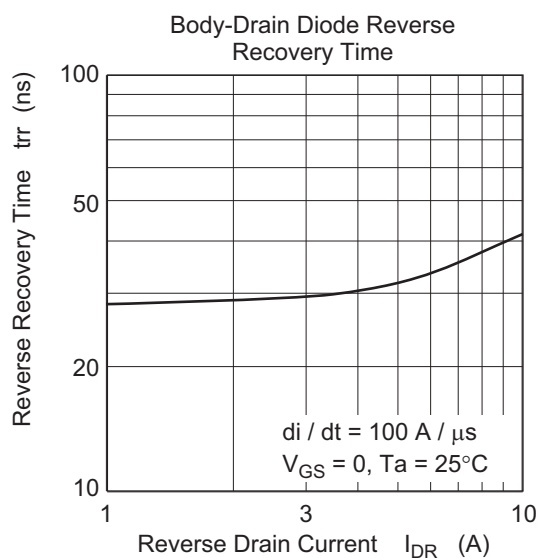
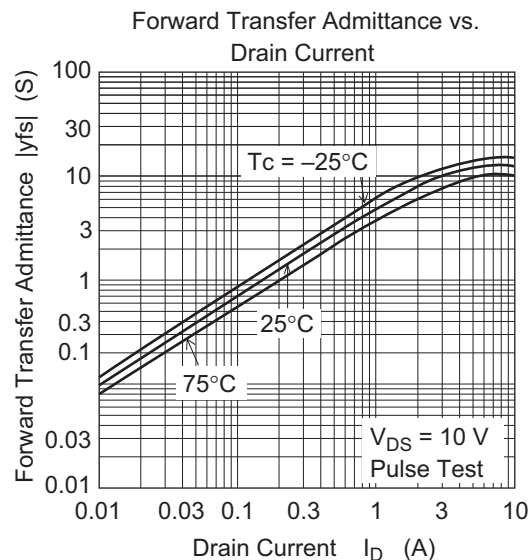
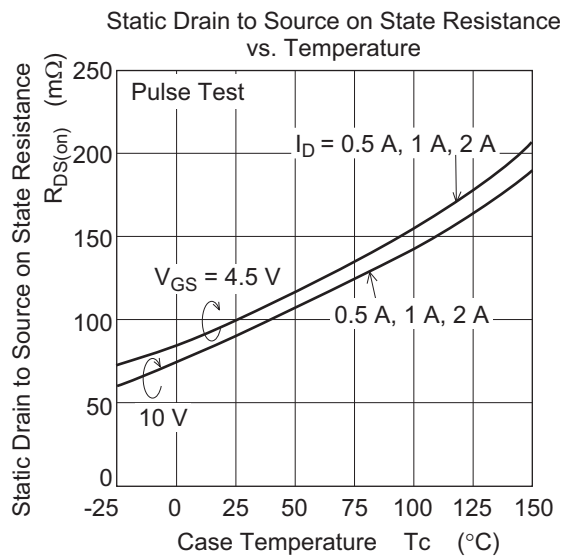
(Ta = 25°C)

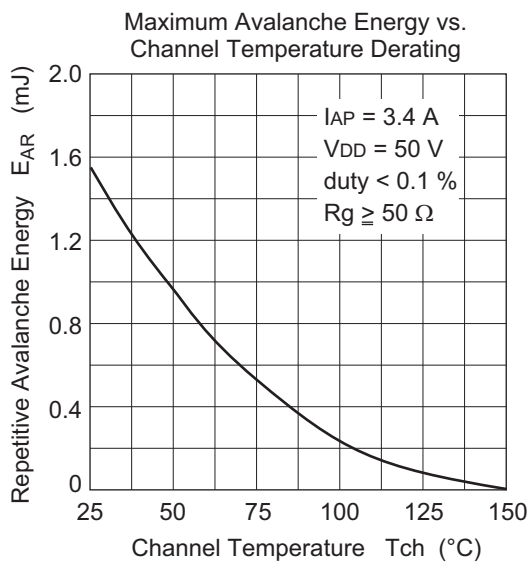
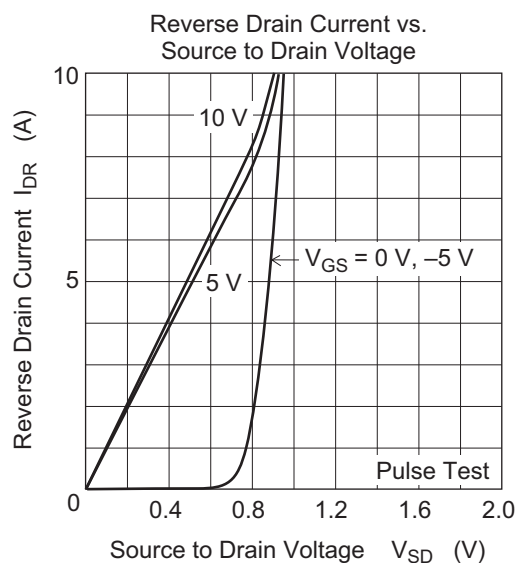
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	80	—	—	V	$I_D = 10 \text{ mA}$, $V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_D = \pm 100 \text{ }\mu\text{A}$, $V_{GS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}$, $V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	1	μA	$V_{DS} = 80 \text{ V}$, $V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.5	V	$V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$
Static drain to source on state resistance	$R_{DS(on)}$	—	88	115	$\text{m}\Omega$	$I_D = 1.7 \text{ A}$, $V_{GS} = 10 \text{ V}$ ^{Note4}
	$R_{DS(on)}$	—	100	145	$\text{m}\Omega$	$I_D = 1.7 \text{ A}$, $V_{GS} = 4.5 \text{ V}$ ^{Note4}
Forward transfer admittance	$ y_{fs} $	4.2	7.0	—	S	$I_D = 1.7 \text{ A}$, $V_{DS} = 10 \text{ V}$ ^{Note4}
Input capacitance	C_{iss}	—	400	—	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	C_{oss}	—	57	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	C_{rss}	—	24	—	pF	$f = 1 \text{ MHz}$
Total gate charge	Q_g	—	7.3	—	nC	$V_{DD} = 25 \text{ V}$
Gate to source charge	Q_{gs}	—	1.1	—	nC	$V_{GS} = 10 \text{ V}$
Gate to drain charge	Q_{gd}	—	1.3	—	nC	$I_D = 3.4 \text{ A}$
Turn-on delay time	$t_{d(on)}$	—	6.0	—	ns	$V_{GS} = 10 \text{ V}$, $I_D = 1.7 \text{ A}$
Rise time	t_r	—	4.0	—	ns	$V_{DD} \approx 30 \text{ V}$
Turn-off delay time	$t_{d(off)}$	—	39	—	ns	$R_L = 17.6 \text{ }\Omega$
Fall time	t_f	—	3.5	—	ns	$R_g = 4.7 \text{ }\Omega$
Body-drain diode forward voltage	V_{DF}	—	0.83	1.08	V	$I_F = 3.4 \text{ A}$, $V_{GS} = 0$ ^{Note4}
Body-drain diode reverse recovery time	t_{rr}	—	30	—	ns	$I_F = 3.4 \text{ A}$, $V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

Notes: 4. Pulse test

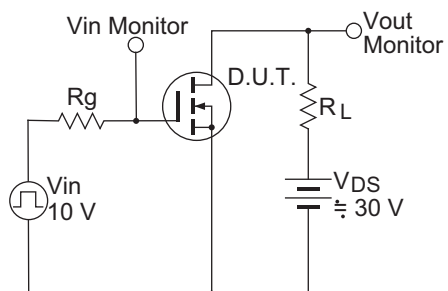
Main Characteristics



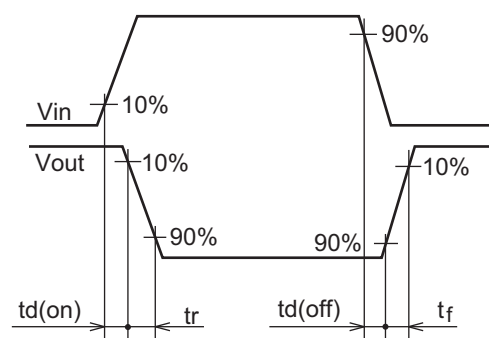




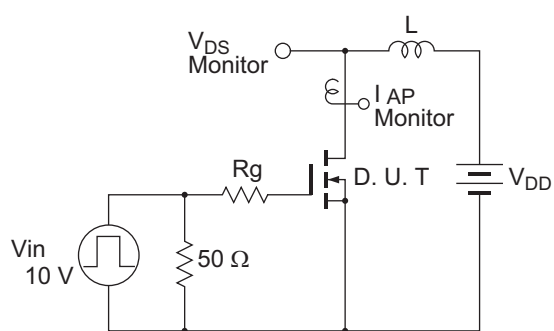
Switching Time Test Circuit



Switching Time Waveform

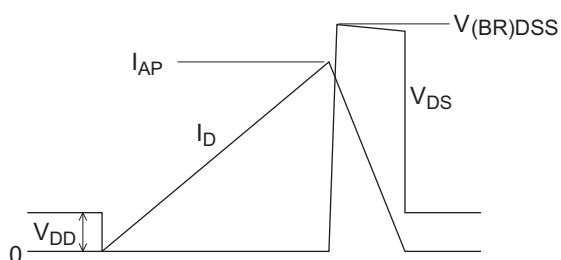


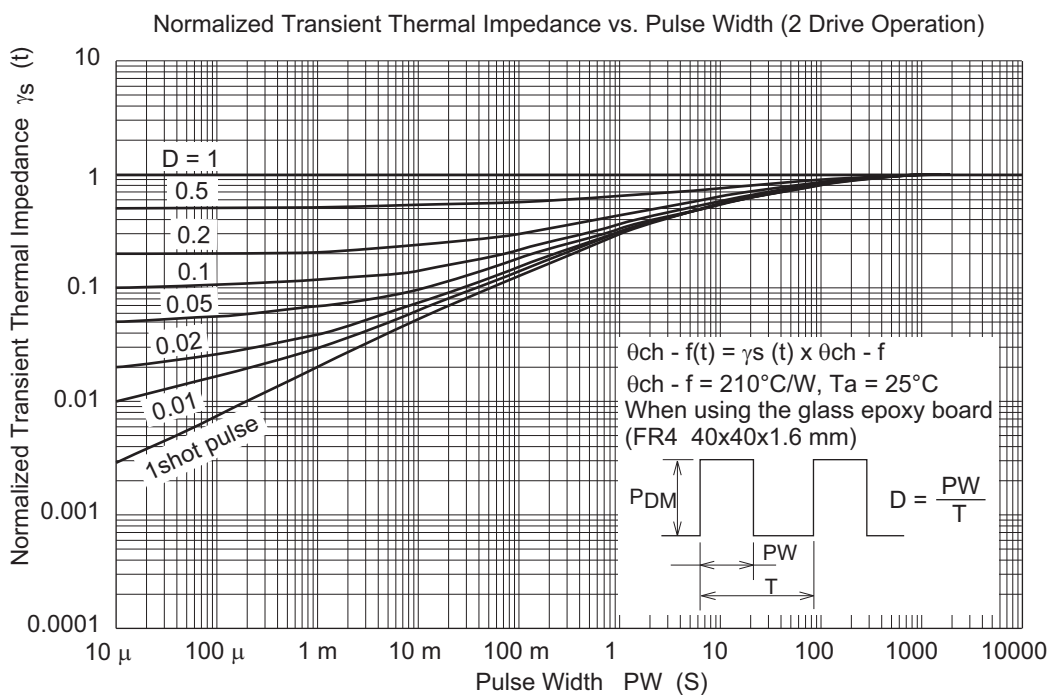
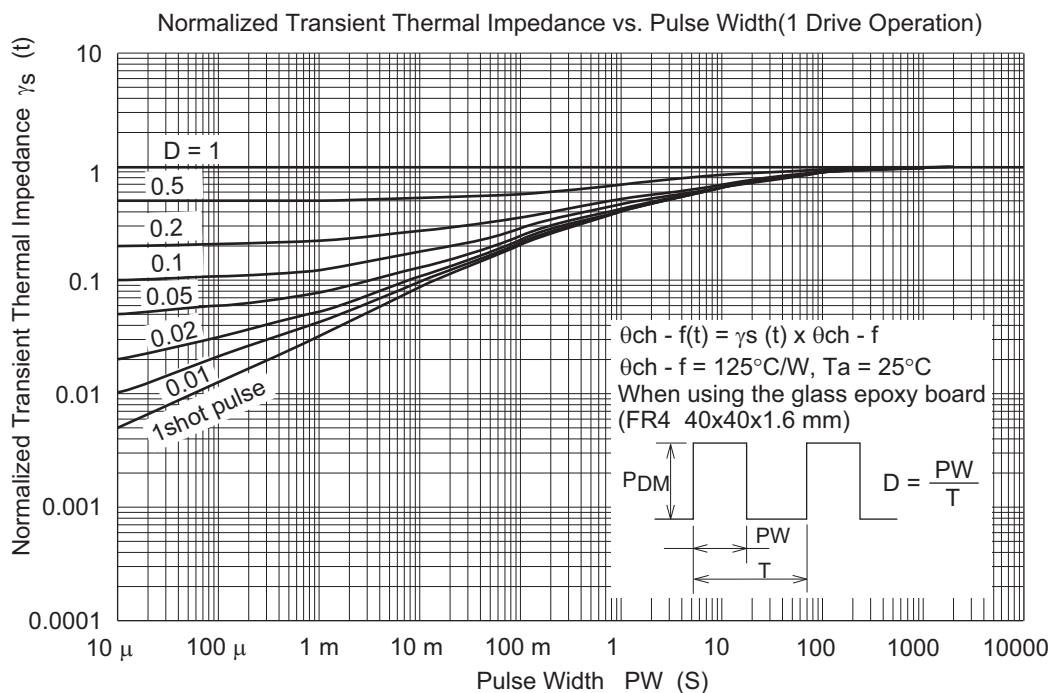
Avalanche Test Circuit



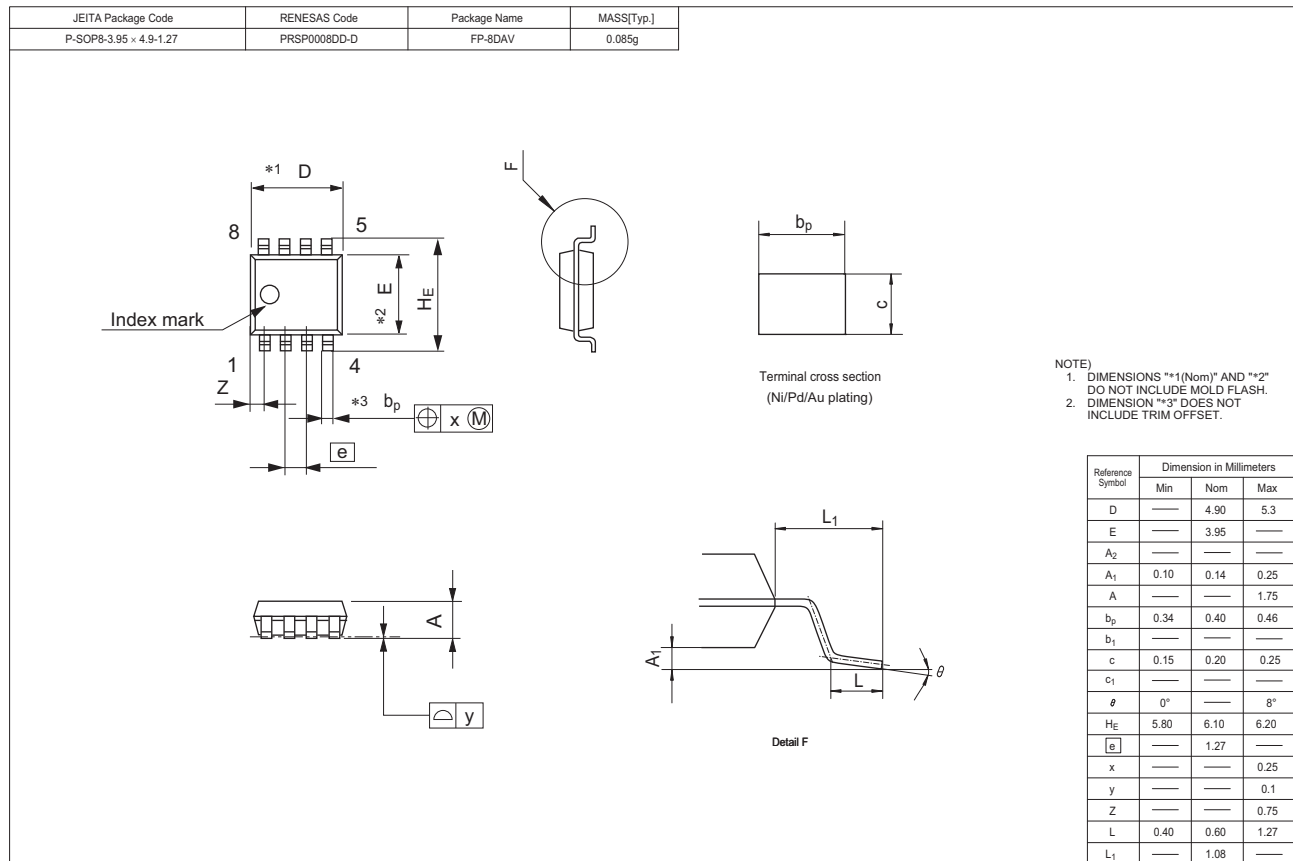
Avalanche Waveform

$$E_{AR} = \frac{1}{2} L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$





Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
HAT2215R-EL-E	2500 pcs	Taping

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