

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

Not recommended  
for new design

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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

To all our customers

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## **Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.**

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The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.


Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

Renesas Technology Corp.  
Customer Support Dept.  
April 1, 2003

# FS50VSJ-3

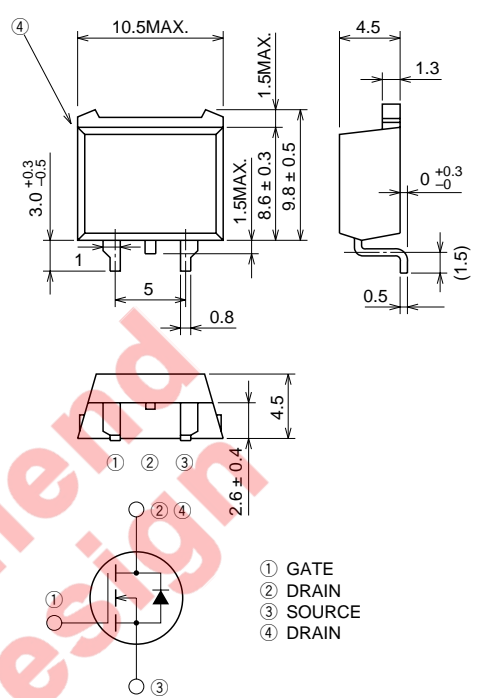
HIGH-SPEED SWITCHING USE

**FS50VSJ-3**



- 4V DRIVE
- $V_{DSS}$  .....150V
- $r_{DS(ON)}$  (MAX) .....30m $\Omega$
- $I_D$  .....50A
- Integrated Fast Recovery Diode (TYP.) .....125ns

**OUTLINE DRAWING** Dimensions in mm



① GATE  
② DRAIN  
③ SOURCE  
④ DRAIN

**TO-220S**

**APPLICATION**

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

**MAXIMUM RATINGS** ( $T_c = 25^\circ\text{C}$ )

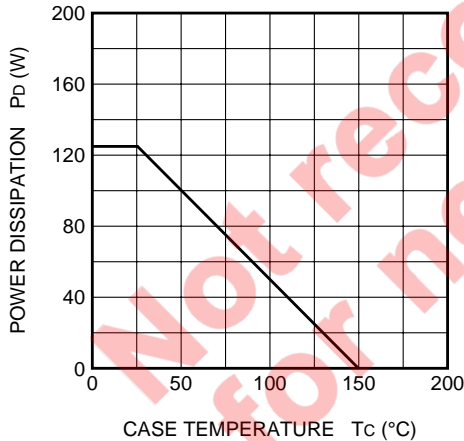
Symbol	Parameter	Conditions	Ratings	Unit
$V_{DSS}$	Drain-source voltage	$V_{GS} = 0V$	150	V
$V_{GSS}$	Gate-source voltage	$V_{DS} = 0V$	$\pm 20$	V
$I_D$	Drain current		50	A
$I_{DM}$	Drain current (Pulsed)		200	A
$I_{DA}$	Avalanche drain current (Pulsed)	$L = 100\mu\text{H}$	50	A
$I_S$	Source current		50	A
$I_{SM}$	Source current (Pulsed)		200	A
$P_D$	Maximum power dissipation		125	W
$T_{ch}$	Channel temperature		$-55 \sim +150$	$^\circ\text{C}$
$T_{stg}$	Storage temperature		$-55 \sim +150$	$^\circ\text{C}$
—	Weight	Typical value	1.2	g

**ELECTRICAL CHARACTERISTICS** (T<sub>ch</sub> = 25°C)

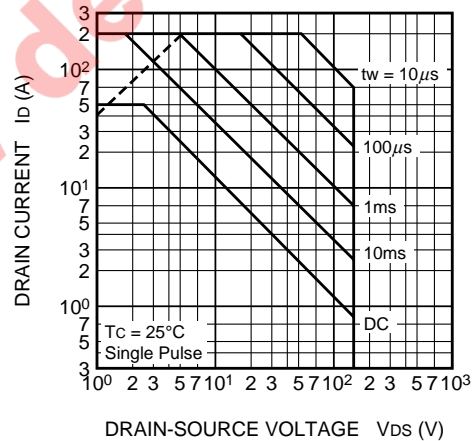
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V (BR) DSS	Drain-source breakdown voltage	I <sub>D</sub> = 1mA, V <sub>GS</sub> = 0V	150	—	—	V
I <sub>GSS</sub>	Gate-source leakage current	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	—	—	±0.1	μA
I <sub>DSS</sub>	Drain-source leakage current	V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0V	—	—	0.1	mA
V <sub>GS</sub> (th)	Gate-source threshold voltage	I <sub>D</sub> = 1mA, V <sub>DS</sub> = 10V	1.0	1.5	2.0	V
r <sub>DS</sub> (ON)	Drain-source on-state resistance	I <sub>D</sub> = 25A, V <sub>GS</sub> = 10V	—	23	30	mΩ
r <sub>DS</sub> (ON)	Drain-source on-state resistance	I <sub>D</sub> = 25A, V <sub>GS</sub> = 4V	—	24	32	mΩ
V <sub>DS</sub> (ON)	Drain-source on-state voltage	I <sub>D</sub> = 25A, V <sub>GS</sub> = 10V	—	0.58	0.75	V
y <sub>fs</sub>	Forward transfer admittance	I <sub>D</sub> = 25A, V <sub>DS</sub> = 10V	—	62	—	S
C <sub>iss</sub>	Input capacitance	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1MHz	—	8200	—	pF
C <sub>oss</sub>	Output capacitance		—	870	—	pF
C <sub>rss</sub>	Reverse transfer capacitance		—	440	—	pF
t <sub>d</sub> (on)	Turn-on delay time	V <sub>DD</sub> = 80V, I <sub>D</sub> = 25A, V <sub>GS</sub> = 10V, R <sub>GEN</sub> = R <sub>GS</sub> = 50Ω	—	54	—	ns
t <sub>r</sub>	Rise time		—	110	—	ns
t <sub>d</sub> (off)	Turn-off delay time		—	850	—	ns
t <sub>f</sub>	Fall time		—	340	—	ns
V <sub>SD</sub>	Source-drain voltage	I <sub>S</sub> = 25A, V <sub>GS</sub> = 0V	—	1.0	1.5	V
R <sub>th</sub> (ch-c)	Thermal resistance	Channel to case	—	—	1.00	°C/W
t <sub>rr</sub>	Reverse recovery time	I <sub>S</sub> = 50A, di <sub>s</sub> /dt = -100A/μs	—	125	—	ns

**PERFORMANCE CURVES**

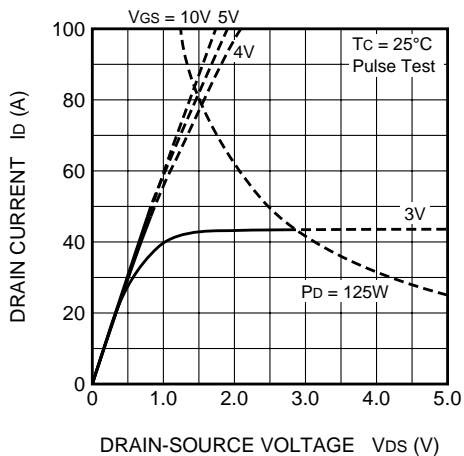
**POWER DISSIPATION DERATING CURVE**



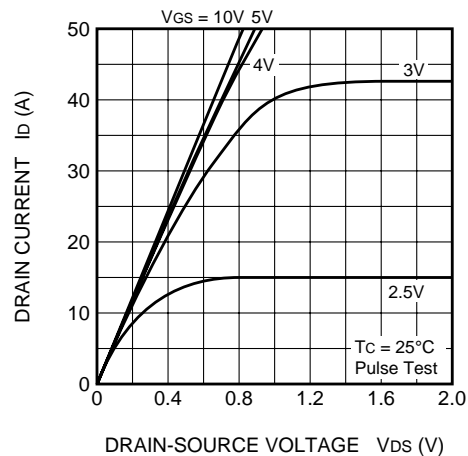
**MAXIMUM SAFE OPERATING AREA**

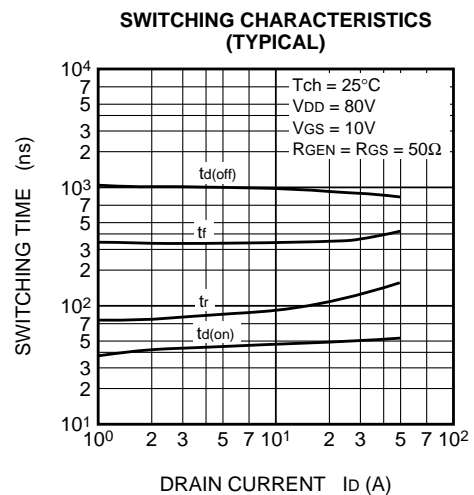
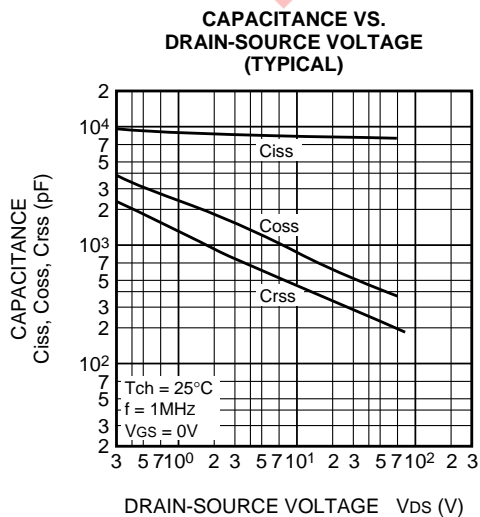
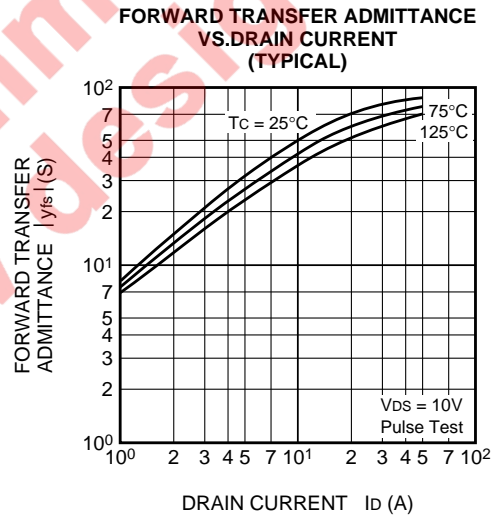
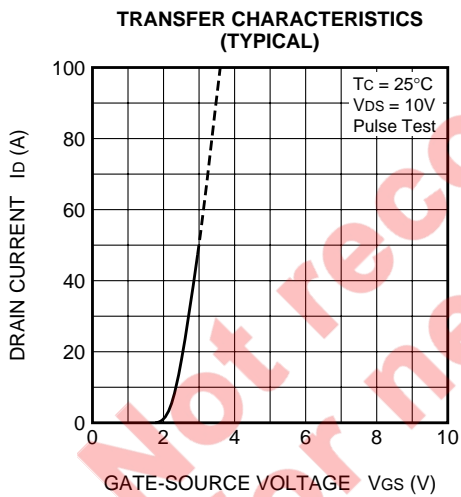
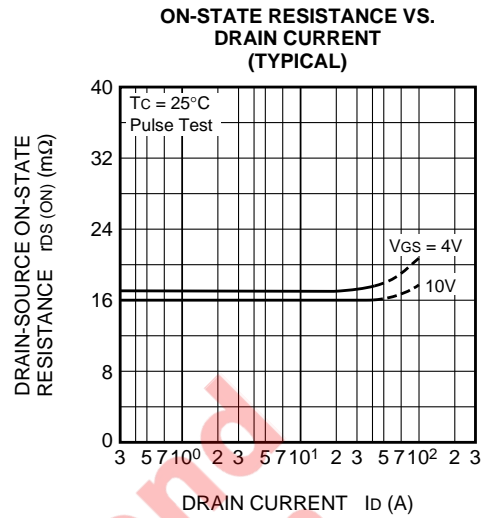
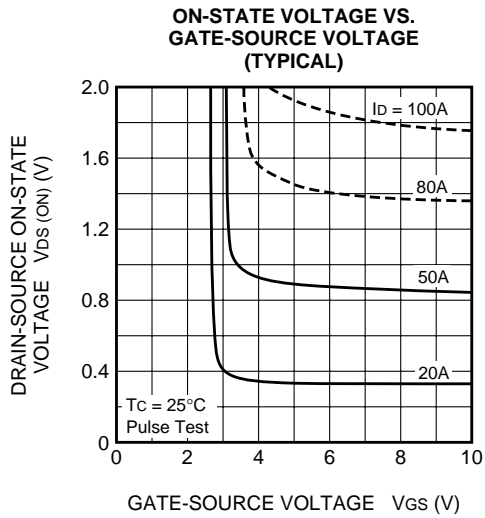


**OUTPUT CHARACTERISTICS (TYPICAL)**

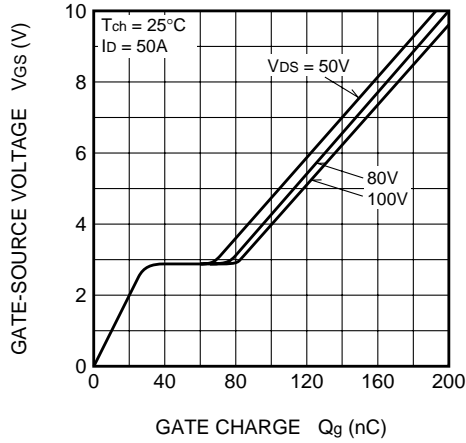


**OUTPUT CHARACTERISTICS (TYPICAL)**

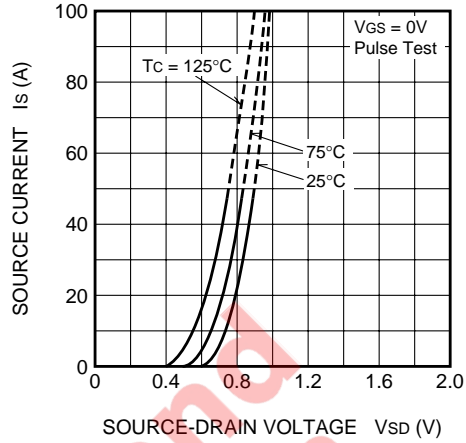




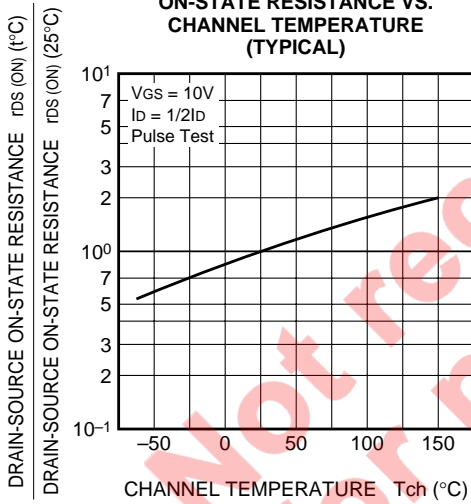
GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)



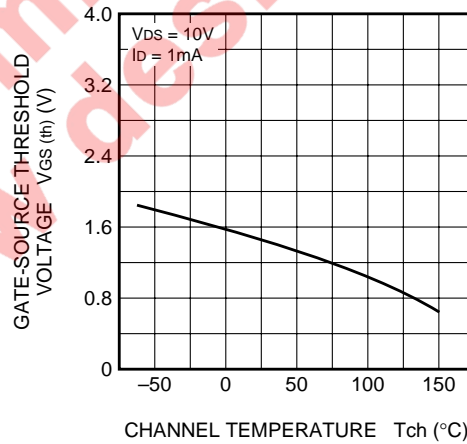
SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)



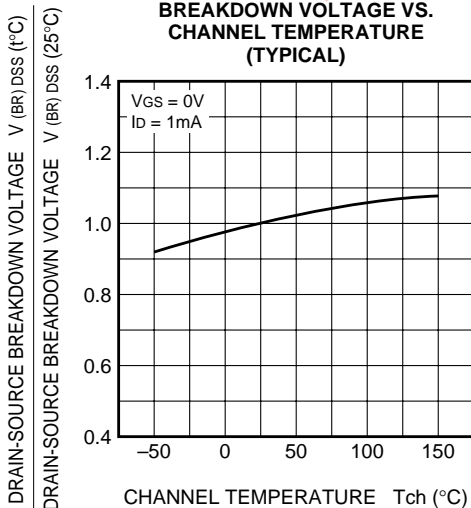
ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)



THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

