

To our customers,

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## Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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EOL product

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## 2SK3214

Silicon N Channel MOS FET  
High Speed Power Switching

REJ03G1093-0400

Rev.4.00

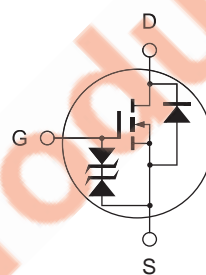
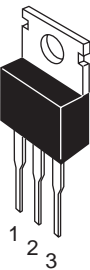
May 15, 2006

### Features

- Low on-resistance  
 $R_{DS} = 130 \text{ m}\Omega$  typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

### Outline

RENESAS Package code: PRSS0004AC-A  
(Package name: TO-220AB)



1. Gate
2. Drain (Flange)
3. Source

## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	200	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	10	A
Drain peak current	I <sub>D (pulse)</sub> <sup>Note 1</sup>	40	A
Body-drain diode reverse drain current	I <sub>DR</sub>	10	A
Avalanche current	I <sub>AP</sub> <sup>Note 3</sup>	10	A
Avalanche energy	E <sub>AR</sub> <sup>Note 3</sup>	6.6	mJ
Channel dissipation	P <sub>ch</sub> <sup>Note 2</sup>	50	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1%  
 2. Value at T<sub>c</sub> = 25°C  
 3. Value at T<sub>ch</sub> ≤ 25°C, R<sub>g</sub> ≥ 50 Ω

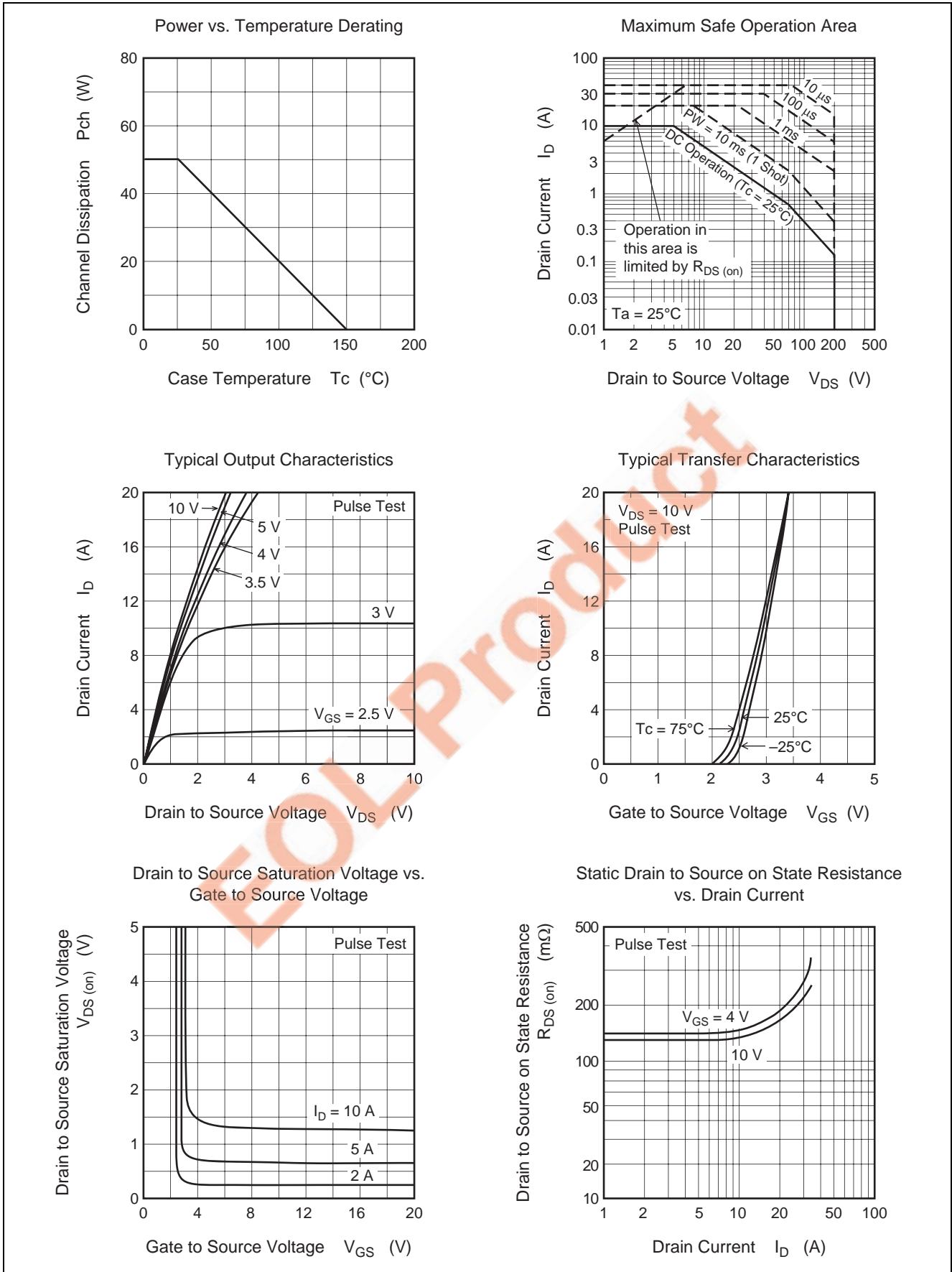
## Electrical Characteristics

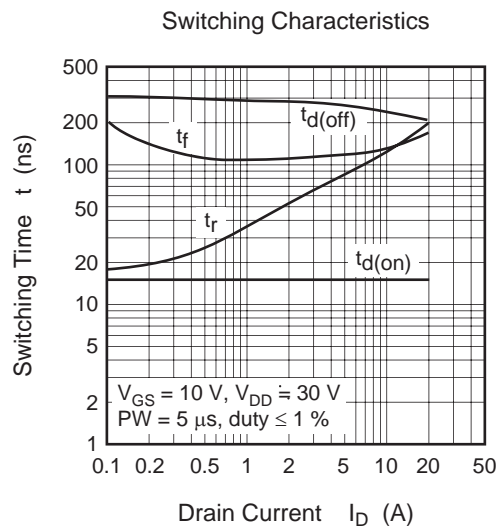
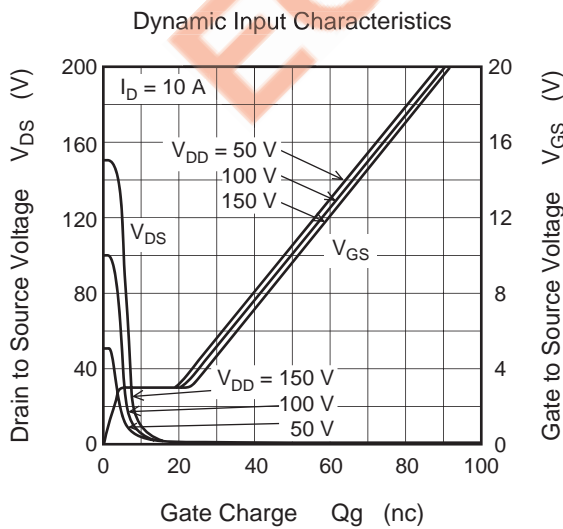
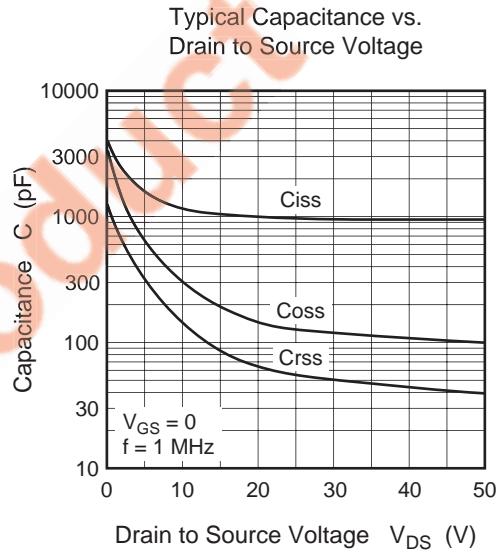
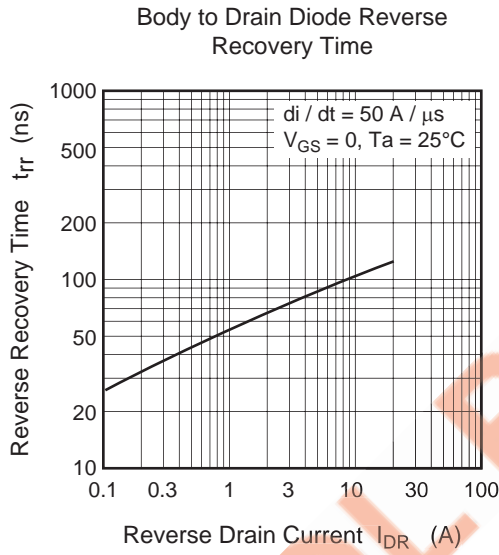
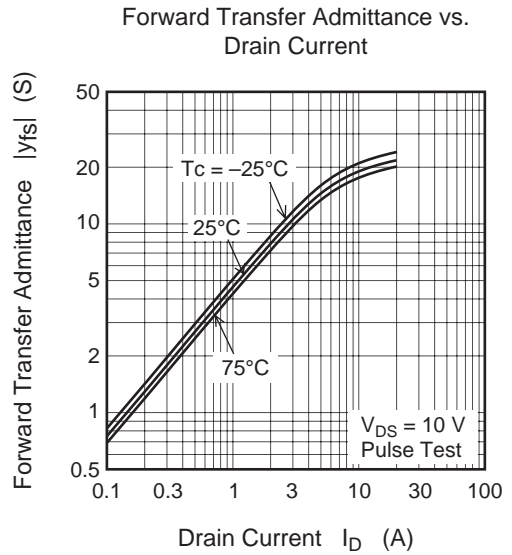
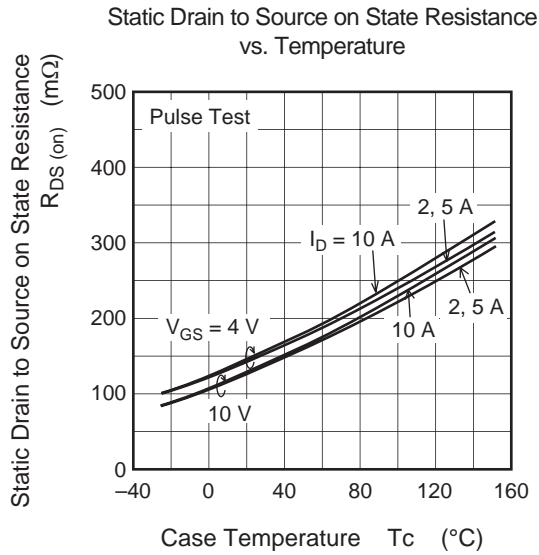
(Ta = 25°C)

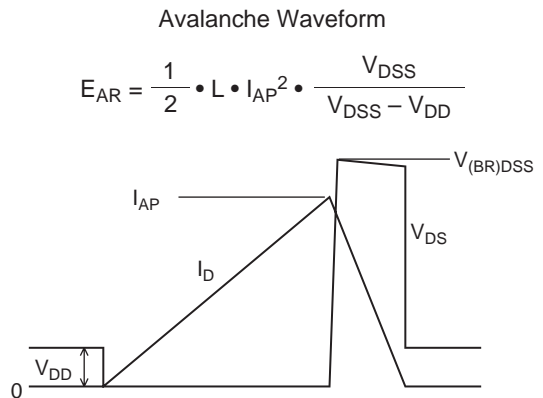
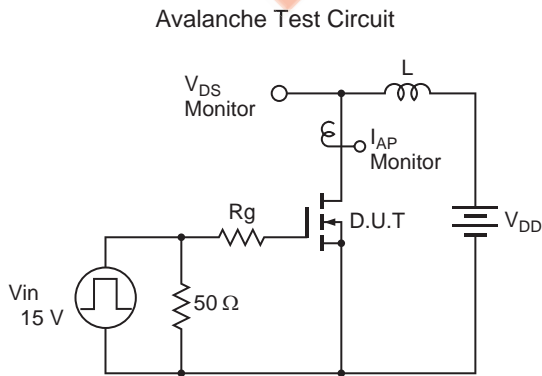
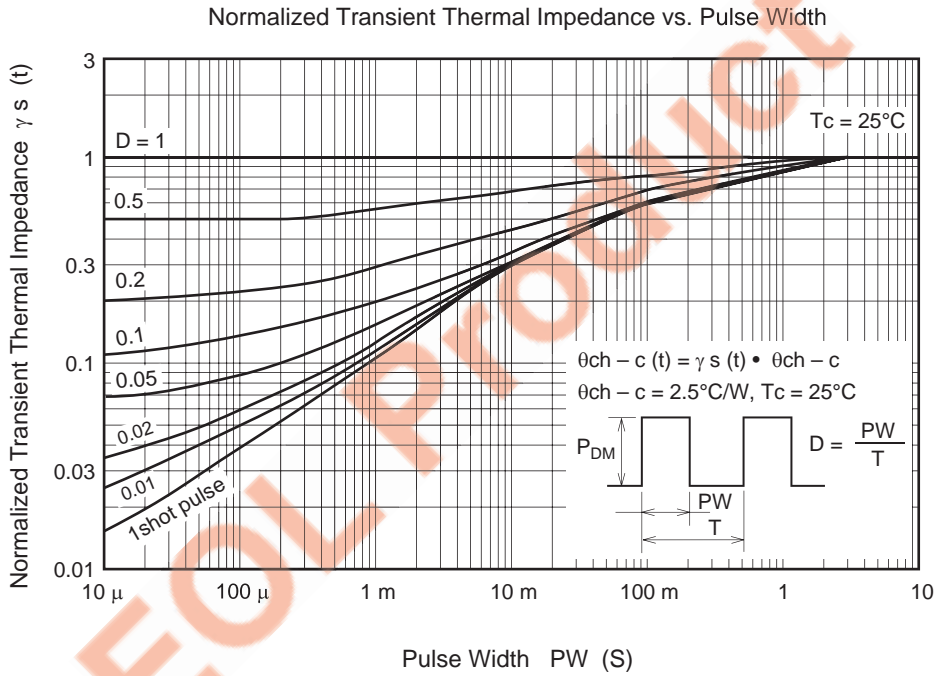
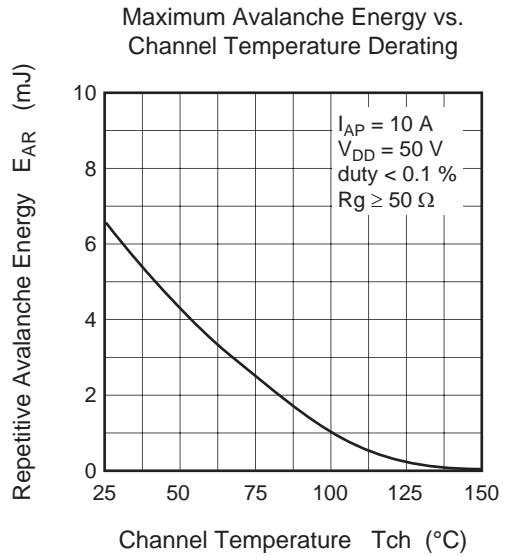
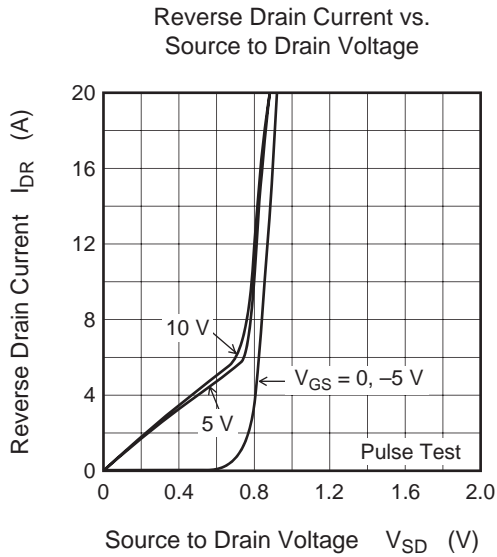
Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	200	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR) GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 200 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	—	2.5	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	130	170	mΩ	I <sub>D</sub> = 5 A, V <sub>GS</sub> = 10 V <sup>Note 4</sup>
	R <sub>DS (on)</sub>	—	150	190	mΩ	I <sub>D</sub> = 5 A, V <sub>GS</sub> = 4 V <sup>Note 4</sup>
Forward transfer admittance	y <sub>fs</sub>	8	13	—	S	I <sub>D</sub> = 5 A, V <sub>DS</sub> = 10 V <sup>Note 4</sup>
Input capacitance	C <sub>iss</sub>	—	1100	—	pF	I <sub>D</sub> = 10 V
Output capacitance	C <sub>oss</sub>	—	300	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	C <sub>rss</sub>	—	150	—	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	—	15	—	ns	I <sub>D</sub> = 5 A
Rise time	t <sub>r</sub>	—	75	—	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	t <sub>d (off)</sub>	—	280	—	ns	R <sub>L</sub> = 6 Ω
Fall time	t <sub>f</sub>	—	110	—	ns	
Body-drain diode forward voltage	V <sub>DF</sub>	—	0.85	—	V	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0
Body-drain diode reverse recovery time	t <sub>rr</sub>	—	100	—	ns	I <sub>F</sub> = 10 A, V <sub>GS</sub> = 0 di <sub>F</sub> /dt = 50 A/μs

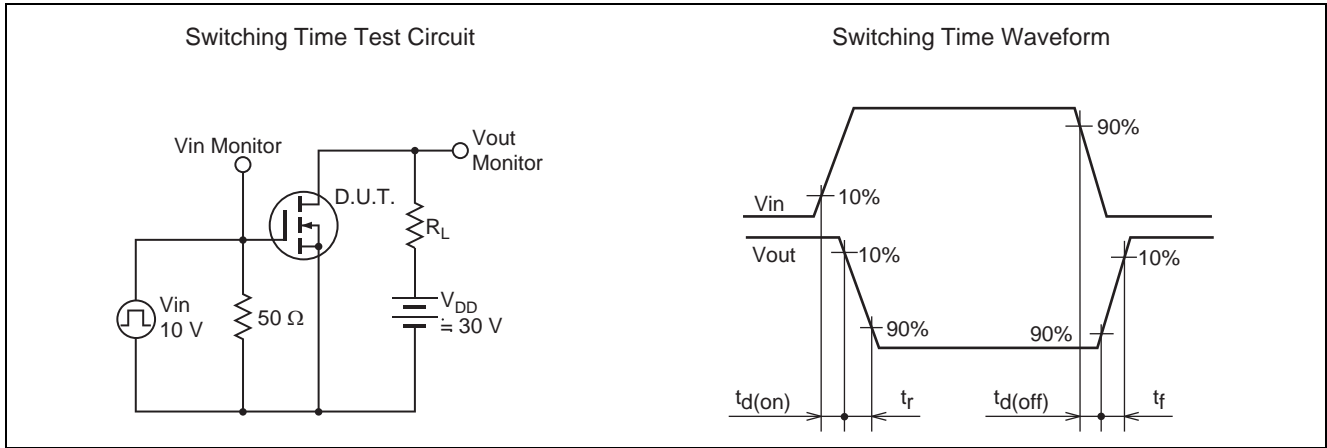
Note: 4. Pulse test

Main Characteristics





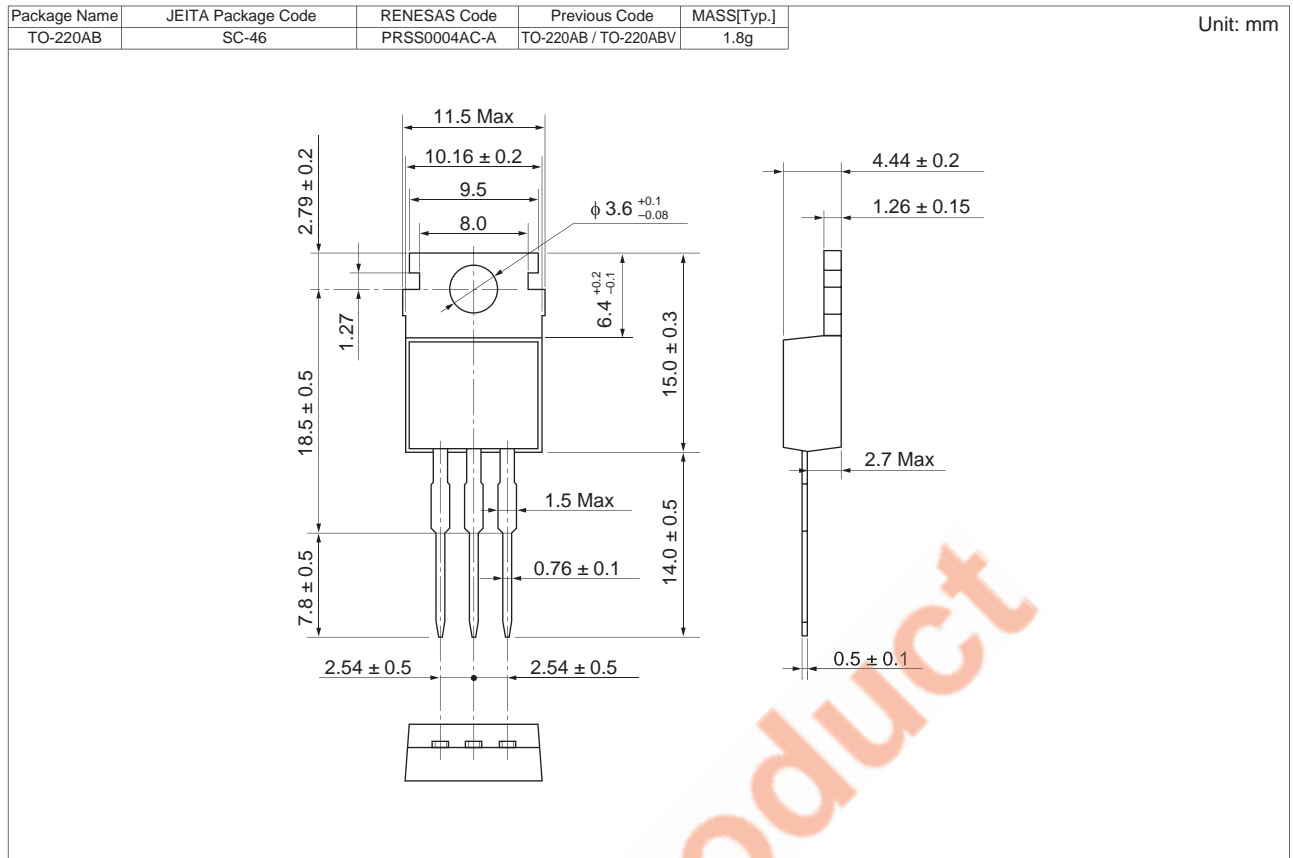




EOL Product



## Package Dimensions



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Part Name	Quantity	Shipping Container
2SK3214-E	500 pcs	Box (Sack)

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