Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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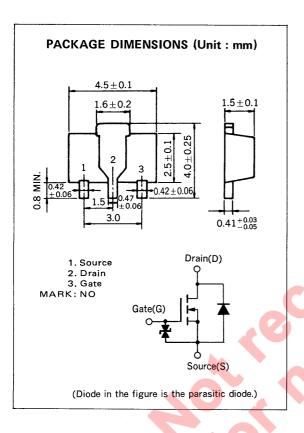
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MOS FIELD EFFECT TRANSISTOR **2SK1592**

N-CHANNEL MOS FET FOR SWITCHING



The 2SK1592, N-channel vertical type MOS FET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

FEATURES

- Directly driven by ICs having a 5 V power source.
- Has low on-state resistance.

 $R_{DS(on)1} = 2.5 \Omega MAX$. @ $V_{GS} = 4.0 V$, $I_D = 0.3 A$ $R_{DS(on)2} = 2.0 \Omega MAX$. @ $V_{GS} = 10 V$, $I_D = 0.3 A$

QUALITY GRADE

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

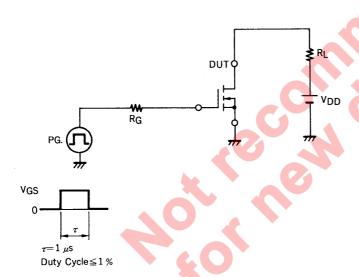
PARAMETER	SYMBOL	RATINGS	UNIT	TEST CONDITIONS
Drain to Source Voltage	V _{DSS}	60	V	V _{GS} = 0
Gate to Source Voltage	V _{GSS}	±20	V	V _{DS} = 0
Drain Current	I _{D(DC)}	±500	mA	
Drain Current	I _D (pulse)	±1.0	А	PW ≤ 10 ms, Duty Cycle ≤ 50 %
Total Power Dissipation	PT	2.0	w	When using ceramic board of 16 cm ² x 0.7 mm
Channel Temperature	T _{ch}	150	°C	·
Storage Temperature	T _{stg}	-55 to +150	°C	

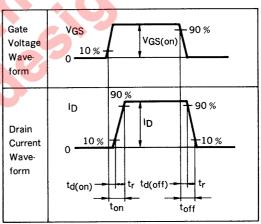


ELECTRICAL CHARACTERISTICS (T_a = 25 $^{\circ}$ C)

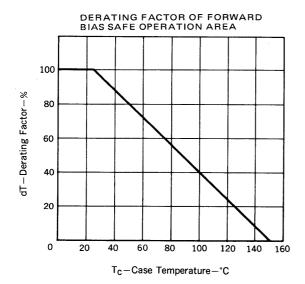
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	IDSS			10	μА	V _{DS} = 60 V, V _{GS} = 0
Gate Leakage Current	IGSS			±10	μА	V _{GS} = ±20 V, V _{DS} = 0
Gate Cut-off Voltage	VGS(off)	0.8	1.2	2.0	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	lyfsl	400	570		mS	V _{DS} = 10 V, I _D = 0.5 A
Drain to Source On-State Resistance	R _{DS(on)1}		1.6	2.5	Ω	V _{GS} = 4.0 V, I _D = 0.3 A
Drain to Source On-State Resistance	R _{DS(on)2}		1.2	2.0	Ω	V _{GS} = 10 V, I _D = 0.3 A
Input Capacitance	C _{iss}		52		pF	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz
Output Capacitance	Coss		34		pF	
Feedback Capacitance	C _{rss}		7		pF	
Turn-On Delay Time	td(on)		60		ns	V_{DD} = 10 V, I_{D} = 0.3 A $V_{GS(on)}$ = 4 V, R_{G} = 10 Ω R_{L} = 33 Ω
Rise Time	t _r		150		ns	
Turn-Off Delay Time	^t d(off)		150		ns	
Fall Time	tf		100		ns	

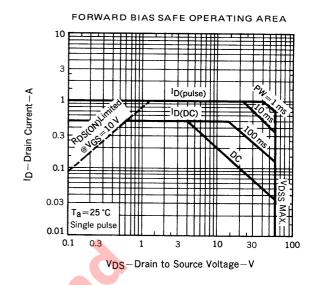
SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS

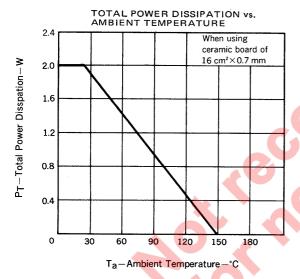


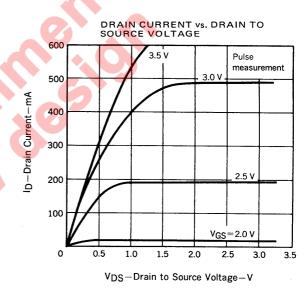


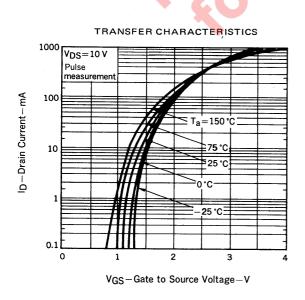
TYPICAL CHARACTERISTICS (Ta = 25 °C)

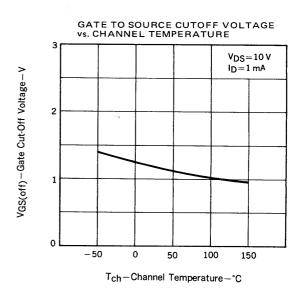


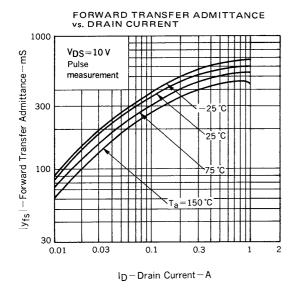


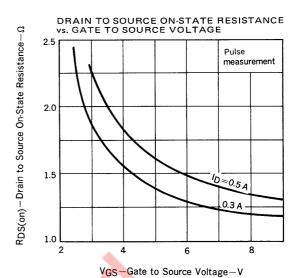


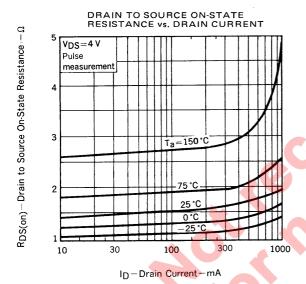


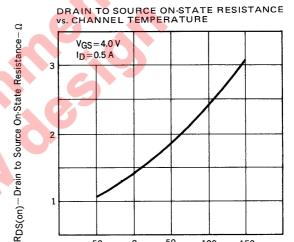






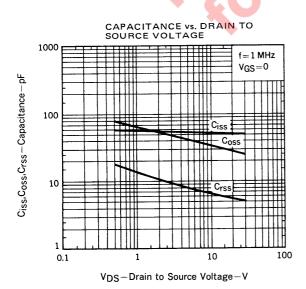


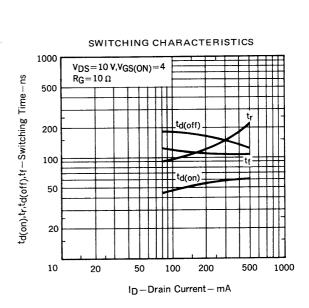




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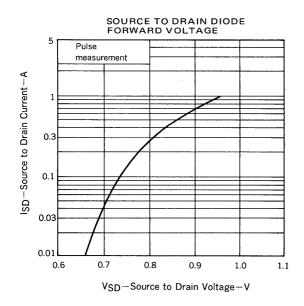
-50





T_{ch}-Channel Temperature-°C

4



RECOMMENDED SOLDERING CONDITIONS

Mounting of this product by soldering should be done under the following conditions.

Please consult our representatives about soldering methods and conditions other than these.

SURFACE MOUNT TYPE

For details of the recommended soldering conditions, see the information document "SMT MANUAL" (IEI-1207).

Soldering Method	Soldering Conditions	Symbol for Recommended Conditions
Infrared Reflow	Package peak temp.: 230 °C Soldering time: within 30 sec (above 210 °C) Soldering times: 1, Days limitation: none*	IR30-00
Vapor Phase Soldering	Package peak temp.: 215 °C Soldering time: within 40 sec (above 200 °C) Soldering times: 1, Days limitation: none*	VP15-00
Wave Soldering	Soldering bath temp.: below 260 °C Soldering time: within 10 sec Soldering times: 1, Days limitation: none*	WS60-00

^{*:} Stored days under storage conditions at 25 °C and below 65 % R.H. after the dry-pack has been opened.

Note 1 Combination of soldering methods should be avoided.

REFERENCE

Document Name	Document No.	
NEC semiconductor device reliability/quality control system.	TEI-1202	
Quality grade on NEC semiconductor devices.	IEI-1209	
Semiconductor device mounting technology manual.	IEI-1207	
Semiconductor device package manual.	IEI-1213	
Guide to quality assurance for semiconductor devices.	MEI-1202	
Semiconductor selection guide.	MF-1134	



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Application examples recommended by NEC Corporation

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tools, Industrial robots, Audio and Visual equipment, Other consumer products, etc.

Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.