

Tsi381 Ballmap -- Bottom View (Document 80E2000_PN001_04)

	12	11	10	9	8	7	6	5	4	3	2	1
A	VSS_IO	GPIO[3]	GPIO[1]	N/C	PCIE_RXD_p	VDD_PCIE	PCIE_REFCLK_p	VDD_PCIE	PCIE_TXD_n	TEST_ON	SR_DIN	SR_DOUT
B	PCIE_PERST_n	VDD_PCI	GPIO[2]	N/C	VSS	PCIE_RXD_n	PCIE_REFCLK_n	PCIE_TXD_p	VSS	VDD_PCI	SR_CLK	TEST_BIDR_CTL
C	NC	VSS_IO	VDD	GPIO[0]	TEST_BCE	VSSA_PLL	VSS	VSS	SR_CS_n	VSS_IO	PCI_CLKO[1]	PCI_AD[1]
D	PCI_CLK	PCI_INTC_n	PCI_M66EN	VSS	TEST_SPARE[1]	VSS	VSS	VDD_PCIE	VDD	PCI_AD[0]	PCI_AD[3]	PCI_AD[2]
E	PCI_INTA_n	PCI_GNT_n[3]	PCI_INTD_n	PCI_INTB_n	N/C	VDDA_PLL	VSS	VDDA_PCIE	PCI_AD[7]	PCI_AD[6]	PCI_AD[5]	PCI_AD[4]
F	VSS_IO	PCI_GNT_n[2]	PCI_RST_n	VDD_PCI	VDD	VSS	VDD	VIO_PCI	PCI_AD[10]	PCI_AD[9]	PCI_AD[8]	PCI_CBEn[0]
G	PCI_REQ_n[2]	PCI_REQ_n[0]	PCI_GNT_n[0]	PCI_GNT_n[1]	VIO_PCI	VDD	VDD	VSS	VDD_PCI	PCI_AD[13]	PCI_AD[12]	PCI_AD[11]
H	PCI_PME_n	PCI_REQ_n[3]	PCI_CLKO[0]	PCI_REQ_n[1]	VDD_PCI	VSS	VIO_PCI	VDD_PCI	VSS	PCI_CBEn[1]	PCI_AD[15]	PCI_AD[14]
J	PCI_AD[30]	PCI_AD[29]	PCI_AD[31]	VDD	VSS_IO	VDD_PCI	PCI_AD[20]	PCI_AD[16]	VSS_IO	PCI_PERR_n	PCI_SERR_n	PCI_PAR
K	PCI_AD[28]	PWRUP_EN_ARB	VSS_IO	N/C	PCI_AD[25]	PCI_AD[23]	PCI_AD[19]	PCI_CBEn[2]	PCI_DEVSEL_n	VDD	VSS_IO	TEST_SPARE[2]
L	NC	NC	JTAG_TRST_n	PCI_AD[27]	PCI_AD[24]	PCI_AD[22]	PCI_AD[17]	PCI_IRDY_n	PCI_STOP_n	JTAG_TDI	VDD_PCI	JTAG_TDO
M	JTAG_TMS	TEST_SPARE[0]	VDD_PCI	PCI_AD[26]	PCI_CBEn[3]	PCI_AD[21]	PCI_AD[18]	PCI_FRAME_n	PCI_TRDY_n	PCI_LOCK_n	JTAG_TCK	PWRUP_PLL_BYPASS
	12	11	10	9	8	7	6	5	4	3	2	1

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