

## RL78/L23

### LCD display (Temperature display demo)

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#### Abstract

This document describes how to control an LCD panel using the RL78/L23 LCD controller/driver and the sample code operations.

The sample code uses the RL78/L23 LCD controller/driver to demonstrate a usage example of the temperature display. The temperature data is adjusted by pressing the user switch and then displayed by touch detection of the touch buttons.

#### Target Device

RL78/L23

When using this application note with other MCUs, conduct careful evaluation after making modifications to meet the specifications of the alternate MCU.

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## 1. Specifications

The RL78/L23 LCD controller/driver is used to display the temperature data. The user switch operation displays the temperature data stored in the LCD display data memory and the touch button operation is used to change temperature data. When the user switch is not pressed and no touch detection of the touch buttons occurs for a certain period, the LCD display turns off.

lists the peripheral functions and applications used in this application note, Figure 1.1 and shows the Table 1-1 operation overview.

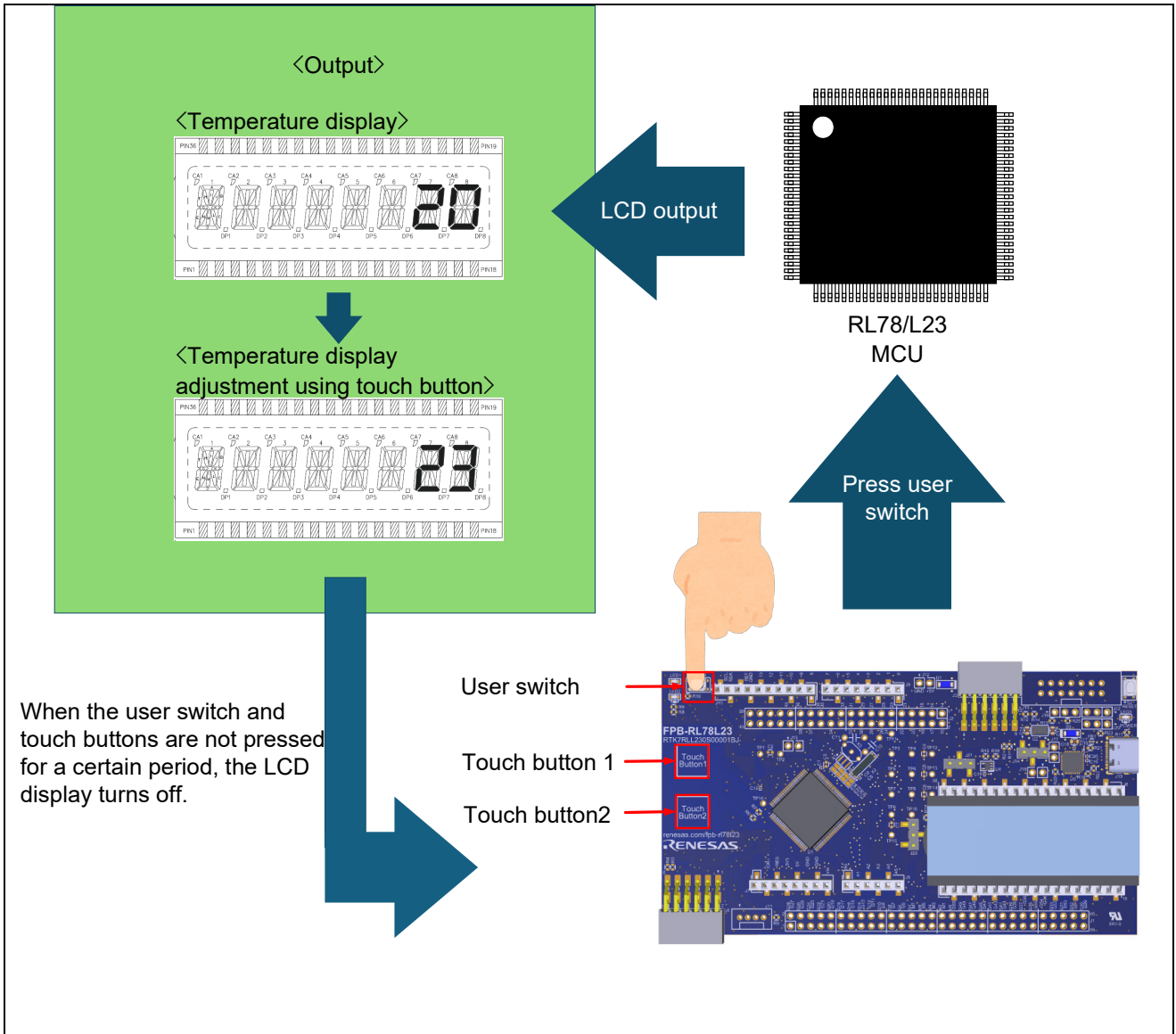
**Table 1-1 Peripheral Functions and Applications**

| Peripheral Function                      | Application  |
|--|--|
| LCD controller/driver                    | LCD display  |
| Realtime clock (RTC)                     | Determines period to transition to wait state.                                   |
| Channel 3 of timer array unit 0 (TAU0_3) | Counts wait time.  |
| Channel 5 of timer array unit 0 (TAU0_5) | Determines continuous touch (long press) on the touch button.                    |
| Capacitive Touch Sensing Unit (CTSU2La)  | Measures capacitance generated on the touch electrode.                           |
| External interrupt INTPO                 | Detects input from the user switch and switches to the temperature change state. |

The LCD controller/driver can use the external resistance division, internal voltage boosting, or capacitor split method to generate LCD drive voltage. For details, refer to 3.3 LCD Drive Voltage Generator.

The sample code selects the capacitor split method for the LCD drive voltage generator

Figure 1.1 Operation Overview



| Switch         | State                            |
|----------------|----------------------------------|
| User switch    | Temperature display/change state |
| Touch button 1 | Temperature +1                   |
| Touch button 2 | Temperature -1                   |

Note: Temperature can be displayed between 12°C to 30°C.

## 2. Operation Confirmation Conditions

The sample code accompanying this application note has been run and confirmed under the conditions below.

**Table 2-1 Operation Confirmation Conditions**

| Item   | Contents   |
|--|--|
| MCU used   | RL78/L23(R7F100LPL3CFB)  |
| Operating frequencies                                      | <ul style="list-style-type: none"> <li>• High-speed on-chip oscillator clock (<math>f_{HOCO}</math>) : 32MHz(typ.)</li> <li>• CPU/peripheral hardware clock (<math>f_{CLK}</math>) : 32MHz</li> <li>• Low-speed on-chip oscillator clock(<math>f_{IL}</math>) : 32.768kHz</li> </ul> |
| Operating voltage  | 3.3V<br>LVD operation( $V_{LVD0}$ ) : in reset mode is 2.97 V at the rising edge or 2.91 V at the falling edge.  |
| Integrated development environment (CS+)                   | CS+ for CC V8.14.00 from Renesas Electronics Corp.   |
| C compiler (CS+)   | Renesas CC-RL V1.15.01 from Renesas Electronics Corp.  |
| Integrated development environment (e <sup>2</sup> studio) | e <sup>2</sup> studio Version 2025-07 from Renesas Electronics Corp.   |
| C compiler (e <sup>2</sup> studio)                         | Renesas CC-RL V1.15.01 from Renesas Electronics Corp.  |
| Integrated development environment (IAR)                   | IAR Systems<br>IAR Embedded Workbench for Renesas RL78 V 5.20.1  |
| C compiler (IAR)   |  |
| Smart Configurator   | RL78 Smart Configurator V1.14.0 from Renesas Electronics Corp.   |
| Development support tool for the capacitive touch sensor   | QE for Capacitive Touch V4.2.0   |
| Board support package (BSP)                                | V1.91 Smart Configurator from Renesas Electronics Corp..   |
| TOUCH Driver   | V2.20 from Renesas Electronics Corp.   |
| CTSU Driver  | V2.20 from Renesas Electronics Corp.   |
| Board used   | RL78/L23 Fast Prototyping Board( RTK7RLL230S00001BJ)   |
| LCD module   | Varitronix VIM-878-DP-FC-S-LV 16 segments x 8 digits<br>Header: 36-pin (18 x 2 rows) x 1<br>Operating voltage condition: 3V to 4.6V (When using the module with operating voltage other than 3.3V, remove the LCD panel.)  |

### 3. Peripheral Function

This chapter describes the LCD controller/driver.

For more information on the operation, refer to section 22 LCD Controller/Driver in RL78/L23 User's Manual: Hardware (R01UH1082).

#### 3.1 Basic Features of RL78/L23 LCD Controller/Driver

RL78/L23 LCD controller/driver includes the following features:

- Waveform A or B selectable
- LCD driver voltage generator can be switched between internal voltage boosting, capacitor split, or external resistance division
- Segment and common signals can be output automatically by reading the LCD display data register automatically Reference voltage generated when the voltage boost circuit is operating can be selected from 23 levels (contrast adjustment)
- LCD blinking is available

### 3.2 LCD Controller/Driver Display Mode

LCD controller/driver display modes are combinations of the LCD drive waveform and LCD voltage generator. Table 3-1, Table 3-2 and Table 3-3 list the maximum number of pixels in each display mode.

**Table 3-1 Maximum Number of Pixels for an 100-pin Package(waveform A)(1/2)**

| Drive Waveform for LCD Driver | LCD Driver Voltage Generator |  | Bias Mode                 | Number of Time Slices     | Maximum Number of Pixels                    |   |   |
|-------------------------------|------------------------------|--|---------------------------|---------------------------|---|---|---|
| waveform A                    | External resistance division |  | -                         | Static                    | 56 (56 segment signals × 1 common signals)  |   |   |
|                               |                              |  | 1/2                       | 2                         | 112 (56 segment signals × 2 common signals) |   |   |
|                               |                              |  |                           | 3                         | 168 (56 segment signals × 3 common signals) |   |   |
|                               |                              |  | 1/3                       | 3                         |   |   |   |
|                               |                              |  |                           | 4                         | 224 (56 segment signals × 4 common signals) |   |   |
|                               |                              |  |                           | 6                         | 324 (54 segment signals × 6 common signals) |   |   |
|                               |                              |  |                           | 8                         | 416 (52 segment signals × 8 common signals) |   |   |
|                               |                              |  | 1/4                       | 8                         |   |   |   |
|                               | Internal voltage Boosting    |  | V <sub>L1</sub> Reference | 1/3                       | 3   | 168 (56 segment signals × 3 common signals) |   |
|                               |                              |  |                           |                           | 4   | 224 (56 segment signals × 4 common signals) |   |
|                               |                              |  |                           |                           | 6   | 324 (54 segment signals × 6 common signals) |   |
|                               |                              |  |                           |                           | 8   | 416 (52 segment signals × 8 common signals) |   |
|                               |                              |  |                           | 1/4                       | 6   | 324 (54 segment signals × 6 common signals) |   |
|                               |                              |  |                           |                           | 8   | 416 (52 segment signals × 8 common signals) |   |
|                               |                              |  |                           | V <sub>L2</sub> Reference | 1/3   | 3   | 168 (56 segment signals × 3 common signals) |
|                               |                              |  |                           |                           |   | 4   | 224 (56 segment signals × 4 common signals) |
|                               |                              |  | 6                         |                           |   | 324 (54 segment signals × 6 common signals) |   |
|                               |                              |  | 1/4                       |                           | 8   | 416 (52 segment signals × 8 common signals) |   |
|                               |                              |  |                           |                           | 6   | 324 (54 segment signals × 6 common signals) |   |
|                               |                              |  |                           |                           | 8   | 416 (52 segment signals × 8 common signals) |   |

Table 3-2 Maximum Number of Pixels for an 100-pin Package(waveform A)(2/2)

| Drive Waveform for LCD Driver | LCD Driver Voltage Generator |   | Bias Mode | Number of Time Slices     | Maximum Number of Pixels                    |   |   |
|-------------------------------|------------------------------|---|-----------|---------------------------|---|---|---|
| waveform A                    | Capacitor split              | V <sub>DD</sub> Reference                   | 1/3       | 3                         | 168 (56 segment signals × 3 common signals) |   |   |
|                               |                              |   |           | 4                         | 224 (56 segment signals × 4 common signals) |   |   |
|                               |                              |   |           | 6                         | 324 (54 segment signals × 6 common signals) |   |   |
|                               |                              |   |           | 8                         | 416 (52 segment signals × 8 common signals) |   |   |
|                               |                              |   | 1/4       | 6                         | 324 (54 segment signals × 6 common signals) |   |   |
|                               |                              |   |           | 8                         | 416 (52 segment signals × 8 common signals) |   |   |
|                               |                              |   |           | V <sub>L4</sub> Reference | 1/3   | 3 | 168 (56 segment signals × 3 common signals) |
|                               |                              |   |           |                           |   | 4 | 224 (56 segment signals × 4 common signals) |
|                               | 6                            | 324 (54 segment signals × 6 common signals) |           |                           |   |   |   |
|                               | 8                            | 416 (52 segment signals × 8 common signals) |           |                           |   |   |   |

Table 3-3 Maximum Number of Pixels for an 100-pin Package(waveform B)

| Drive Waveform for LCD Driver | LCD Driver Voltage Generator |   | Bias Mode                 | Number of Time Slices | Maximum Number of Pixels                    |   |
|-------------------------------|------------------------------|---|---------------------------|-----------------------|---|---|
| waveform B                    | External resistance division |   | 1/3                       | 3                     | 168 (56 segment signals × 3 common signals) |   |
|                               |                              |   |                           | 4                     | 224 (56 segment signals × 4 common signals) |   |
|                               |                              |   |                           | 6                     | 324 (54 segment signals × 6 common signals) |   |
|                               |                              |   |                           | 8                     | 416 (52 segment signals × 8 common signals) |   |
|                               |                              |   | 1/4                       | 8                     |   |   |
|                               | Internal voltage boosting    |   | V <sub>L1</sub> Reference | 1/3                   | 3   | 168 (56 segment signals × 3 common signals) |
|                               |                              |   |                           |                       | 4   | 224 (56 segment signals × 4 common signals) |
|                               |                              |   |                           |                       | 6   | 324 (54 segment signals × 6 common signals) |
|                               |                              |   |                           |                       | 8   | 416 (52 segment signals × 8 common signals) |
|                               |                              |   |                           | 1/4                   | 8   |   |
|                               |                              |   | V <sub>L2</sub> Reference | 1/3                   | 3   | 168 (56 segment signals × 3 common signals) |
|                               |                              |   |                           |                       | 4   | 224 (56 segment signals × 4 common signals) |
|                               |                              |   |                           |                       | 6   | 324 (54 segment signals × 6 common signals) |
|                               |                              |   |                           |                       | 8   | 416 (52 segment signals × 8 common signals) |
|                               |                              |   | Capacitor split           |                       | V <sub>DD</sub> Reference                   | 1/3   |
|                               | 4                            | 224 (56 segment signals × 4 common signals) |                           |                       |   |   |
|                               | 6                            | 324 (54 segment signals × 6 common signals) |                           |                       |   |   |
|                               | 8                            | 416 (52 segment signals × 8 common signals) |                           |                       |   |   |
|                               | 1/4                          | 8   |                           |                       |   |   |
|                               | V <sub>L4</sub> Reference    | 1/3   |                           |                       | 3   | 168 (56 segment signals × 3 common signals) |
|                               |                              |   |                           |                       | 4   | 224 (56 segment signals × 4 common signals) |
|                               |                              |   |                           |                       | 6   | 324 (54 segment signals × 6 common signals) |
|                               |                              |   |                           |                       | 8   | 416 (52 segment signals × 8 common signals) |

### 3.3 LCD Drive Voltage Generator

The RL78/L23 LCD controller/driver can use external resistance division, internal voltage boosting, or capacitor split to generate LCD drive voltage. This chapter covers the features of each method.

Table 3-4 LCD Drive Method and Its Application

| LCD Drive Method             | Feature/Usage       |                   |  | Application  |
|------------------------------|---------------------|-------------------|--|--|
|                              | Drive capacity      | Operating current | Drive voltage  |  |
| External resistance division | High                | Standard          | $V_{DD}$ - dependent   | <p><b><u>Suitable for large format LCDs or AC power supply sets</u></b></p> <p>The LCD drive capacity is high and the drive voltage is generated by a resistor divider, which contributes to cost reduction. This method generates the LVD drive voltage by an external resistor divider. As the voltage is applied externally, the operating current and drive capacity can be adjusted by an external resistor.</p>  |
|                              | Supports large LCDs |                   |  |  |
| Internal voltage boosting    | Standard            | Small             | Constant   | <p><b><u>Suitable for battery sets</u></b></p> <p>The operating current is small and the LCD display does not become dim as the drive voltage is constant even when the battery voltage is reduced. This method generates the reference voltage internally and boosts it by an external capacitor. As the reference voltage is adjusted by software, the LCD contrast can be adjusted from 23 levels in RL78/L23.</p>  |
|                              |                     |                   | As the drive voltage is constant, the LCD display does not change with the battery voltage decreased |  |
| Capacitor split              | Standard            | Much Smaller      | Depends on $V_{DD}$ when the reference voltage is $V_{DD}$   | <p><b><u>Suitable for battery sets</u></b></p> <p>This method has the smallest operating current among three LCD drive modes, and thus the LCD display becomes dim with decreasing the supply voltage. Use this method to allow the screen to be dim according to the battery level. If you do not want the screen to be dim when the battery voltage is decreased, change the LCD drive method to internal voltage boosting. It works in an external circuit of the capacitor split method.</p> |
|                              |                     |                   | LCD display becomes dim with the supply voltage decreased  |  |

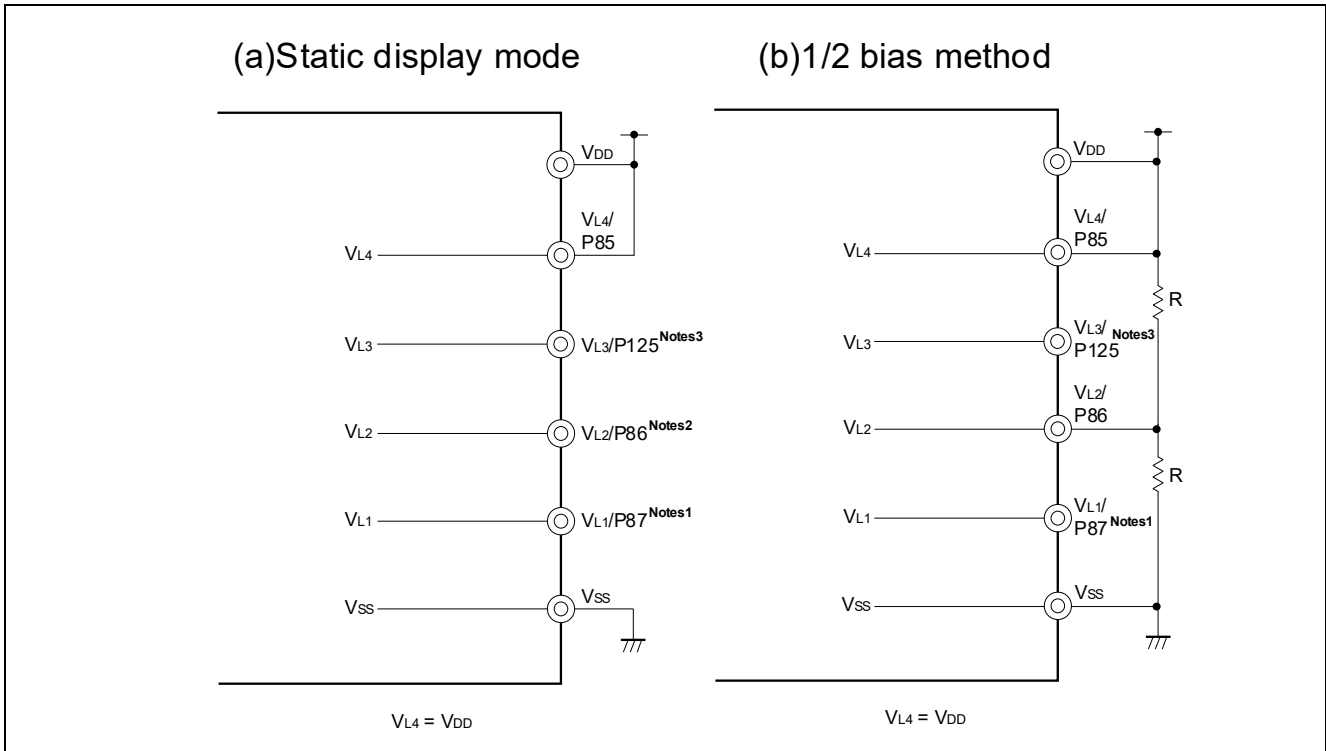
**3.3.1 External Resistance Division Method**

This is suitable for large format LCDs or AC power sets. As it has a large drive capacity and generates the drive voltage by a resistor divider, which contributes to cost reduction.

To be more specific, this method generates an LCD drive voltage using an external resistor divider. As the voltage is applied externally, the operating current or the drive capacity can be adjusted by the external resistor.

Figure 3.1 and Figure 3.2 show connection examples of external resistance division methods.

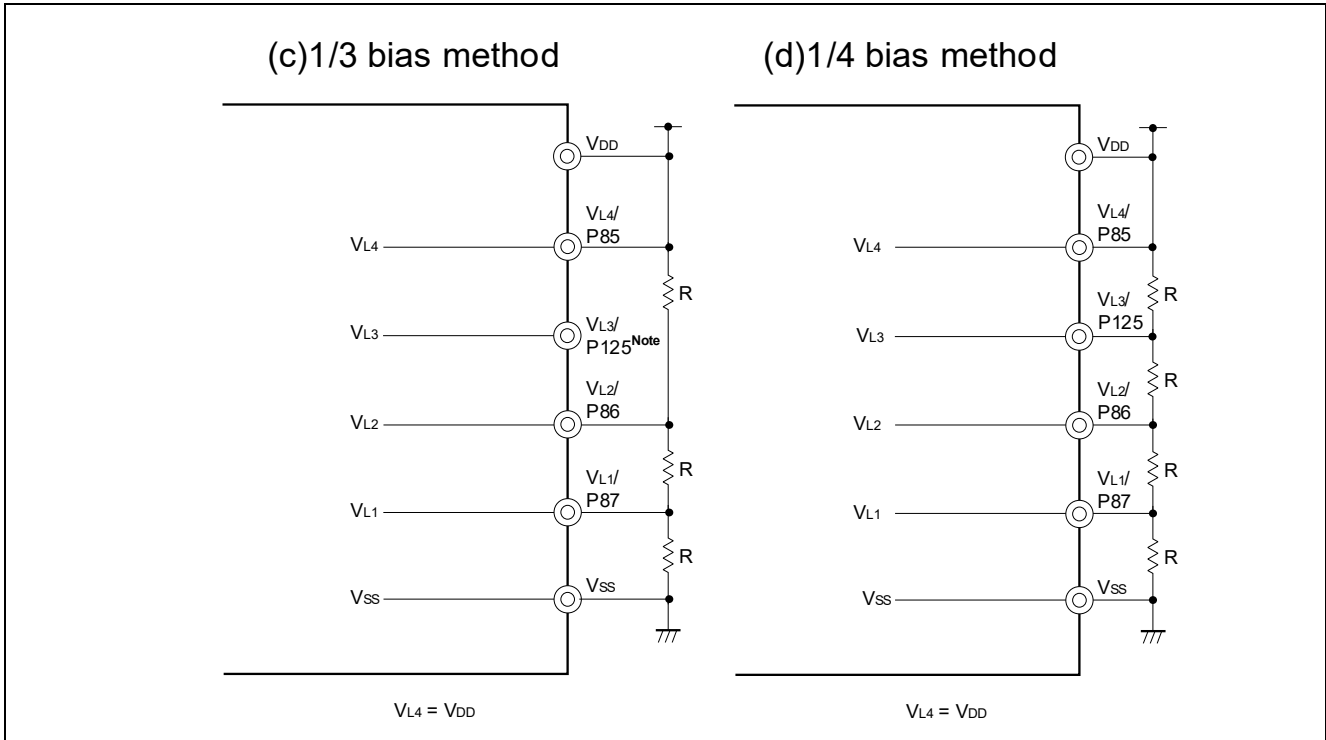
**Figure 3.1 Connection Example of External Resistance Division Method (1/2)**



Notes for Figure 3.1 (a) and (b).

- Notes 1. VL1 can be used as a port(P87).
- Notes 2. VL2 can be used as a port(P86).
- Notes 3. VL3 can be used as a port(P125).

Figure 3.2 Connection Example of External Resistance Division Method (2/2)



Note.  $V_{L3}$  can be used as a port(P125).

Notes 1.

CAPL can be used as port-pin P126 and CAPH can be used as port-pin P127.

Notes 2.

The reference resistance "R" value for external resistance division is 10 k $\Omega$  to 1 M $\Omega$ .

In addition, to stabilize the potential of the  $V_{L1}$  to  $V_{L4}$  pins, connect a capacitor between each of pins  $V_{L1}$  to  $V_{L4}$  and the GND pin as needed. The reference capacitance is about 0.47  $\mu$ F but it depends on the LCD panel used, the number of segment pins, the number of common pins, the frame frequency, and the operating environment. Thoroughly evaluate these values in accordance with your system and adjust and determine the capacitance.

### 3.3.2 Internal Voltage Boosting Method

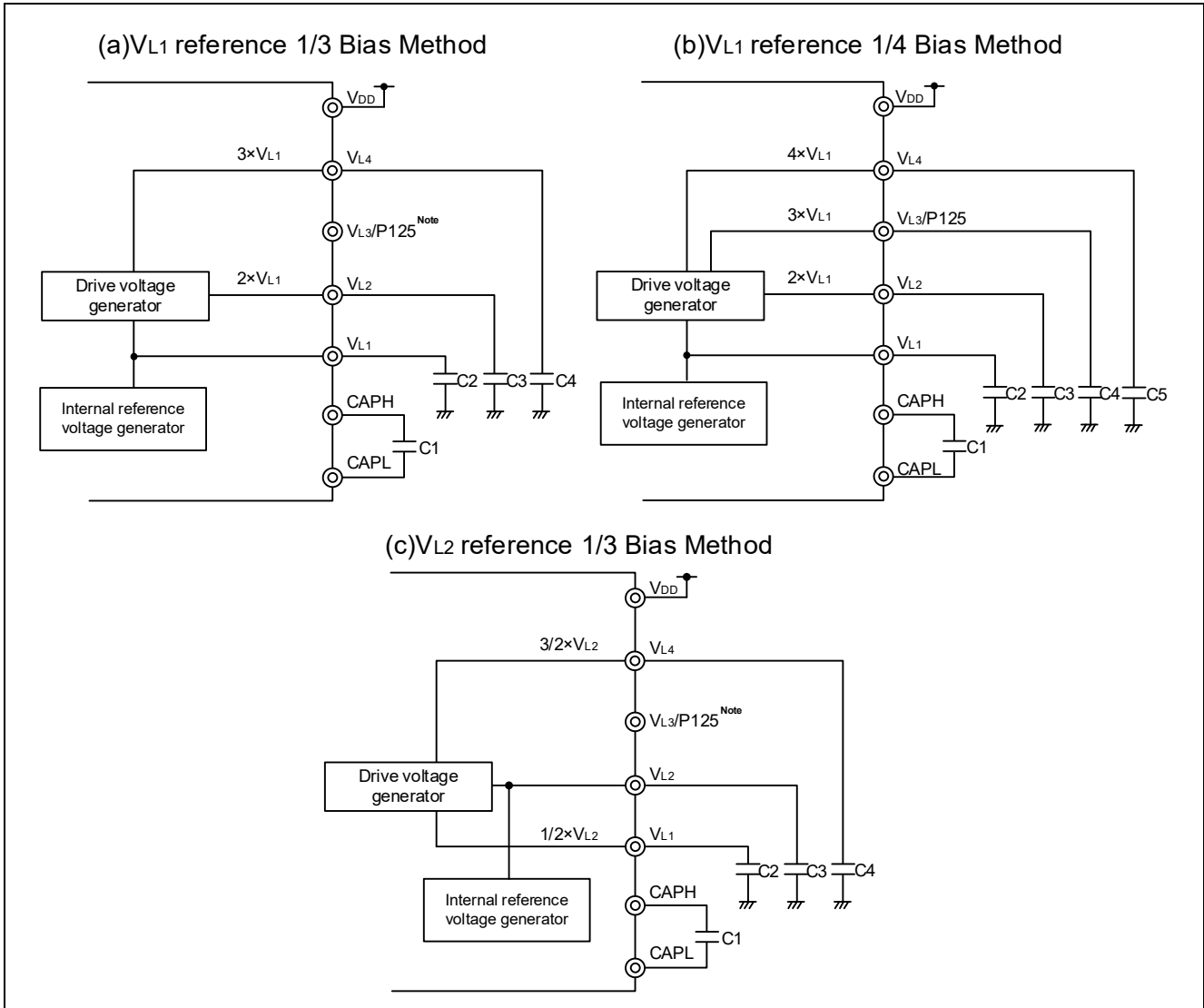
This is suitable for a battery set.

The operating current is small, and the LCD display does not become dim as the drive voltage is constant even when the battery voltage is reduced.

This method generates the reference voltage internally and boosts it by an external capacitor. As the reference voltage is adjusted by software (the LCD boost level control register, VLCD), the LCD contrast can be adjusted from 23 levels in RL78/L23.

Figure 3.3 shows a connection example of an internal voltage boosting method.

Figure 3.3 Connection Example of Internal Voltage Boosting Method



Note:  $V_{L3}$  can be used as a port(P125)

Remark: Use a capacitor with as little leakage as possible. Make sure to use a non-polar capacitor for  $C_1$ .

### 3.3.3 Capacitor Split Method

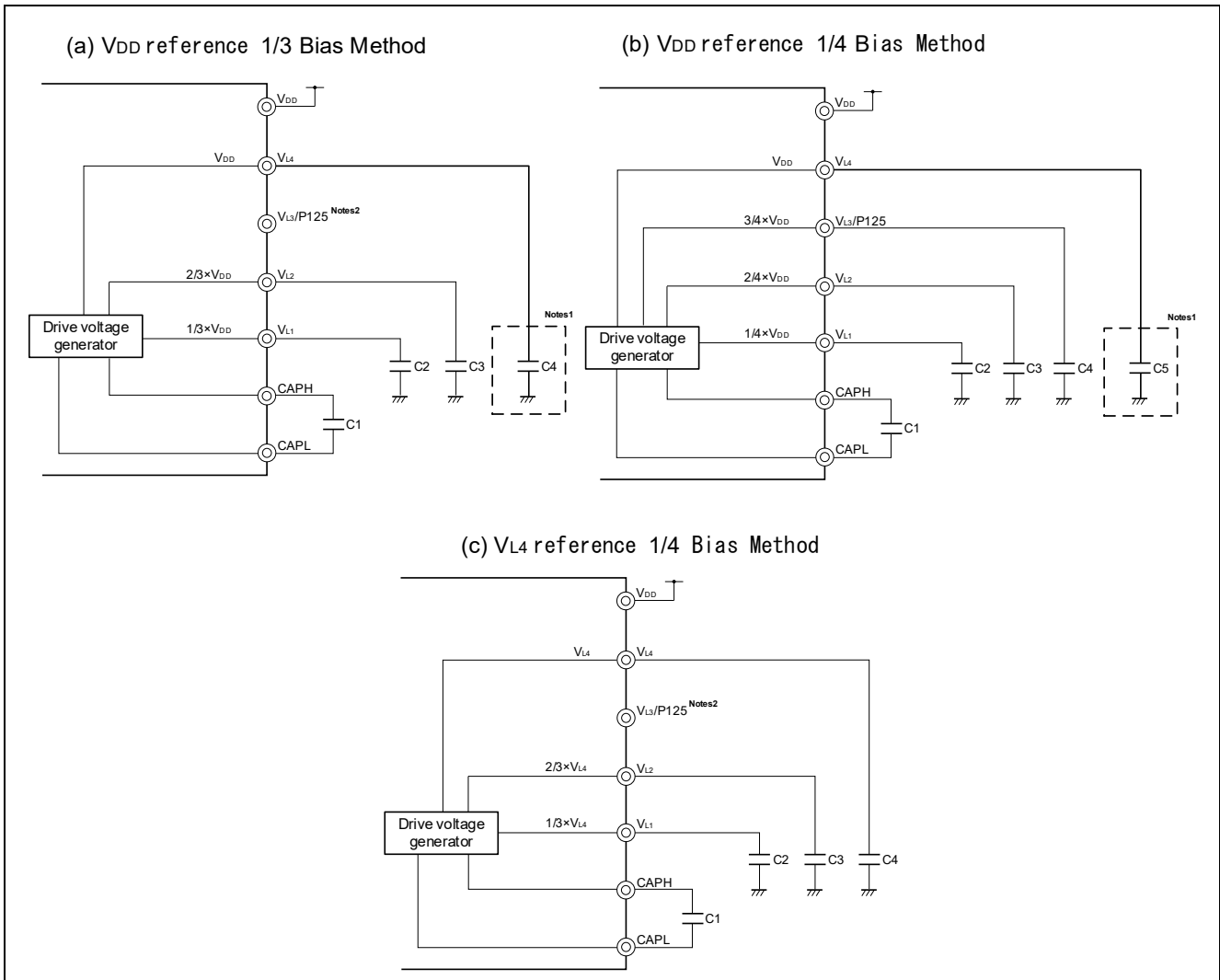
This is also suitable for a battery set.

This method has the smallest operating current among three LCD drive modes, and thus the LCD display becomes dim with the supply voltage decreased.

If you do not want the screen to be dim when the battery voltage is decreased, change the LCD drive method to internal voltage boosting method. The method works in an external circuit of the capacitor split method.

Figure 3.4 shows a connection example of capacitor split method.

**Figure 3.4 Connection Example of Capacitor Split Method**



Notes 1. As the  $V_{L4}$  pin is internally connected to the  $V_{DD}$  pin, the capacitor is not always essential.

Note, however, that selection of the internal voltage boosting method requires connection of this capacitor. In addition, when only capacitor split method is in use, the capacitor can be connected as a means for stabilizing the voltage supplied to the LCD.

Notes 2.  $V_{L3}$  can be used as port-pin P125

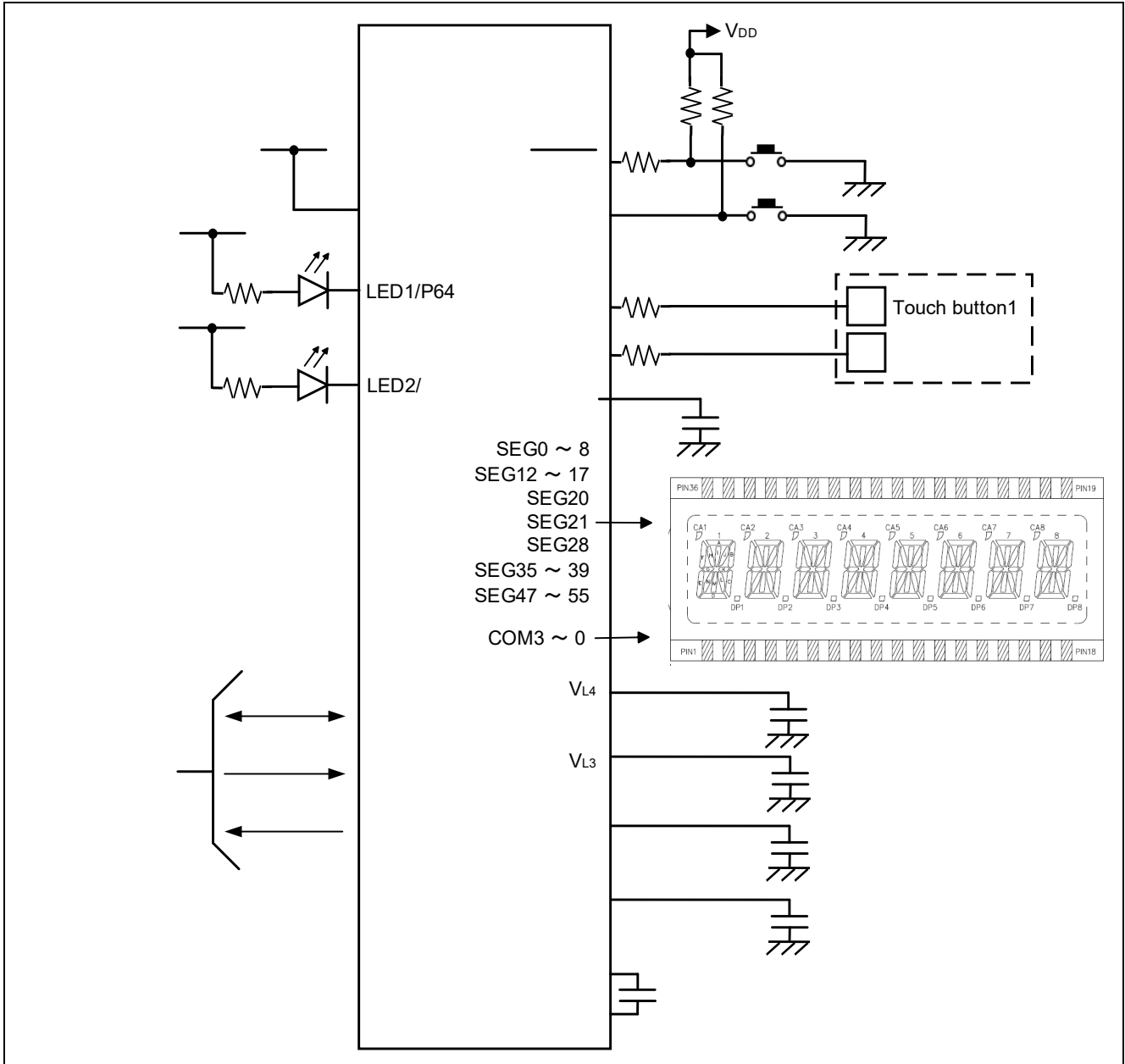
Remark: Use a capacitor with as little leakage as possible. Make sure to use a non-polar capacitor for C1.

4. Hardware

4.1 Hardware Example

Figure 4.1 shows the hardware configuration used in this application note.

Figure 4.1 Hardware Configuration



Notes 1. The above figure is simplified to show an overview of the hardware connection. When designing application circuits, make sure to handle unused pins appropriately to satisfy the electrical characteristics (connect input only ports independently to either V<sub>DD</sub> or V<sub>SS</sub> via resistors).

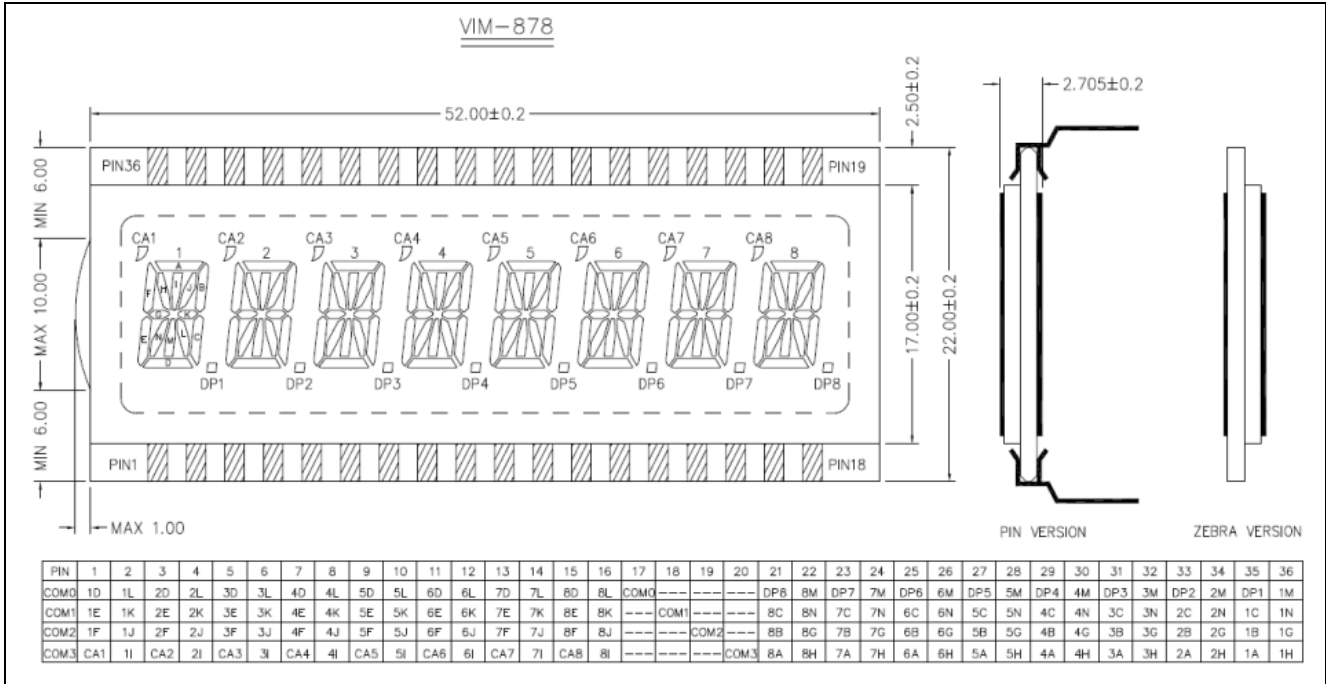
Notes 2. Make sure to set V<sub>DD</sub> greater than the detection voltage (V<sub>LVD</sub>) specified by the LVD.

### 4.2 LCD Module

This section describes the LCD module used in the sample code accompanying this application note.

The RL78/L23 Fast Prototyping Board is equipped with the LCD panel (16 segments, 8 digits) with the sockets. Figure 4.2 shows the panel image and the pin allocation table. Table 4-1 and Table 4-2 show the connections between the LCD panel and RL78/L23.

Figure 4.2 LCD Panel and Pin Allocation Table



(Source : [Datasheet for VIM-878-DP-FC-S-LV Varitronix Optoelectronics | Octopart](#))

Table 4-1 J5 Socket and LCD Module Connection Table

| J5 socket pin number | LCD panel pin number | Symbol | port | Symbol and port | Pin number | LCD Header |
|----------------------|----------------------|--------|------|-----------------|------------|------------|
| 1                    | LCD_1                | SEG8   | P54  | P54(SEG8)       | 54         | J1_21      |
| 2                    | LCD_2                | SEG7   | P53  | P53(SEG7)       | 55         | J1_22      |
| 3                    | LCD_3                | SEG6   | P52  | P52(SEG6)       | 56         | J1_23      |
| 4                    | LCD_4                | SEG5   | P51  | P51(SEG5)       | 57         | J1_24      |
| 5                    | LCD_5                | SEG4   | P50  | P50(SEG4)       | 58         | J1_25      |
| 6                    | LCD_6                | SEG3   | P97  | P97(SEG3)       | 59         | J1_26      |
| 7                    | LCD_7                | SEG2   | P96  | P96(SEG2)       | 60         | J1_27      |
| 8                    | LCD_8                | SEG1   | P95  | P95(SEG1)       | 61         | J1_28      |
| 9                    | LCD_9                | SEG0   | P94  | P94(SEG0)       | 62         | J1_29      |
| 10                   | LCD_10               | SEG50  | P07  | P07(SEG50)      | 69         | J1_34      |
| 11                   | LCD_11               | SEG49  | P06  | P06(SEG49)      | 70         | J1_35      |
| 12                   | LCD_12               | SEG48  | P05  | P05(SEG48)      | 71         | J1_36      |
| 13                   | LCD_13               | SEG47  | P04  | P04(SEG47)      | 72         | J1_37      |
| 14                   | LCD_14               | SEG39  | P14  | P14(SEG39)      | 83         | J1_45      |
| 15                   | LCD_15               | SEG38  | P13  | P13(SEG38)      | 84         | J1_46      |
| 16                   | LCD_16               | SEG37  | P12  | P12(SEG37)      | 85         | J1_47      |
| 17                   | LCD_17               | COM0   | P90  | P90(COM0)       | 66         | J1_33      |
| 18                   | LCD_18               | COM1   | P91  | P91(COM1)       | 65         | J1_32      |

Table 4-2 J6 Socket and LCD Module Connection Table

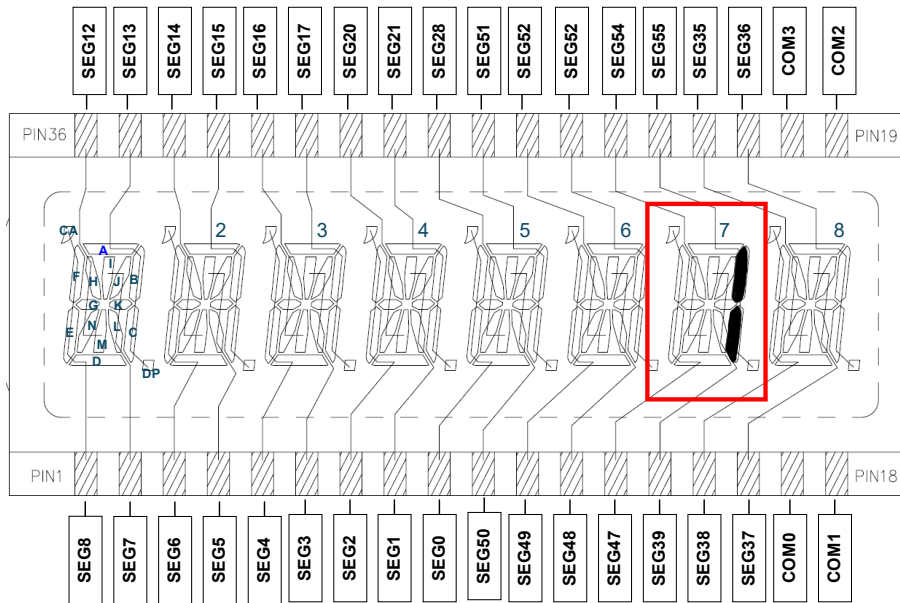
| J6 socket pin number | LCD panel pin number | Symbol | port | Symbol and port | Pin number | LCD Header |
|----------------------|----------------------|--------|------|-----------------|------------|------------|
| 1                    | LCD_19               | COM2   | P92  | P92(COM2)       | 64         | J1_31      |
| 2                    | LCD_20               | COM3   | P93  | P93(COM3)       | 63         | J1_30      |
| 3                    | LCD_21               | SEG36  | P11  | P11(SEG36)      | 86         | J1_48      |
| 4                    | LCD_22               | SEG35  | P10  | P10(SEG35)      | 87         | J1_49      |
| 5                    | LCD_23               | SEG55  | P145 | P145(SEG55)     | 88         | J1_50      |
| 6                    | LCD_24               | SEG54  | P144 | P144(SEG54)     | 89         | J1_51      |
| 7                    | LCD_25               | SEG53  | P143 | P143(SEG53)     | 94         | J1_56      |
| 8                    | LCD_26               | SEG52  | P142 | P142(SEG52)     | 95         | J1_57      |
| 9                    | LCD_27               | SEG51  | P141 | P141(SEG51)     | 96         | J1_58      |
| 10                   | LCD_28               | SEG28  | P130 | P130(SEG28)     | 2          | J1_1       |
| 11                   | LCD_29               | SEG21  | P31  | P31(SEG21)      | 39         | J1_8       |
| 12                   | LCD_30               | SEG20  | P30  | P30(SEG20)      | 40         | J1_9       |
| 13                   | LCD_31               | SEG17  | P75  | P75(SEG17)      | 43         | J1_12      |
| 14                   | LCD_32               | SEG16  | P74  | P74(SEG16)      | 44         | J1_13      |
| 15                   | LCD_33               | SEG15  | P73  | P73(SEG15)      | 45         | J1_14      |
| 16                   | LCD_34               | SEG14  | P72  | P72(SEG14)      | 46         | J1_15      |
| 17                   | LCD_35               | SEG13  | P71  | P71(SEG13)      | 47         | J1_16      |
| 18                   | LCD_36               | SEG12  | P70  | P70(SEG12)      | 48         | J1_17      |

Figure 4.3 shows an example of the 8-digit LCD panel display and the segments and their corresponding segment signals used to achieve the display.

Figure 4.3 Segments and Corresponding Signals

Example of displaying "1" on DISIT 7

The following processing is implemented based on the pin assignments indicated in Tables 4.1 and 4.2.



Segment signal SEG55 is connected to PIN23 of the LCD module.

From the pin assignment table:

PIN23 7C is connected to the COM1 signal

PIN23 7B is connected to the COM2 signal

When 06H is set in the SEG55 register, 7C and 7B will turn on and DISIT 7 will display "1".

|      |     |    |     |    |     |    |     |    |     |    |     |    |     |    |     |    |      |      |      |      |     |    |     |    |     |   |
|------|-----|----|-----|----|-----|----|-----|----|-----|----|-----|----|-----|----|-----|----|------|------|------|------|-----|----|-----|----|-----|---|
| PIN  | 1   | 2  | 3   | 4  | 5   | 6  | 7   | 8  | 9   | 10 | 11  | 12 | 13  | 14 | 15  | 16 | 17   | 18   | 19   | 20   | 21  | 22 | 23  | 24 | 25  | 2 |
| COM0 | 1D  | 1L | 2D  | 2L | 3D  | 3L | 4D  | 4L | 5D  | 5L | 6D  | 6L | 7D  | 7L | 8D  | 8L | COM0 | ---  | ---  | ---  | DP8 | 8M | DP7 | 7M | DP6 | 6 |
| COM1 | 1E  | 1K | 2E  | 2K | 3E  | 3K | 4E  | 4K | 5E  | 5K | 6E  | 6K | 7E  | 7K | 8E  | 8K | ---  | COM1 | ---  | ---  | 8C  | 8N | 7C  | 7N | 6C  | 6 |
| COM2 | 1F  | 1J | 2F  | 2J | 3F  | 3J | 4F  | 4J | 5F  | 5J | 6F  | 6J | 7F  | 7J | 8F  | 8J | ---  | ---  | COM2 | ---  | 8B  | 8G | 7B  | 7G | 6B  | 6 |
| COM3 | CA1 | 1I | CA2 | 2I | CA3 | 3I | CA4 | 4I | CA5 | 5I | CA6 | 6I | CA7 | 7I | CA8 | 8I | ---  | ---  | ---  | COM3 | 8A  | 8H | 7A  | 7H | 6A  | 6 |

(Additional info : [VIM-878.pdf](#))

Table 4-3 Segments and Corresponding Commons (DISIT 7)

| LCD Module<br>PIN No. <sup>Note</sup> | LCD Display<br>Data Register <sup>Note</sup> | COM3 | COM2 | COM1 | COM0 |
|---------------------------------------|--|------|------|------|------|
|                                       |  | bit3 | bit2 | bit1 | bit0 |
| PIN23                                 | SEG55  | A    | B    | C    | DP   |
| PIN13                                 | SEG47  | CA   | F    | E    | D    |
| PIN14                                 | SEG39  | I    | J    | K    | L    |
| PIN24                                 | SEG54  | H    | G    | N    | M    |

Note: For other DISITs, replace with the corresponding PIN number and segment signal register name.

### 4.3 Capacitive Touch Sensing Unit

This section describes the capacitive touch sensing unit used in this sample code.

RL78/L23 Fast Prototyping Board is equipped with two electrodes: touch buttons 1 and 2.

The touch buttons can be enabled by using the CTSU module, the TOUCH module, and QE for Capacitive Touch (development support tool for the capacitive touch sensor). For details on how to use the touch buttons, refer to Capacitive Sensor Microcontrollers CTSU Capacitive Touch Introduction Guide (R30AN0424).

## 4.4 Pins Used

Table 4-4 Pins Used and Their Functions (1/2)

| Pin name            | I/O          | Function   |
|---------------------|--------------|--|
| P137/INTP0          | Input        | Detects input from the user switch and enters hour, minute or second setting mode                    |
| P57/TSCAP           | -            | Secondary power supply capacitor connection pin for measurement                                      |
| P56/TS17            | Input-Output | Detects input from the touch button 1 and increments hours, minutes and seconds displayed on the LCD |
| P55/TS18            | Input-Output | Detects input from the touch button 2 and decrements hours, minutes and seconds displayed on the LCD |
| P94/SEG0            | Output       | LCD controller/driver common signals   |
| P95/SEG1            |              |  |
| P96/SEG2            |              |  |
| P97/SEG3            |              |  |
| P50/SEG4            |              |  |
| P51/SEG5            |              |  |
| P52/SEG6            |              |  |
| P53/SEG7            |              |  |
| P54/SEG8            |              |  |
| P70/SEG12           |              |  |
| P71/SEG13           |              |  |
| P72/SEG14           |              |  |
| P73/SEG15           |              |  |
| P74/SEG16           |              |  |
| P75/SEG17           |              |  |
| P30/SEG20           |              |  |
| P31/SEG21           |              |  |
| P130/SEG28          |              |  |
| P10/SEG35           |              |  |
| P11/SEG36           |              |  |
| P12/SEG37           |              |  |
| P13/SEG38           |              |  |
| P14/SEG39           |              |  |
| P04/SEG47           |              |  |
| P05/SEG48           |              |  |
| P06/SEG49           |              |  |
| P07/SEG50           |              |  |
| P141/SEG51          |              |  |
| P142/SEG52          |              |  |
| P143/SEG53          |              |  |
| P144/SEG54          |              |  |
| P145/SEG55          |              |  |
| P90/COM0            | Output       | LCD controller/driver common signals   |
| P91/COM1            |              |  |
| P92/COM2            |              |  |
| P93/COM3            |              |  |
| P87/V <sub>L1</sub> | -            | LCD drive voltage  |
| P86/V <sub>L2</sub> |              |  |
| P85/V <sub>L4</sub> |              |  |

Table 4-5 Pins Used and Their Functions (2/2)

| Pin name    | I/O          | Function                                       |
|-------------|--------------|--|
| P126/CAPL   | -            | Capacitor connection for LCD controller/driver |
| P127/CAPH   |              |  |
| P40/TOOL0   | Input-Output | COM Port debugging                             |
| P17/TOOLRxD | Input        | COM Port debugging                             |
| P00/TOOLTxD | Output       | COM Port debugging                             |

The following connections of unused pins are applied to P123 and P124.  
 The CSC register is set by the `qe_touch_main` function in `qe_touch_sample.c`. The CMC register is set by the `mcu_clock_setup` function in `mcu_clocks.c` as shown in Figure 4.4.

Figure 4.4 Editing the `mcu_clocks.c` File

```

844
845
846 /* Clock operation mode control register(CMC) setting */
847
848 cmc_tmp &= 0xDF; /* Connection of unused pins for P123 and P124 */
849 cmc_tmp |= 0x10; /* Connection of unused pins for P123 and P124 */
850
851 CMC = cmc_tmp;
    
```

Caution If you change the version of the "r\_bsp" component in the Smart Configurator, `mcu_clocks.c` will be overwritten and any code added by the user will be erased. Therefore, whenever you change the version of the "r\_bsp" component, you will need to add the code for the above settings.

## 5. Software

### 5.1 Operation Overview

This sample code uses the RL78/L23 LCD controller/driver to display assumed temperature data on the remote controller. The temperature data is displayed on the LCD with the user switch operation and the displayed temperature data is changed with the touch button operation. When the user switch and touch buttons are not touched for a certain period, the LCD display turns off.

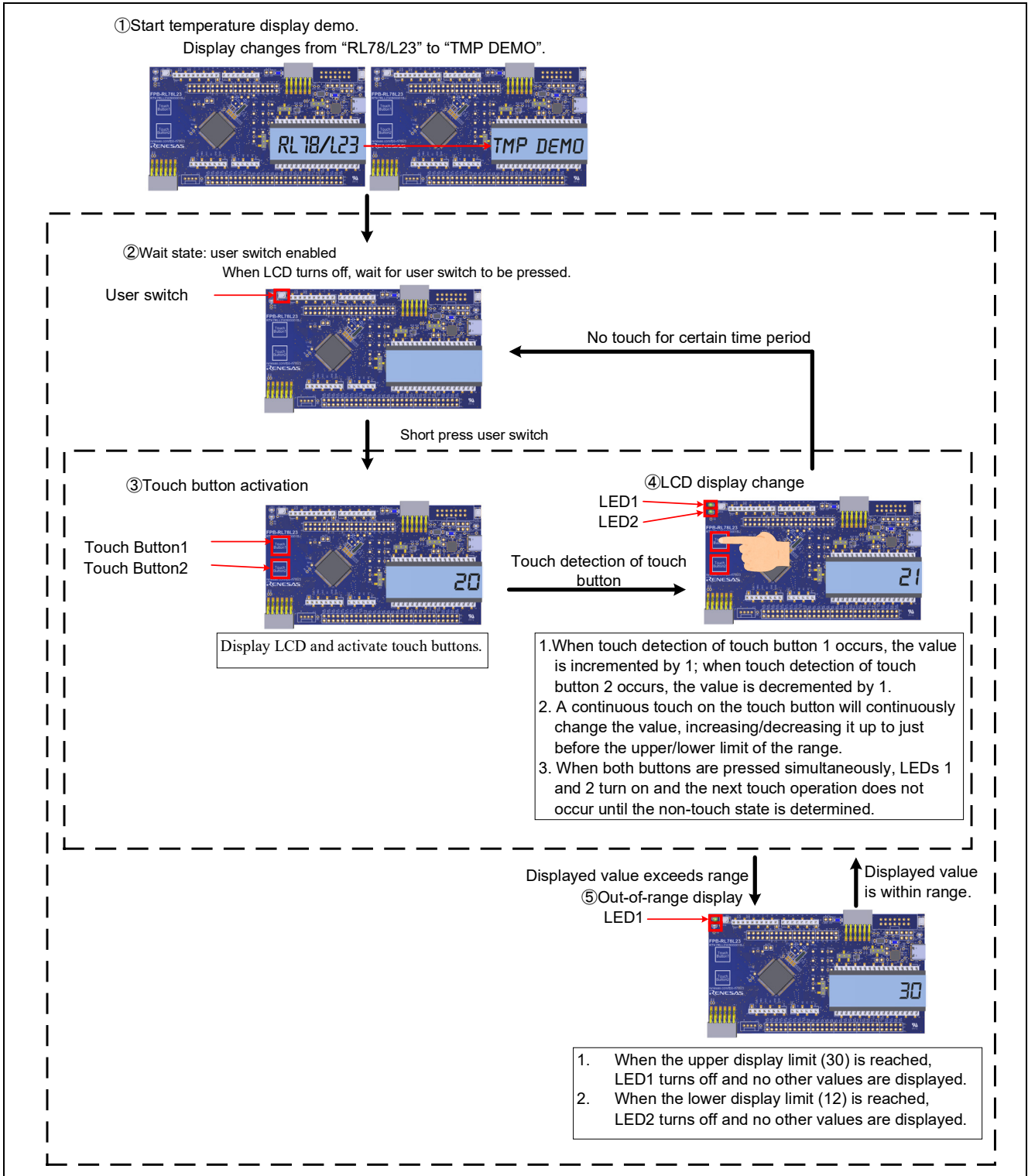
The clock frequency, I/O ports, RTC, and LCD controller/driver are configured in the initial settings.

After the initial settings are complete, the sample code displays "RL78/L23" and "TMP DEMO" on the LCD consecutively, and transitions to the wait state. The wait state is released by a short press on the user switch (both edges of INTPO detected) and the processing transitions to the temperature data adjustment state.

The touch buttons are disabled in the wait state, and available only in the temperature data adjustment state.

In the temperature data adjustment state, the temperature data is adjusted within the range of 12°C and 30°C, and the adjusted data is displayed on the LCD. The initial value of the temperature data is 20°C. Touch detection of touch button 1 increases the temperature data by 1, and touch detection of touch button 2 decreases the temperature data by 1. The temperature data can be changed continuously by touching the touch button continuously (long press). When the user switch is not pressed and no touch detection of the touch buttons occurs for a certain period, the state returns to the wait state and the LCD display turns off. Note that when the temperature is 12°C or 30°C, the program is not returned to the wait state. Refer to the state transition diagram in Figure 5.1 for more details.

Figure 5.1 State Transition Diagram



Note: The temperature can be displayed between 12 °C and 30 °C. When touch detection of touch button 1 is continuous (long press), the temperature increases up to 29 °C. To change the temperature from 29 °C to 30 °C, release the touch button once and then detect touch of touch button 1 again. When touch detection of touch button 2 is continuous (long press), the temperature decreases down to 13 °C. To change the temperature from 13 °C to 12 °C, release the touch button once and then detect touch of touch button 2 again.

## 5.2 File Composition

Table 5-1 lists the files used in the sample code. Note that the table does not include the files for the bsp environment, which are generated by the integrated development environment.

**Table 5-1 File Composition(1/2)**

| Folder and File Names | Description   | Smart Configurator Usage |
|-----------------------|---|--------------------------|
| ¥temp_demo<DIR>       | Sample code folder                                      |                          |
| Source folder         | Source folder   |                          |
| ¥temp_demo.c          | Temperature display demo source file                    |                          |
| ¥temp.c               | Temperature display source file                         |                          |
| ¥temp.h               | Temperature display header file                         |                          |
| ¥lcd_segdata.c        | LCD display source file                                 |                          |
| ¥smc_gen<DIR>         | Smart configurator generation components storage folder | ✓                        |
| ¥Config_INTC<DIR>     | External interrupt components folder                    | ✓                        |
| ¥Config_ITL000<DIR>   | ITL000 components folder                                | ✓                        |
| ¥Config_LCD<DIR>      | LCD components folder                                   | ✓                        |
| ¥Config_PORT<DIR>     | PORT components folder                                  | ✓                        |
| ¥Config_RTC<DIR>      | RTC components folder                                   | ✓                        |
| ¥Config_TAU0_3<DIR>   | TAU0_3 components folder                                | ✓                        |
| ¥Config_TAU0_5<DIR>   | TAU0_5 components folder                                | ✓                        |
| ¥r_ctsu<DIR>          | CTSU driver folder                                      | ✓                        |
| ¥rm_touch<DIR>        | TOUCH driver folder                                     | ✓                        |

Table 5-2 File Composition(2/2)

| Folder and File Names | Description   | Smart Configurator Usage |
|-----------------------|---|--------------------------|
| ¥temp_demo<DIR>       | Sample code folder                                  |                          |
| ¥qe_gen<DIR>          | QE for Capacitive Touch generation file             |                          |
| ¥qe_touch_config.c    | QE generation configuration source file             |                          |
| ¥qe_touch_config.h    | QE generation configuration header file             |                          |
| ¥qe_touch_define.h    | QE generation definition header file                |                          |
| ¥qe_touch_sample.c    | Main function source file including touch operation |                          |
| ¥QE-Touch<DIR>        | QE configuration folder                             |                          |

Precaution regarding Table 5-1 and Table 5-2.

Note: The sample code of the IAR version has a different configuration. Check the sample code of the IAR version for details. In addition, stores temp\_demo.ipcf. For details, refer to "RL78 Smart Configurator User's Guide: IAREW (R20AN0581)".

### 5.3 Smart Configurator Settings

Figure 5.2 shows the clock settings of the Smart Configurator.

Figure 5.2 Clock Settings

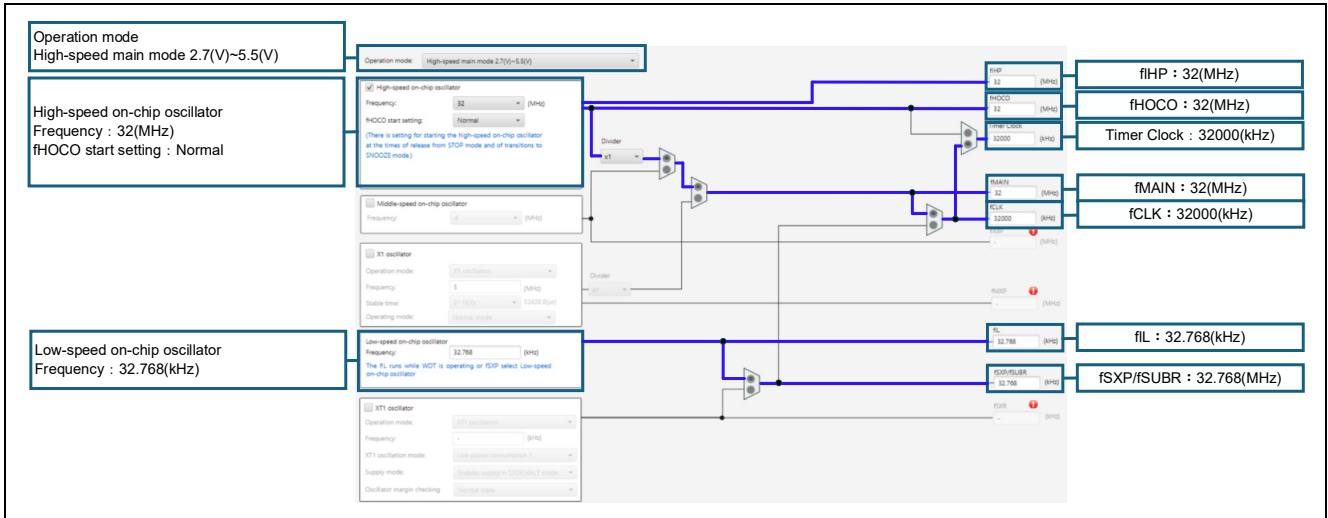


Figure 5.3 shows the system settings of the Smart Configurator.

Figure 5.3 System Settings

The screenshot displays the 'System configuration' window of the Smart Configurator. At the top right, there are buttons for 'Generate Code' and 'Generate Report'. The main area is titled 'On-chip debug setting' and contains several sections:

- On-chip debug operation setting:** Radio buttons for 'Unused', 'Use emulator', and 'COM Port' (selected).
- Emulator setting:** Radio buttons for 'E2' and 'E2 Lite' (selected).
- Pseudo-RRM/DMM function setting:** Radio buttons for 'Unused' and 'Used' (selected).
- Start/Stop function setting:** Radio buttons for 'Unused' (selected) and 'Used'.
- Monitoring point function setting:** Radio buttons for 'Unused' (selected) and 'Used'.
- Trace function setting:** Radio buttons for 'Unused' and 'Used' (selected).
- Security ID setting:** A checked checkbox for 'Use security ID' and a text input field containing '0x00000000000000000000'.
- Security ID authentication failure setting:** Radio buttons for 'Do not erase flash memory data' and 'Erase flash memory data' (selected).

At the bottom, a navigation bar includes tabs for 'Overview', 'Board', 'Clocks', 'System' (highlighted), 'Components', 'Pins', and 'Interrupt'.

Figure 5.4 shows the settings of LVD components (Config\_LVD0).

Figure 5.4 LVD Components (Config\_LVD0) Settings

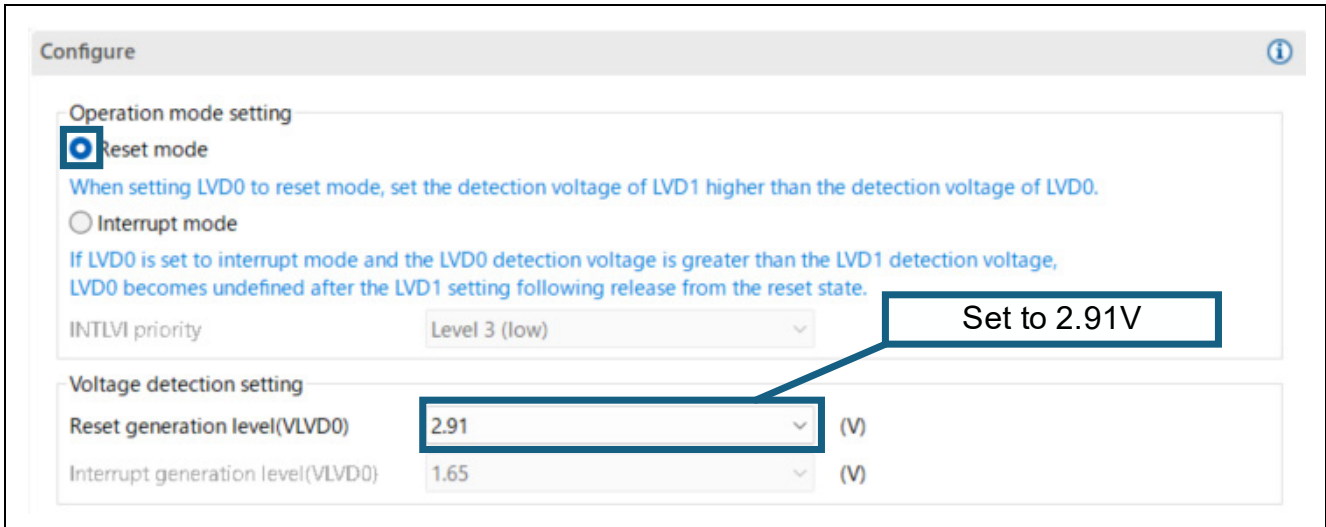


Figure 5.5 shows the settings of external interrupt components (Config\_INTC).

Figure 5.5 External Interrupt Components (Config\_INTC) Settings

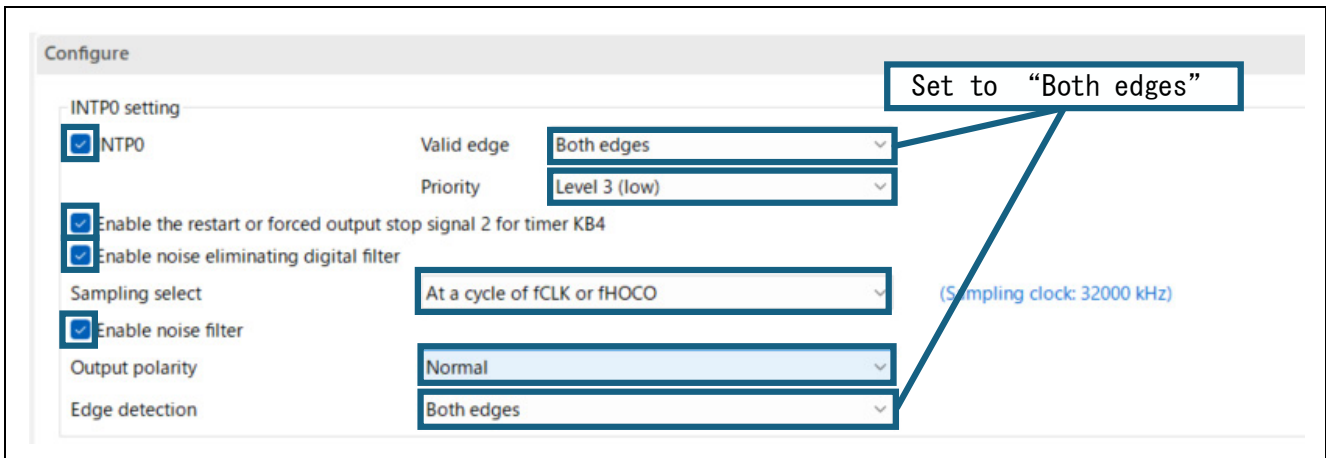


Figure 5.6 shows the settings of RTC components (Config\_RTC).

Figure 5.6 RTC Components (Config\_RTC) Settings

The screenshot shows the 'Configure' window for the RTC components. It is divided into several sections:

- Clock setting:** The 'Clock source' dropdown is set to 'Low-speed on-chip oscillator clock (fIL)'. A callout box points to this dropdown with the text 'Set to "Low-speed on-chip oscillator clock(fil)"'. The clock frequency is noted as 32.768 kHz.
- Real-time clock setting:** 'Hour-system selection' is set to '24-hour'. There are checkboxes for 'Set real-time clock initial value' and 'Enable output of RTC1HZ pin (1Hz)'. The initial date is 2000/01/01 and the time is 12:00:00.
- Alarm detection function setting:** There are checkboxes for 'Use alarm detection function' and 'Set alarm initial value'. The 'Week day' section has radio buttons for Sunday through Saturday. The 'Hour:Minute' field is set to 12:00. A callout box points to this field with the text 'Set to "Once per 1s"'. Note that this callout is positioned over the 'Once per 1 s' option in the interrupt setting section below.
- Interrupt setting:** The checkbox 'Used as constant-period interrupt function (INTRTC)' is checked. The frequency dropdown is set to 'Once per 1 s'. The 'Priority' dropdown is set to 'Level 3 (low)'.

Figure 5.7 shows the settings of TAU0\_3 components (Config\_TAU0\_3).

Figure 5.7 TAU0\_3 Components (Config\_TAU0\_3) Settings

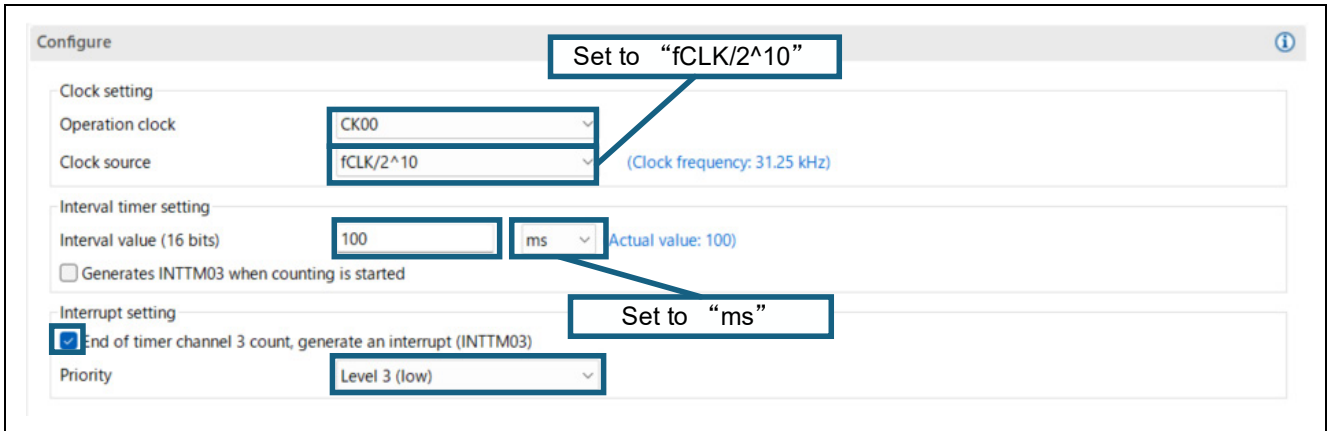


Figure 5.8 shows the settings of TAU0\_5 components (Config\_TAU0\_5).

Figure 5.8 TAU0\_5 Components (Config\_TAU0\_5) Settings

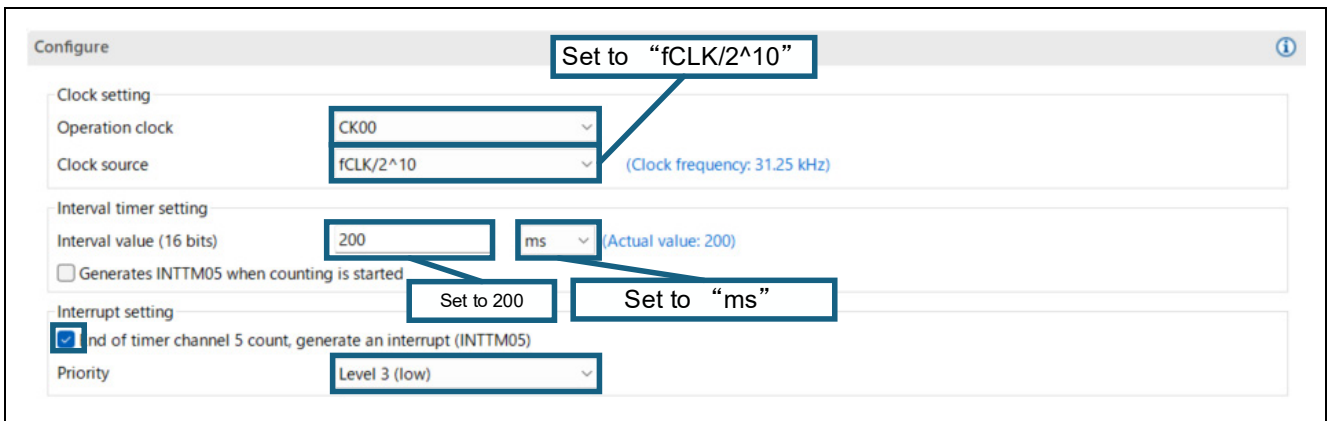


Figure 5.9 shows the settings of LCD components (Config\_LCD).

Figure 5.9 LCD Components (Config\_LCD) Settings

The screenshot shows the 'Configure' window for LCD components. The settings are as follows:

- Display waveform setting:** Type A waveform (selected), Type B waveform.
- Drive voltage generator setting:** Driving voltage generator method: Capacitor split method for the VDD reference.
- Display mode setting:** Static, Number of time slices: 4 (1/3 bias mode).
- Display data area setting:** Display data area selection: A-pattern area data.
- Alternation time selection:** Alternation in response to INTRTC.
- Control initial value of voltage boosting pin:** VDD >= 2.7 V (selected), VDD <= 4.2 V.
- Reference voltage setting:** VLCD voltage (VL1 Voltage): 1.01 V, VLCD voltage (VL2 Voltage): 2.02 V, VLCD voltage (VL4 Voltage): 3.03 V.
- Segment output pin setting:** A grid of checkboxes for segments SEG0/COM4 through SEG55. Segments SEG0, 1, 2, 3, 4, 5, 6, 7, 8, 15, 20, 35, 50, and 55 are checked.
- Clock setting:** Clock source: fil, Frequency divider: fil/2^7, Frame frequency: 64.000 Hz.

Callouts in the image indicate the following settings:

- 'Set to "Capacitor split method for the VDD reference"' points to the driving voltage generator method.
- 'Seto to "fil"' points to the clock source.
- 'Set to "fil/2^7"' points to the frequency divider.

Figure 5.10 and Figure 5.11 show the settings of PORT components (Config\_PORT).

Figure 5.10 PORT Components (Config\_PORT) Settings

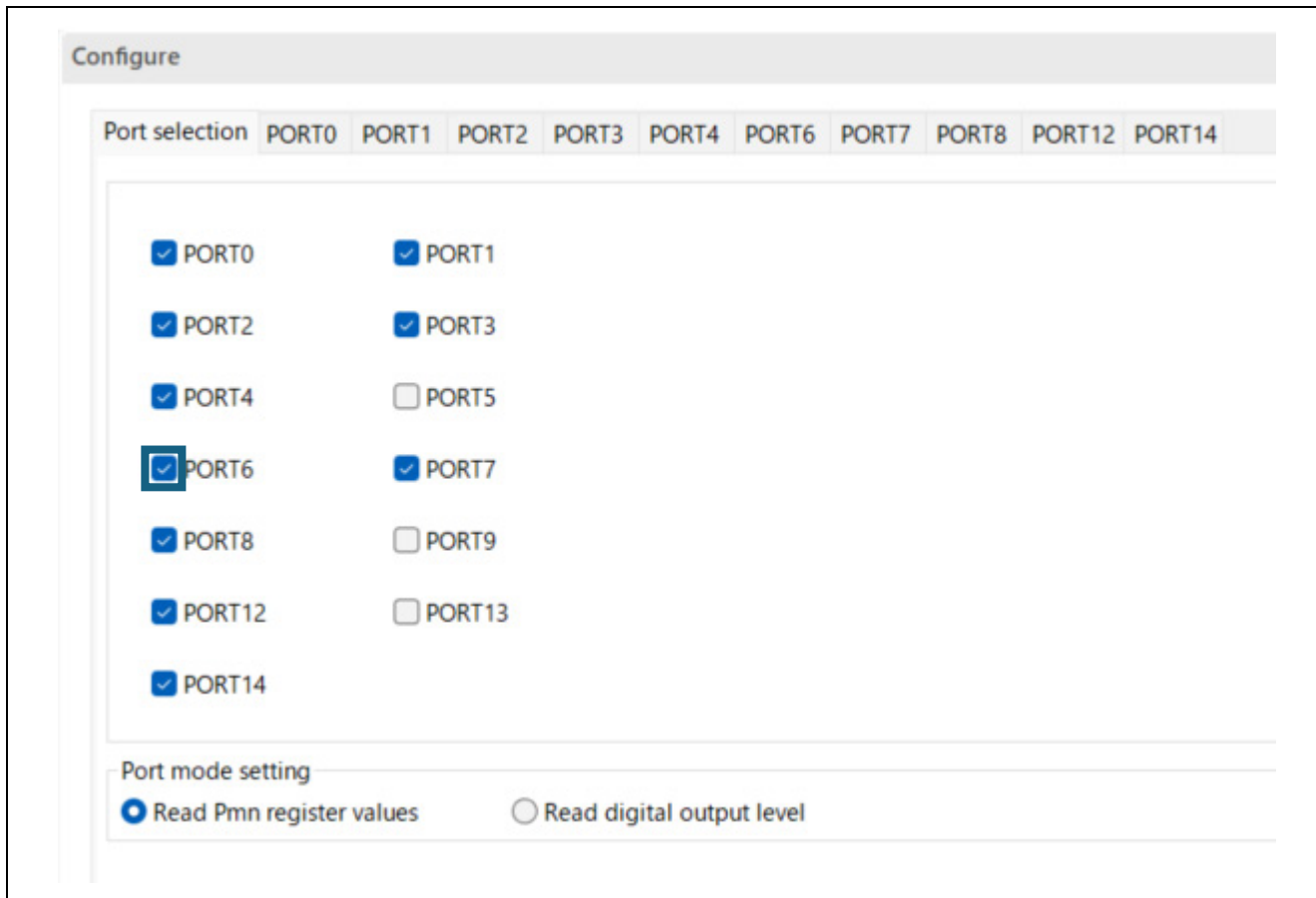
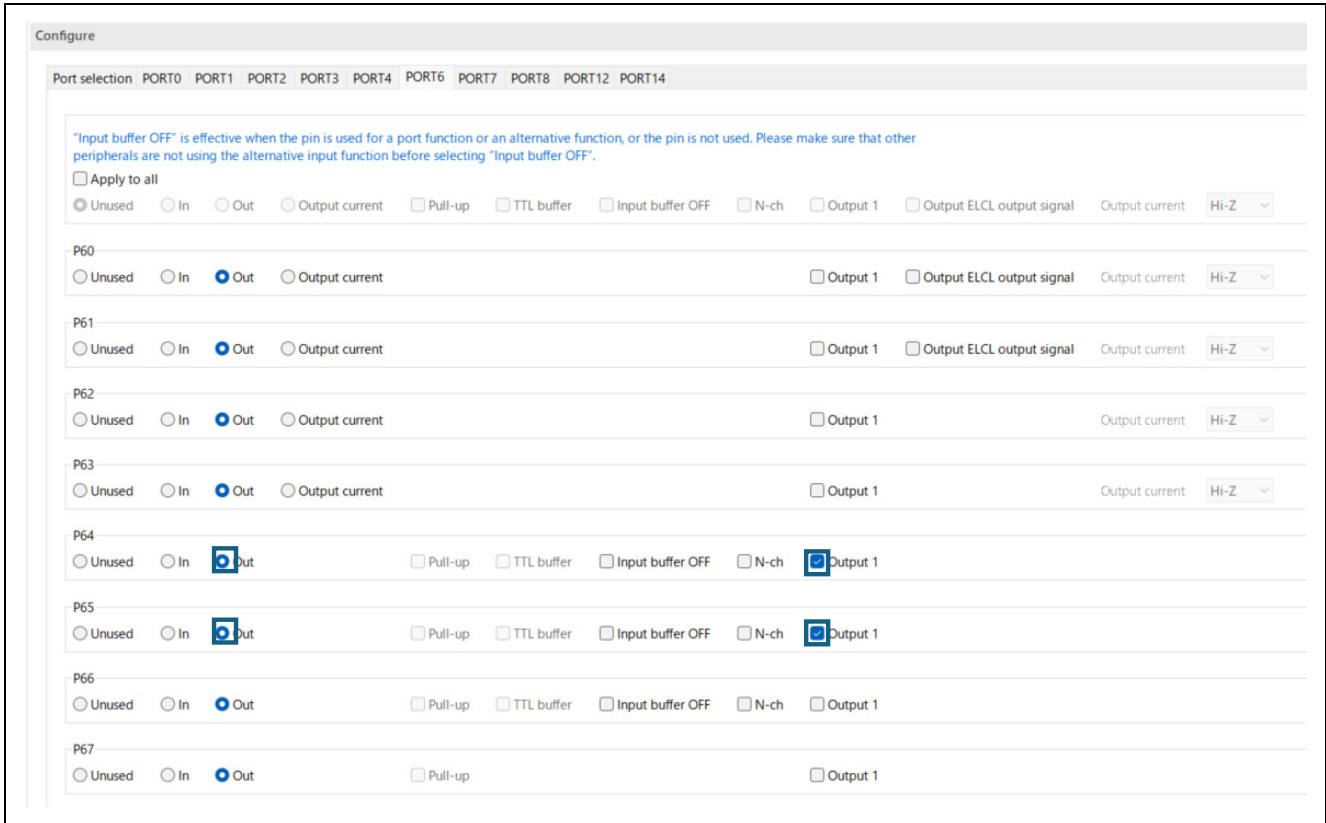


Figure 5.11 PORT6 Settings



Note: Only P64 and P65 are specified as LCD ports in this document. Other ports are set to prevent unnecessary current consumption caused by the floating state. Refer to 2.3 Connection of Unused Pins in RL78/L23 User's Manual: Hardware (R01UH1082) for details on how to properly handle the application's unused ports and design your application to meet the electric characteristics.

Figure 5.12 shows the CTSU driver (r\_ctsu) settings.

Figure 5.12 CTSU Driver (r\_ctsu) Settings

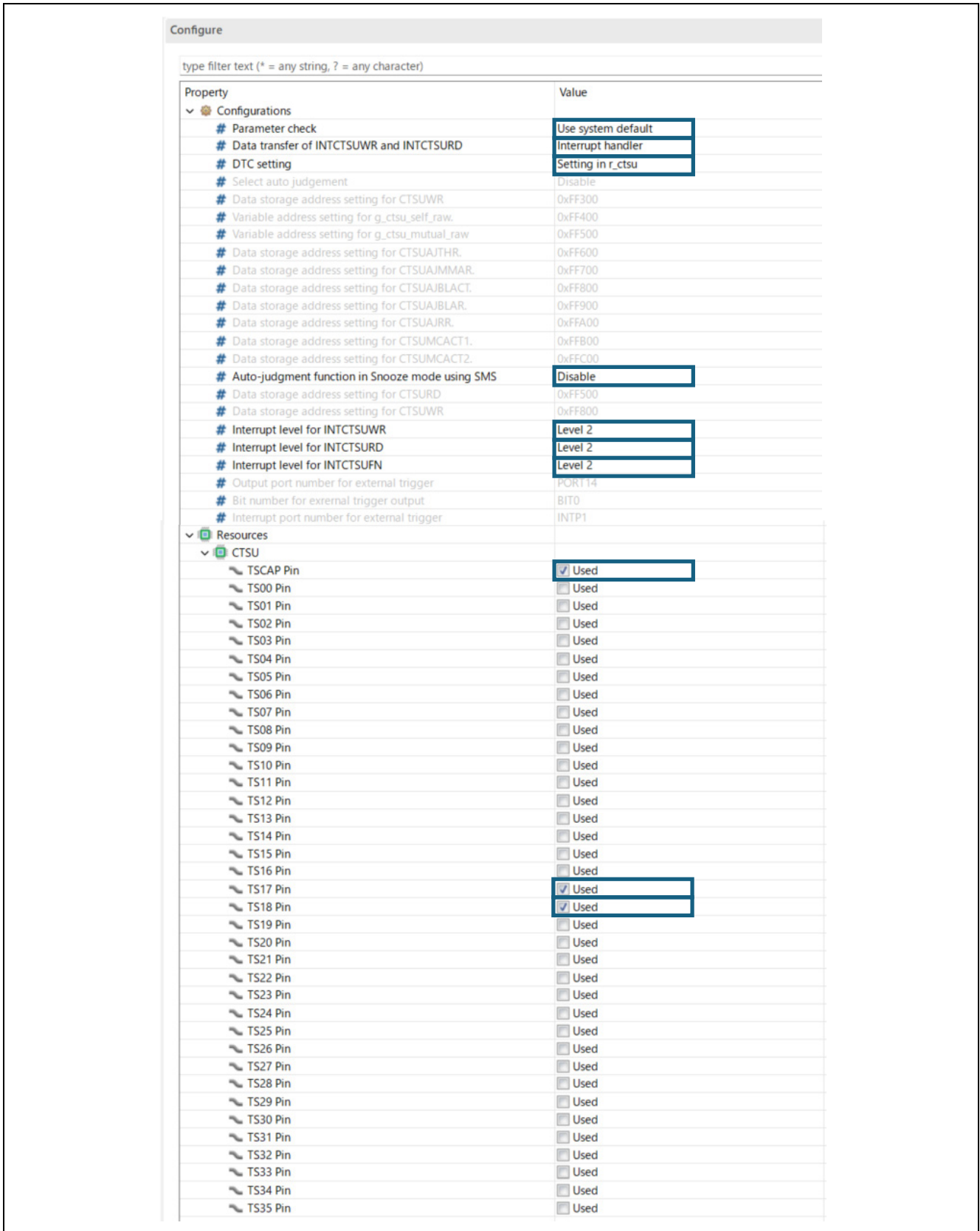



Figure 5.13 shows the settings of the TOUCH driver (rm\_touch).

**Figure 5.13 TOUCH Driver (rm\_touch) Settings**

| Configure  |   |
|--|---|
| type filter text (* = any string, ? = any character)   |   |
| Property   | Value   |
| <ul style="list-style-type: none"> <li>▼  Configurations</li> <li># Parameter check</li> <li># Support QE monitor using UART</li> <li># Support QE tuning using UART</li> <li># UART channel</li> <li># Type of chattering suppression</li> </ul> | <ul style="list-style-type: none"> <li>Use system default</li> <li>Disable</li> <li>Disable</li> <li>UART0</li> <li>TypeA : Counter of exceed threshold is hold within hysteresis range.</li> </ul> |

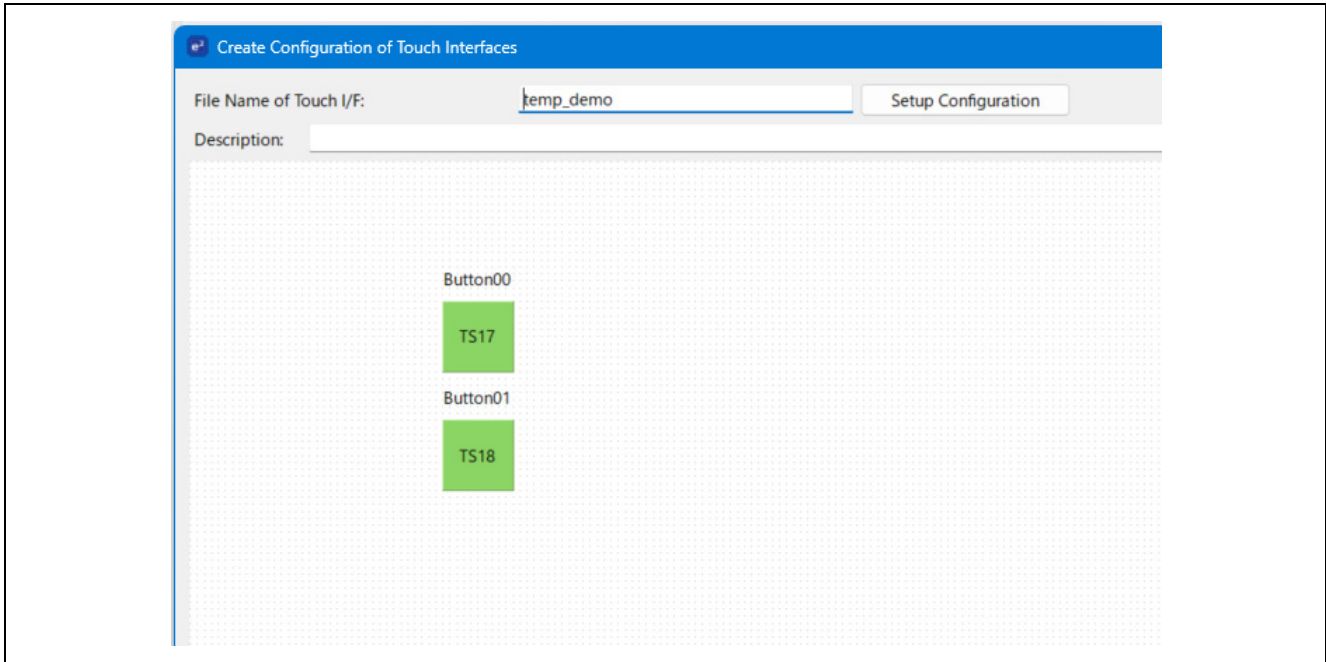
### 5.4 Capacitive Touch Settings

This section describes how to configure QE for Capacitive Touch.

#### 5.4.1 Touch Interface Configuration

Figure 5.14 shows the touch interface configuration. TS17 and TS18 are measured with the self-capacitance method.

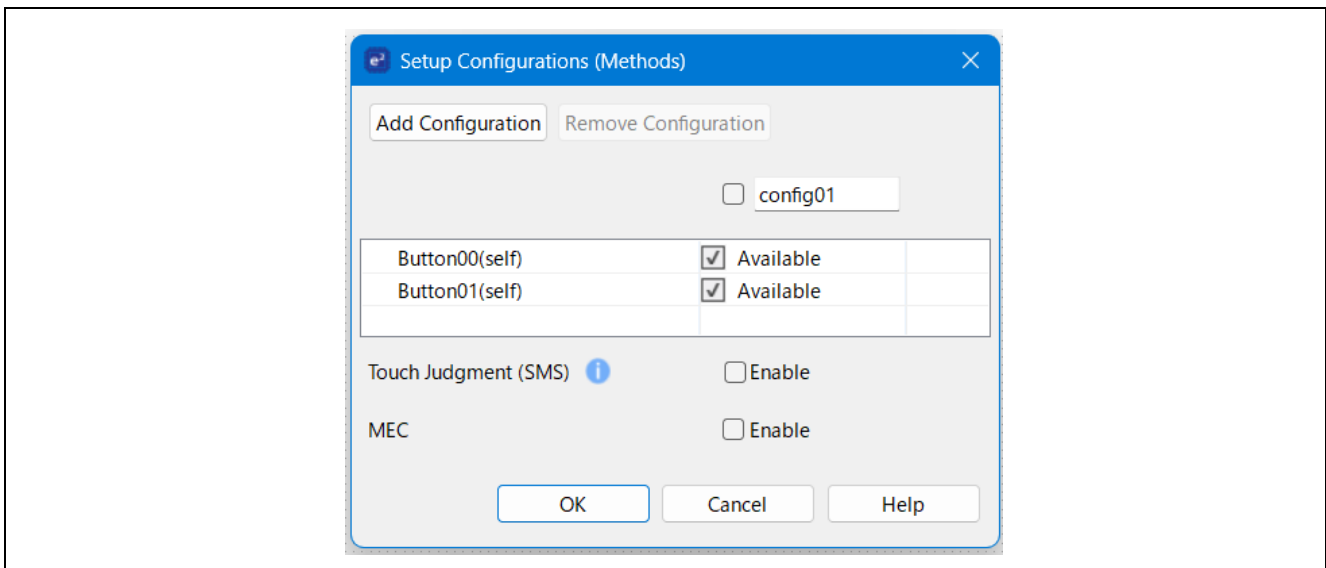
Figure 5.14 Touch Interface Configuration



#### 5.4.2 Configuration (Methods) Settings

Figure 5.15 shows the touch interface configuration (methods) settings. Touch judgment (SMS) is set to "Enable" to use auto judgment measurement with SMS.

Figure 5.15 Configuration (Methods) Settings



### 5.4.3 Tuning Results

Figure 5.16 shows the QE tuning results of the touch interface. The sample code operates with the setting values shown in the figure below.

The tuning results depend on the operating environment when QE tuning is performed. Thus, if QE tuning is performed again, these values may change.

**Figure 5.16 QE Tuning Results**

| Tuning                             |              | Gesture  |              |                           |                                   |           |               |          |  |
|------------------------------------|--------------|----------|--------------|---------------------------|-----------------------------------|-----------|---------------|----------|--|
| Touch I/F Configuration: temp_demo |              |          |              |                           |                                   |           |               |          |  |
| Method                             | Kind         | Name     | Touch Sensor | Parasitic Capacitance[pF] | Sensor Drive Pulse Frequency[MHz] | Threshold | Scan Time[ms] | Overflow |  |
| config01                           | Button(self) | Button00 | TS17         | 7.333                     | 5.534                             | 3432      | 0.576         | None     |  |
| config01                           | Button(self) | Button01 | TS18         | 7.062                     | 5.702                             | 3636      | 0.576         | None     |  |

## 5.5 Constants

Table 5-3 lists the constants used in the sample code.

**Table 5-3 Constants Used in Sample Code**

| Constant Name        | Setting Value | Description   |
|----------------------|---------------|---|
| MAX_VALUE            | 30            | Maximum value of temperature display                        |
| MIN_VALUE            | 12            | Minimum value of temperature display                        |
| DEFAULT_VALUE        | 20            | Default setting value of temperature display                |
| MAX_VALUE_CONTINUOUS | 29            | Maximum count value of continuous touch on the touch button |
| MIN_VALUE_CONTINUOUS | 13            | Minimum count value of continuous touch on the touch button |
| VALUE_SETTING_FINISH | 5             | Non-touch period (sec)                                      |
| CONTINUOUS_START_NUM | 3             | Continuous touch detection count                            |

## 5.6 Variables

Table 5-4 and Table 5-5 list the variables used in the sample code.

**Table 5-4 Variables Used in Sample Code (1/2)**

| Type    | Variable Name            | Contents  | Function Used  |
|---------|--------------------------|---|--|
| uint8_t | s_lcd_value              | LCD display value variable  | r_touch_minus_value<br>r_touch_plus_value<br>r_value_continuous<br>r_value_out_of_range<br>r_value_show  |
| uint8_t | s_100ms_count            | Variable to count wait time in 100ms intervals  | r_delay_100ms<br>r_tau_3_delay_callback  |
| uint8_t | s_prev_button1_flag      | Flag to store the previous state of touch button 1  | r_no_touch<br>r_touch_both_buttons<br>r_touch_plus_value   |
| uint8_t | s_prev_button2_flag      | Flag to store the previous state of touch button 2  | r_no_touch<br>r_touch_both_buttons<br>r_touch_minus_value  |
| uint8_t | s_standby_count_flag     | Flag to start count until transition to standby state.  | r_rtc_standby_callback<br>r_userswitch_callback<br>r_userswitch_release  |
| uint8_t | s_standby_count          | Variable to count until transition to standby state.  | r_rtc_standby_callback<br>r_touch_both_buttons<br>r_touch_minus_value<br>r_touch_plus_value<br>r_userswitch_callback                               |
| uint8_t | s_continuous_touch_flag  | Flag to determine interval of continuous touch  | r_no_touch<br>r_tau0_5_continuous_callback<br>r_touch_both_buttons<br>r_touch_minus_value<br>r_touch_plus_value                                    |
| uint8_t | s_continuous_touch_count | Variable to count continuous touch start  | r_tau0_5_continuous_callback<br>r_touch_minus_value<br>r_touch_plus_value  |
| uint8_t | g_touch_button_flag      | Flag to enable the touch function   | qe_touch_main<br>r_rtc_long_press_callback<br>r_userswitch_callback  |
| uint8_t | g_out_of_range_main_flag | When set to 1, indicates that the temperature data is out of range (12 or less, or 30 or more) and that the main system clock is running. | qe_touch_main<br>r_no_touch<br>r_rtc_standby_callback<br>r_touch_both_buttons<br>r_touch_normal_mode<br>r_value_continuous<br>r_value_out_of_range |
| uint8_t | g_touch_close_open_flag  | Flag to determine OPEN state of the touch function  | qe_touch_main  |
| uint8_t | g_digit_segdata[8][4]    | Array to store the LCD display digits   | r_lcd_show   |

Table 5-5 Variables Used in Sample Code (2/2)

| Type    | Variable Name            | Contents  | Function Used                                 |
|---------|--------------------------|---|---|
| uint8_t | s_userswitch_on_off_flag | Flag to determine ON/OFF state of the user switch | r_userswitch_release<br>r_userswitch_callback |
| uint8_t | g_show_segdata[40][4]    | Array to store characters for display on the LCD  | r_lcd_show                                    |

## 5.7 Functions

Table 5-6 lists functions used in the sample code (temp.c).

**Table 5-6 Functions Used in Sample Code (temp.c)**

| Function Name   | Outline   |
|---|---|
| void r_temp_demo_start (void);  | Temperature demo start processing                                     |
| void r_userswitch_release (void);                                       | User switch detection processing                                      |
| void r_userswitch_callback (void);                                      | Processing for user switch press                                      |
| void r_rtc_standby_callback (void);                                     | Callback for RTC interrupt  |
| void r_tau0_3_delay_callback (void);                                    | Callback for TAU0_3 interrupt   |
| void r_tau0_5_continuous_callback (void);                               | Callback for TAU0_5 interrupt   |
| void r_touch_plus_value(void);  | Callback for touch button 1   |
| void r_touch_minus_value(void);   | Callback for touch button 2   |
| void r_touch_both_buttons(void);  | Processing for simultaneous touch detection of touch buttons 1 and 2  |
| void r_no_touch(void);  | Touch release detection processing                                    |
| static void r_lcd_show (uint8_t value, uint8_t digit);                  | Display of LCD values   |
| static void r_value_show (void);  | Display of current set temperature                                    |
| static void r_array_show (uint8_t * array, uint8_t num, uint8_t delay); | Sequential display processing of array data on LCD                    |
| static void r_clear_show (void);  | Clearing of LCD display   |
| static void r_delay_100ms (uint8_t num);                                | Delay processing  |
| static void r_value_out_of_range (void);                                | Out-of-range temperature display processing                           |
| static void r_value_continuous (void);                                  | Temperature check during continuous touch detection of a touch button |

## 5.8 Function Specifications

The following tables list the sample code (temp.c) function specifications.

### r\_temp\_demo\_start

---

|                     |   |
|---------------------|---|
| <b>Outline</b>      | Temperature demo start processing                                   |
| <b>Header</b>       | temp.h  |
| <b>Declaration</b>  | void r_temp_demo_start (void);                                      |
| <b>Description</b>  | Displays "RL78/L23" and "TMP DEMO" on the LCD when the demo starts. |
| <b>Arguments</b>    | None  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | None  |

### r\_userswitch\_release

---

|                     |   |
|---------------------|---|
| <b>Outline</b>      | User switch detection processing  |
| <b>Header</b>       | temp.h  |
| <b>Declaration</b>  | void r_userswitch_release (void);                                       |
| <b>Description</b>  | Changes the corresponding flag according to release of the user switch. |
| <b>Arguments</b>    | None  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | None  |

### r\_userswitch\_callback

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Processing for user switch press             |
| <b>Header</b>       | temp.h                                       |
| <b>Declaration</b>  | void r_userswitch_callback (void);           |
| <b>Description</b>  | Enables the touch button with a short press. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

### r\_rtc\_standby\_callback

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Callback for RTC interrupt                               |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | void r_rtc_standby_callback(void);                       |
| <b>Description</b>  | Determines the interval to transition to the wait state. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

### r\_tau0\_3\_delay\_callback

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Callback for TAU0_3 interrupt  |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | void r_tau0_3_delay_callback (void);                                 |
| <b>Description</b>  | Increments the delay count variable with the timer (TAU0 channel 3). |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

---

r\_tau0\_5\_continuous\_callback

---

|                     |   |
|---------------------|---|
| <b>Outline</b>      | Callback for TAU0_5 interrupt                   |
| <b>Header</b>       | temp.h  |
| <b>Declaration</b>  | void r_tau0_5_continuous_callback (void);       |
| <b>Description</b>  | Determines continuous touch on the touch button |
| <b>Arguments</b>    | None  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | None  |

---

r\_touch\_plus\_value

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Callback for touch button 1  |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | void r_touch_plus_value(void);   |
| <b>Description</b>  | Adds 1 to the LCD display when touch detection of touch button 1 occurs. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

---

r\_touch\_minus\_value

---

|                     |   |
|---------------------|---|
| <b>Outline</b>      | Callback for touch button 2   |
| <b>Header</b>       | temp.h  |
| <b>Declaration</b>  | void r_touch_minus_value(void);   |
| <b>Description</b>  | Subtracts 1 from the LCD display when touch detection of touch button 2 occurs. |
| <b>Arguments</b>    | None  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | None  |

---

r\_touch\_both\_buttons

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Processing for simultaneous touch detection of touch buttons 1 and 2         |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | void r_touch_both_buttons(void);   |
| <b>Description</b>  | Resets the standby counter, releases the continuous touch, and stops TAU0_5. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

---

r\_no\_touch

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Touch release detection processing   |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | void r_no_touch(void);   |
| <b>Description</b>  | Initializes the continuous touch detection and stops the long press timer when no touch detection of the touch buttons occurs. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |



---

r\_value\_out\_of\_range

---

|                     |  |
|---------------------|--|
| <b>Outline</b>      | Out-of-range temperature display processing                                  |
| <b>Header</b>       | temp.h   |
| <b>Declaration</b>  | static void r_value_out_of_range (void);                                     |
| <b>Description</b>  | Determines if the value is out of range and performs processing accordingly. |
| <b>Arguments</b>    | None   |
| <b>Return Value</b> | None   |
| <b>Remarks</b>      | None   |

---

r\_value\_continuous

---

|                     |   |
|---------------------|---|
| <b>Outline</b>      | Temperature check during continuous touch detection of a touch button                     |
| <b>Header</b>       | temp.h  |
| <b>Declaration</b>  | static void r_value_continuous (void);  |
| <b>Description</b>  | Determines the upper/lower limit of continuous touch and performs processing accordingly. |
| <b>Arguments</b>    | None  |
| <b>Return Value</b> | None  |
| <b>Remarks</b>      | None  |

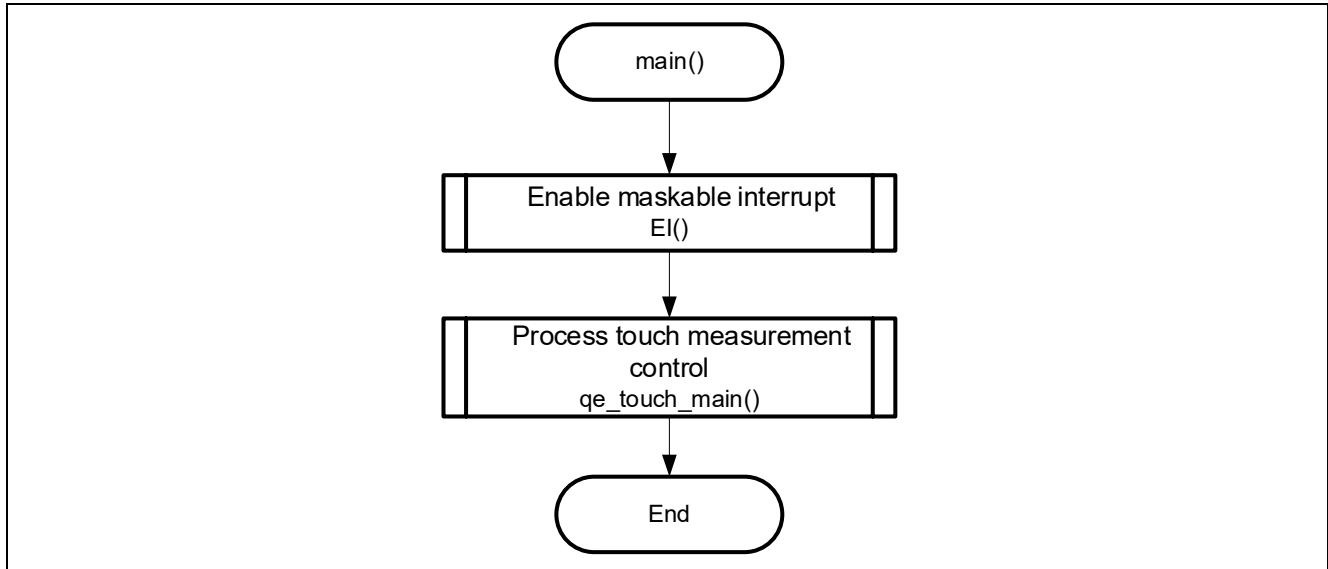
## 5.9 Flowcharts

The flowchart of the main functions showing the flow of this sample code is shown below.

### 5.9.1 main Function

Figure 5.17 shows the main function flowchart.

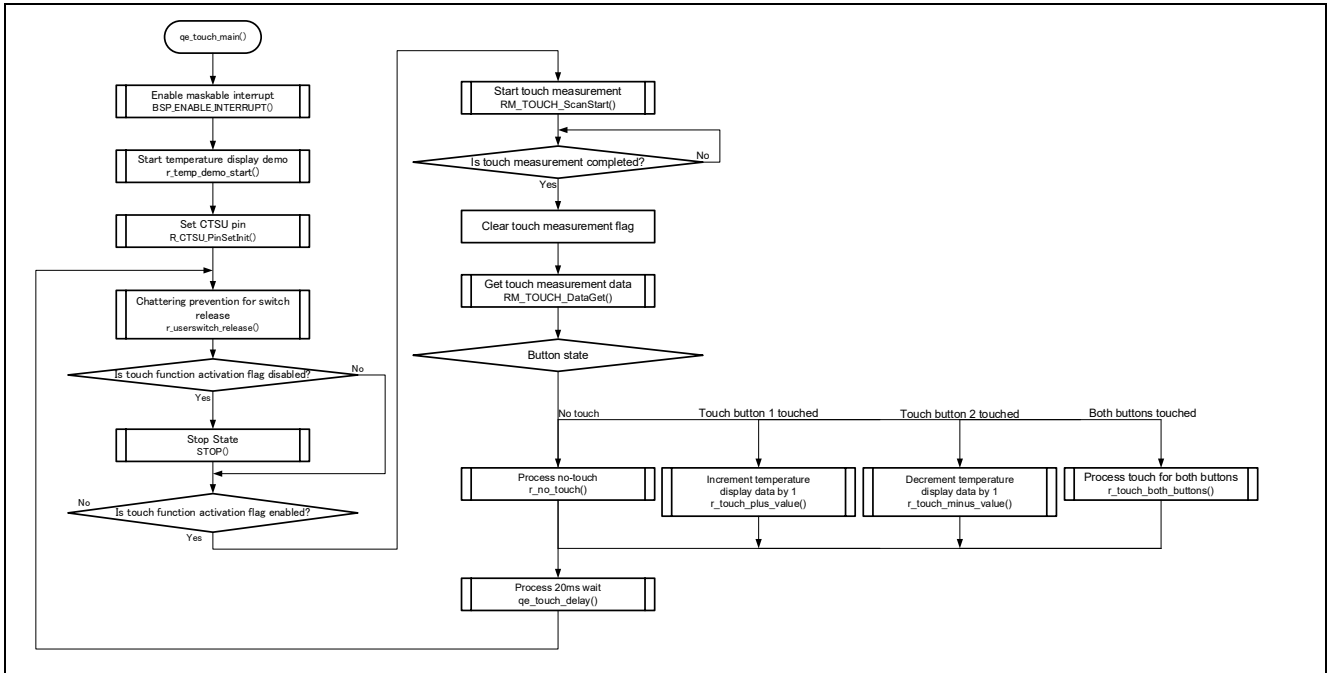
**Figure 5.17** main Function Flowchart



5.9.2 qe\_touch\_main Function

Figure 5.18 shows the qe\_touch\_main function flowchart.

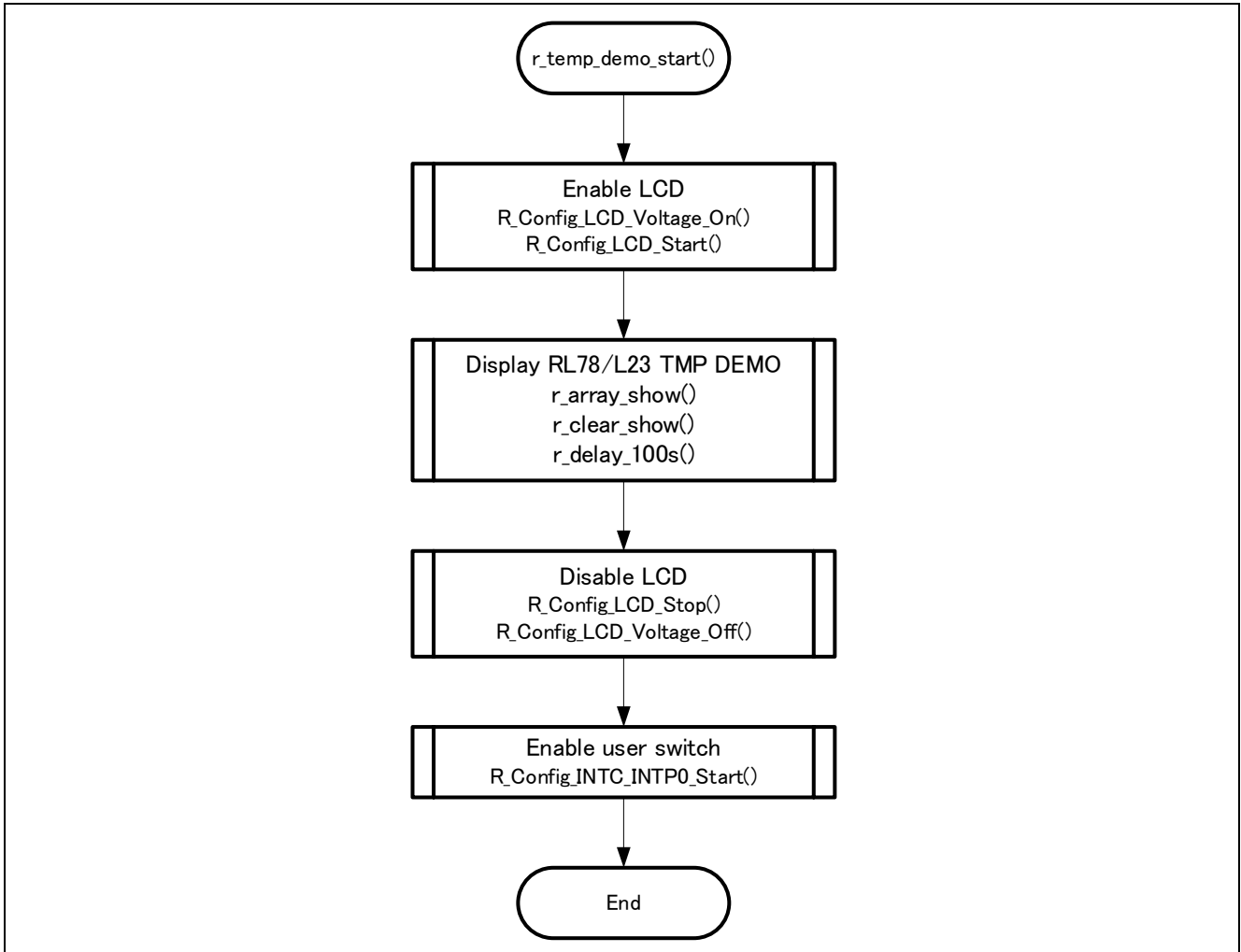
Figure 5.18 qe\_touch\_main Function Flowchart



5.9.3 r\_temp\_demo\_start Function

Figure 5.19 shows the r\_temp\_demo\_start function flowchart.

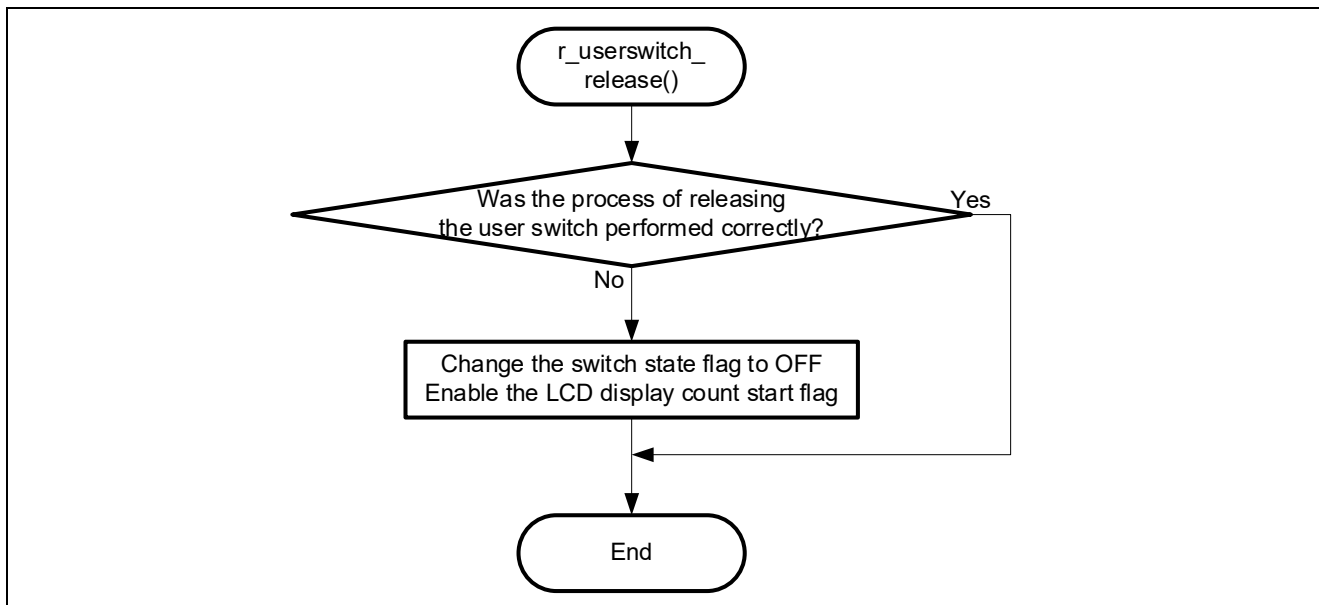
Figure 5.19 r\_temp\_demo\_start Function Flowchart



5.9.4 r\_userswitch\_release Function

Figure 5.20 shows the r\_userswitch\_release function flowcharts.

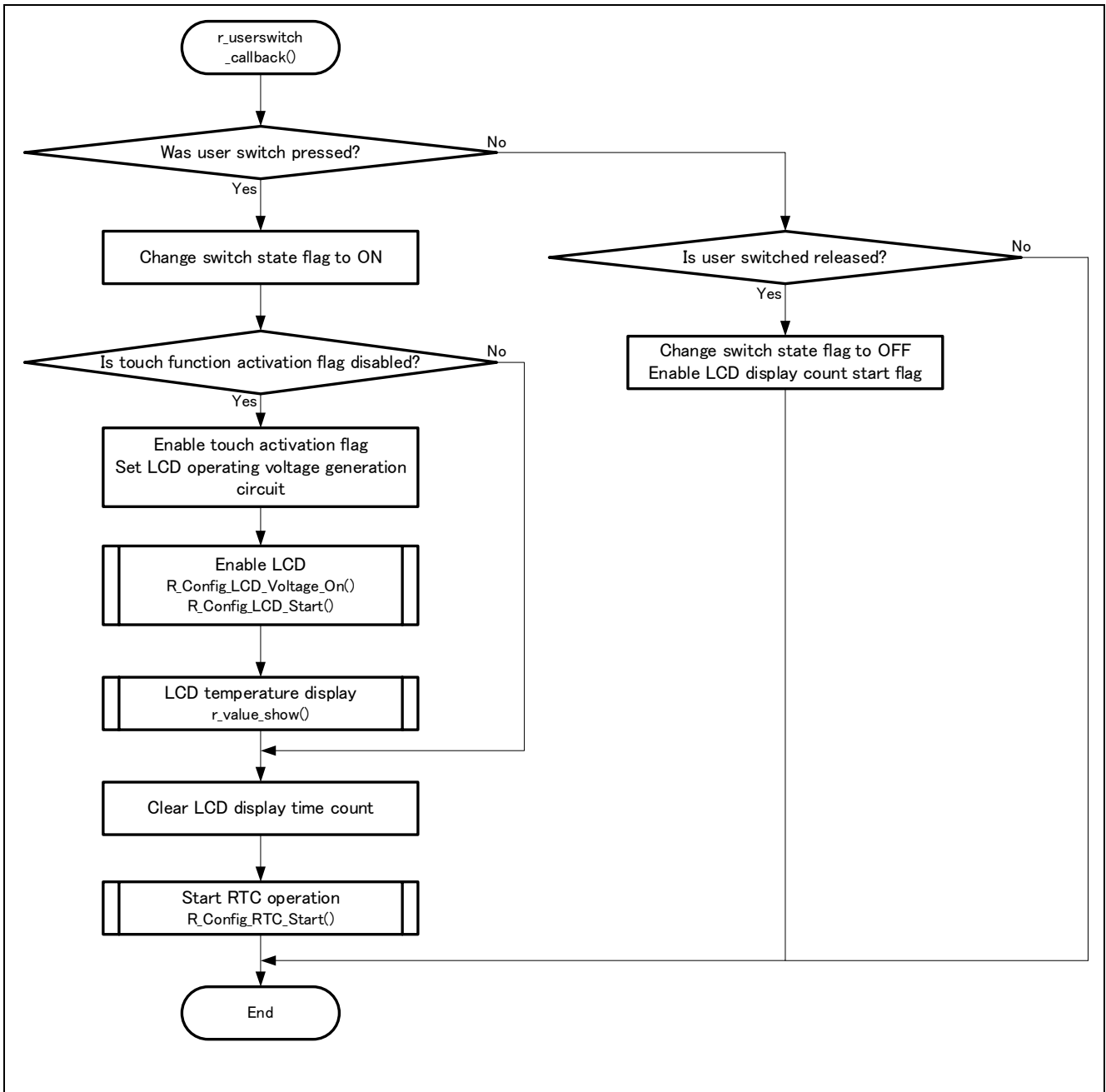
Figure 5.20 r\_userswitch\_release function flowchart



5.9.5 r\_userswitch\_callback Function

Figure 5.21 shows the r\_userswitch\_callback function flowcharts.

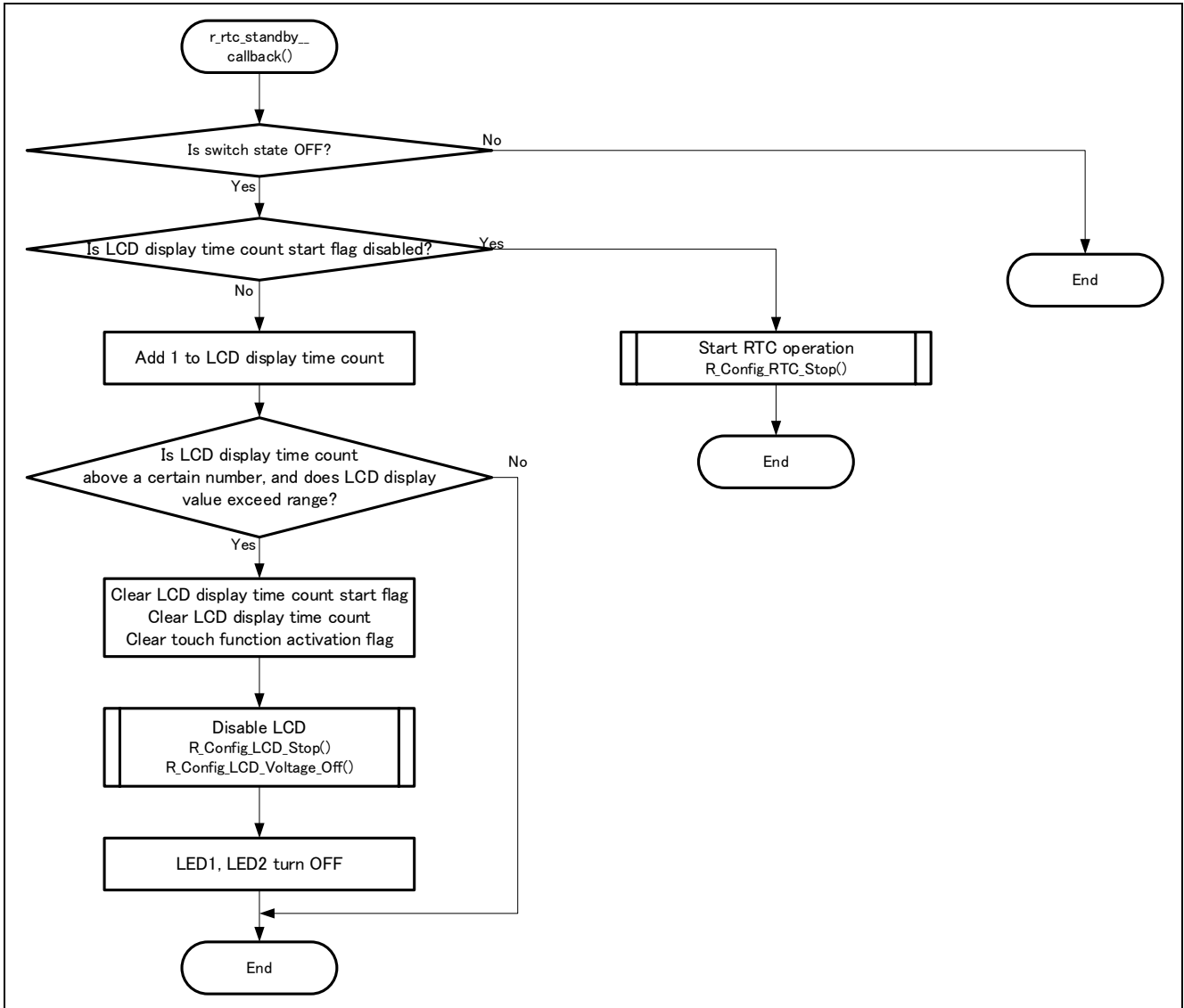
Figure 5.21 r\_userswitch\_callback Function Flowchart



5.9.6 r\_rtc\_standby\_callback Function

Figure 5.22 shows the r\_rtc\_standby\_callback function flowchart.

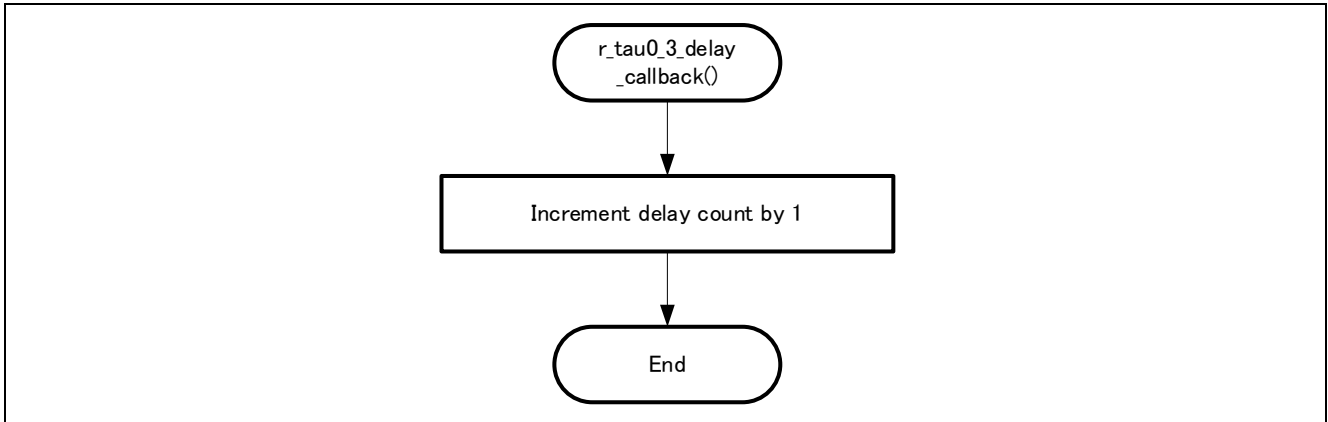
Figure 5.22 r\_rtc\_standby\_callback Function Flowchart



5.9.7 r\_tau0\_3\_delay\_callback Function

Figure 5.23 shows the r\_tau0\_3\_delay\_callback function flowchart.

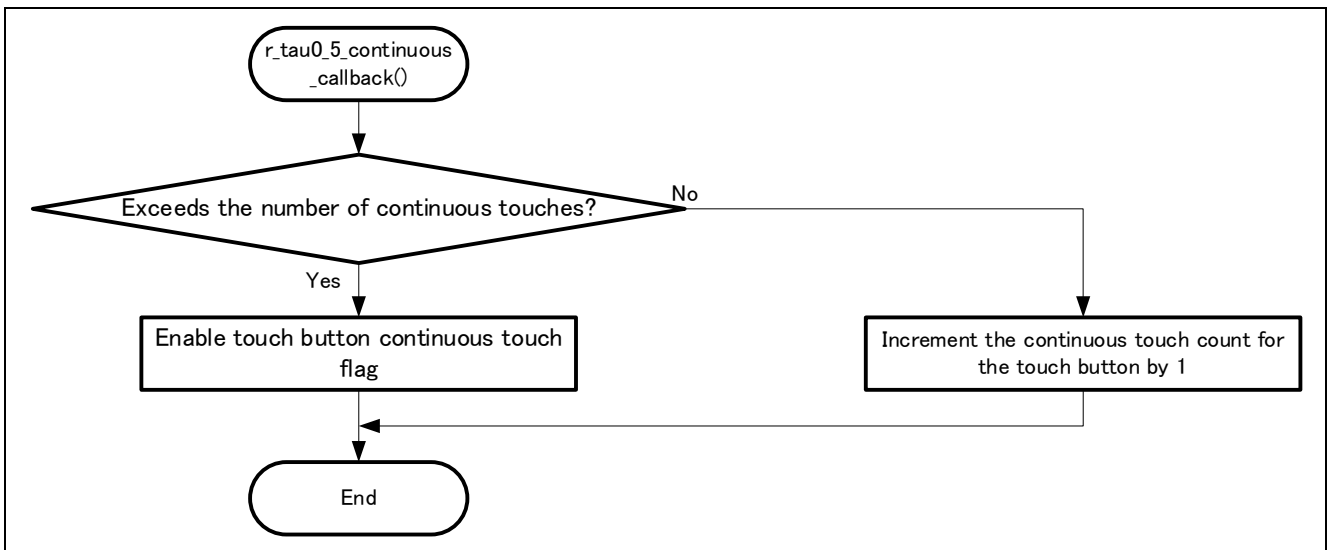
Figure 5.23 r\_tau0\_3\_delay\_callback Function Flowchart



5.9.8 r\_tau0\_5\_continuous\_callback Function

Figure 5.24 shows the r\_tau0\_5\_continuous\_callback function flowchart.

Figure 5.24 r\_tau0\_5\_continuous\_callback Function Flowchart



## 6. Importing the Project

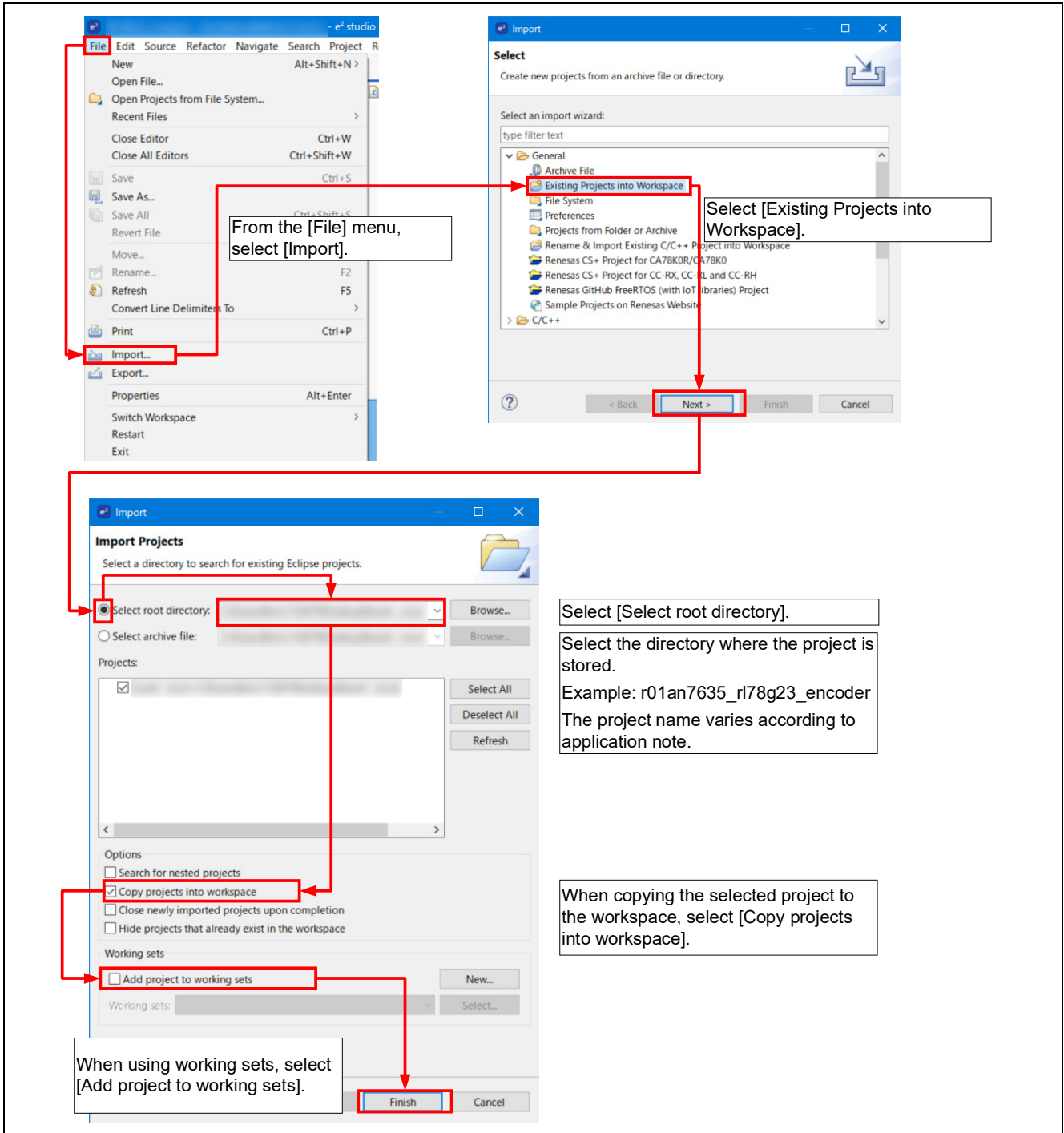
### 6.1 Importing with e<sup>2</sup> studio

When using the sample code with e<sup>2</sup> studio, use the following procedure to import the project in e<sup>2</sup> studio.

Note that spaces and symbols (especially \$, #, and %) cannot be included in e<sup>2</sup> studio project folder names or path to these folders.

(Dialog boxes shown in the figure may differ depending on the e<sup>2</sup> studio version used.)

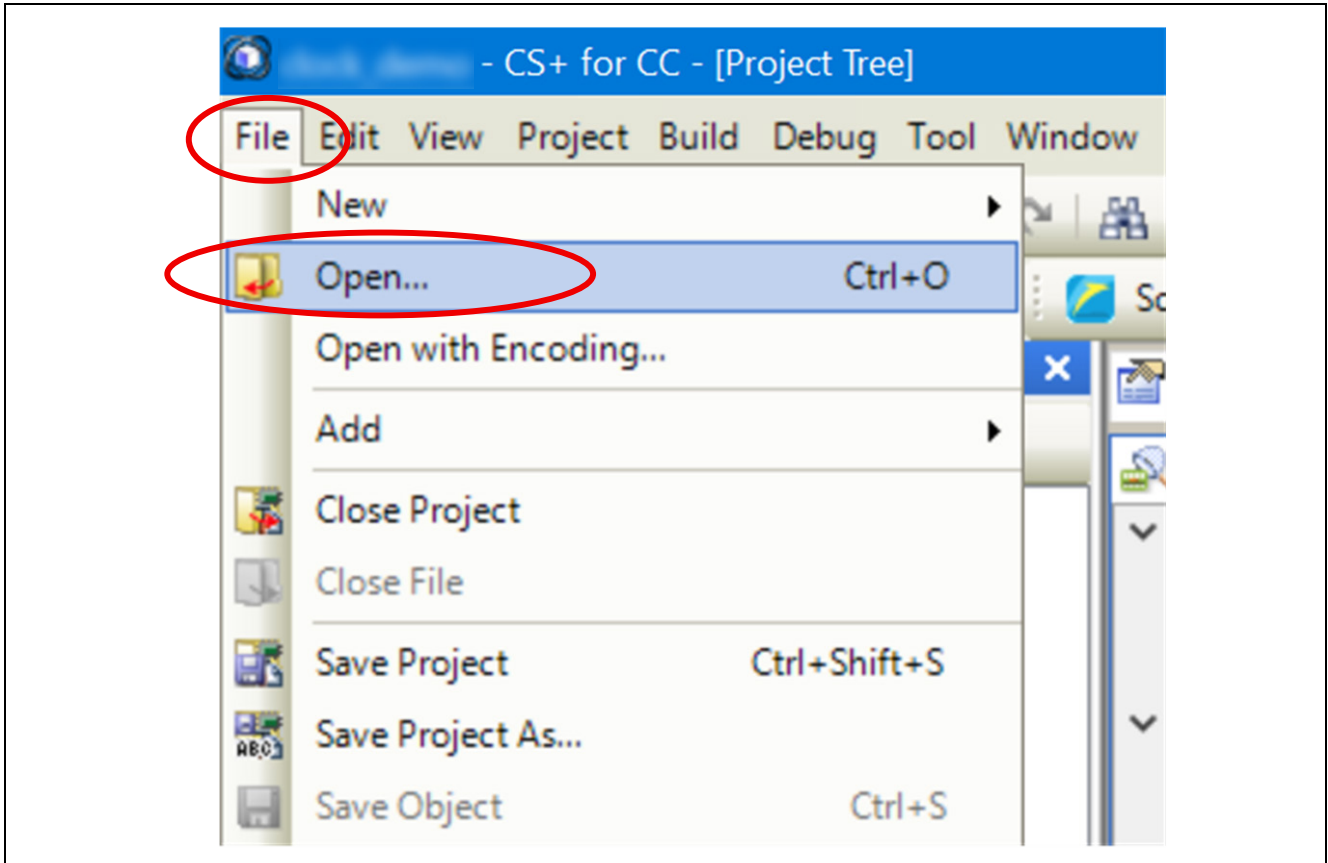
Figure 6.1 Importing the Project into e<sup>2</sup> studio



## 6.2 Importing with CS+

When using the sample code with CS+, open the mtpj file of the project by selecting [File] – [Open...] menu.  
(The image shown in the figure may differ depending on the CS+ version used.)

Figure 6.2 How to Import a Project to CS+

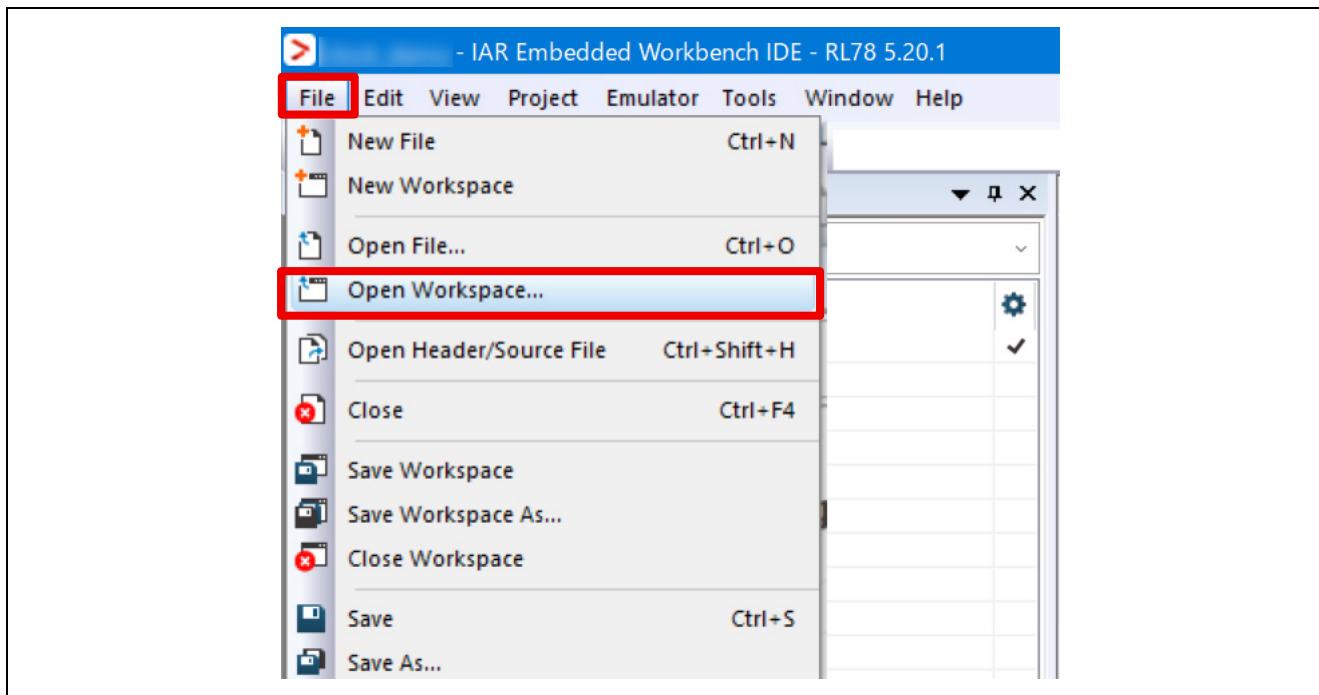


### 6.3 Importing with IAR

When using the sample code with IAR, open the eww file of the project by selecting [File] – [Open Workspace...] menu.

(The image shown in the figure may differ depending on the IAR version used.)

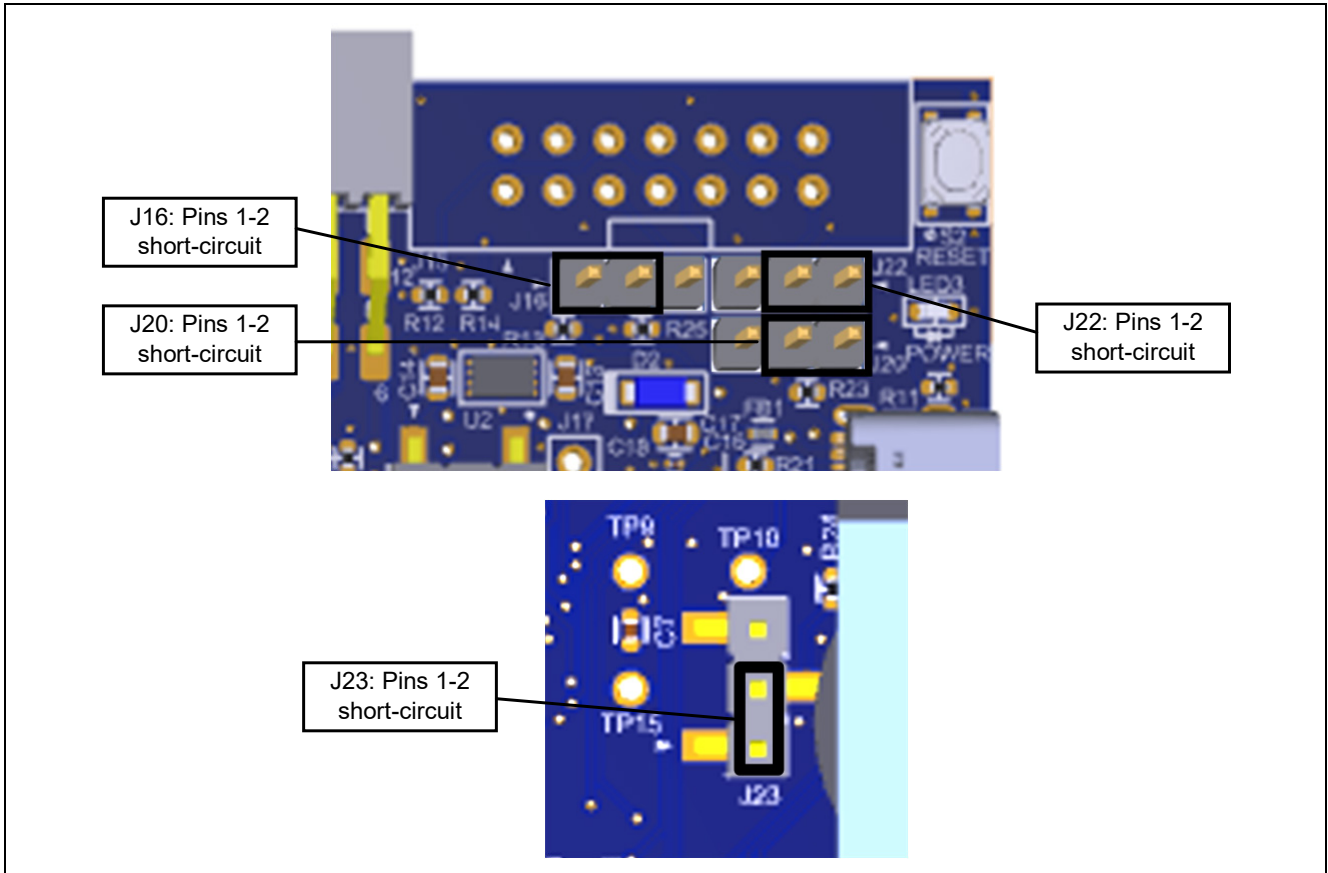
**Figure 6.3** How to Import a Project to IAR



### 7. Setting the Debug Tool

In the sample code, the USB-C port of RL78/L23 Fast Prototyping Board (RTK7RLL230S000001BJ) is used to perform debugging via the COM Port. When debugging via the COM port, make sure that jumper pins of RL78/L23 are configured as shown in Figure 7.1. For details, refer to 5.15 USB-to-Serial Converter and 5.21 Emulator Connector in RL78/L23 Fast Prototyping Board User's Manual (R20UT5544).

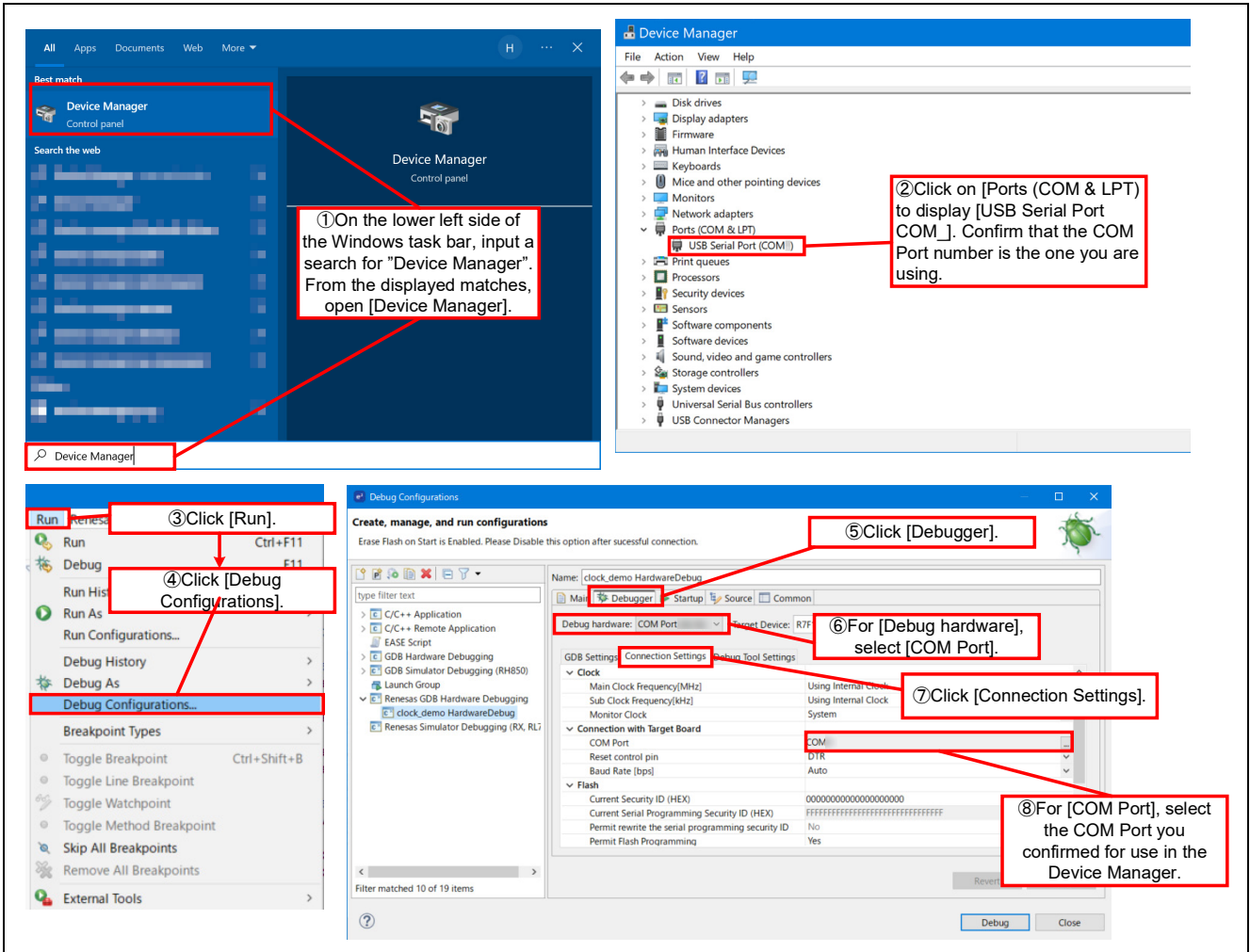
Figure 7.1 COM Port Setting When Using Debug



### 7.1 COM port Setting with e<sup>2</sup> studio

Connect the PC and RL78/L23 with a USB-C cable, then select a COM Port for debugging as shown in Figure 7.2.

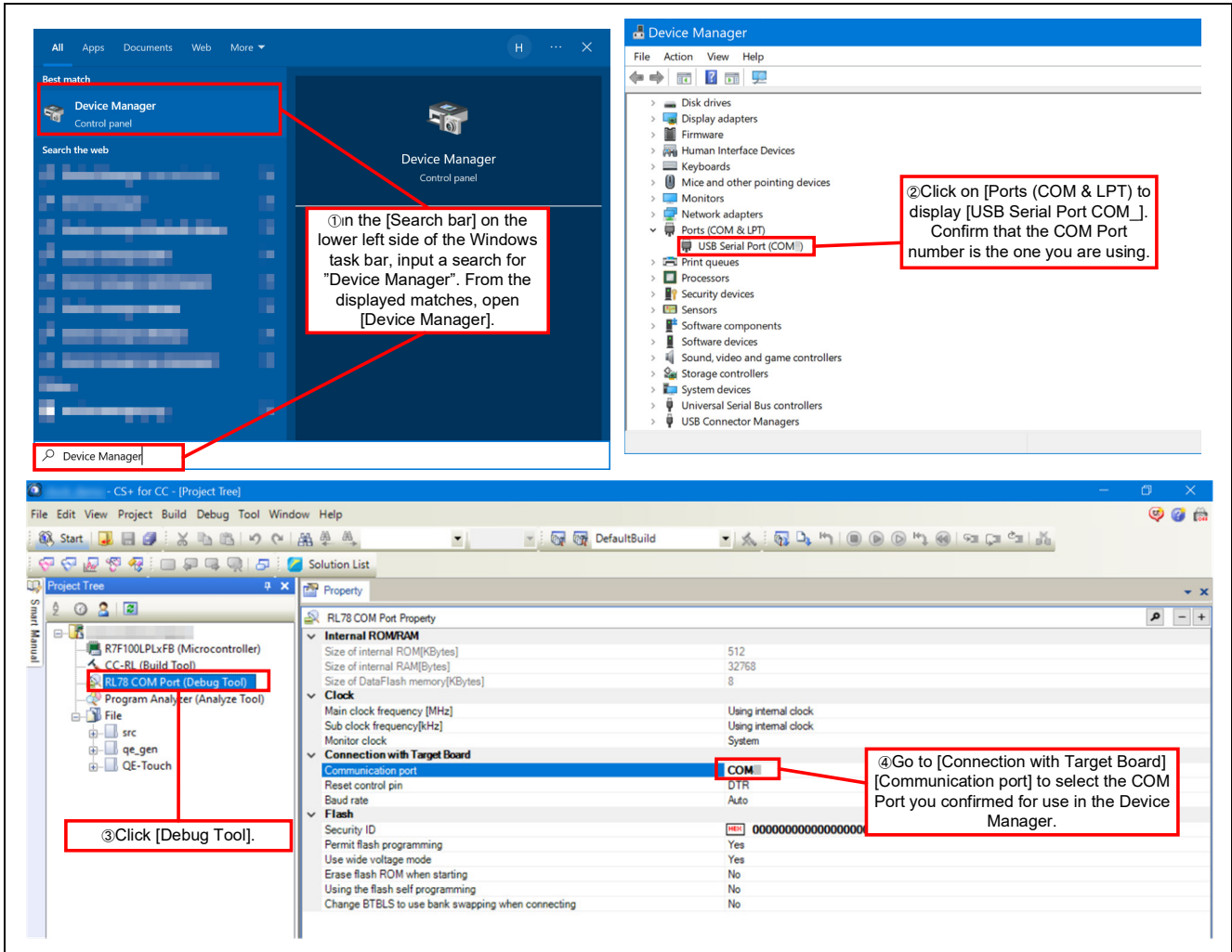
Figure 7.2 COM Port Setting with e<sup>2</sup> studio



## 7.2 COM Port Setting with CS+

Connect the PC and RL78/L23 with a USB-C cable, then select a COM Port for debugging as shown in Figure 7.3.

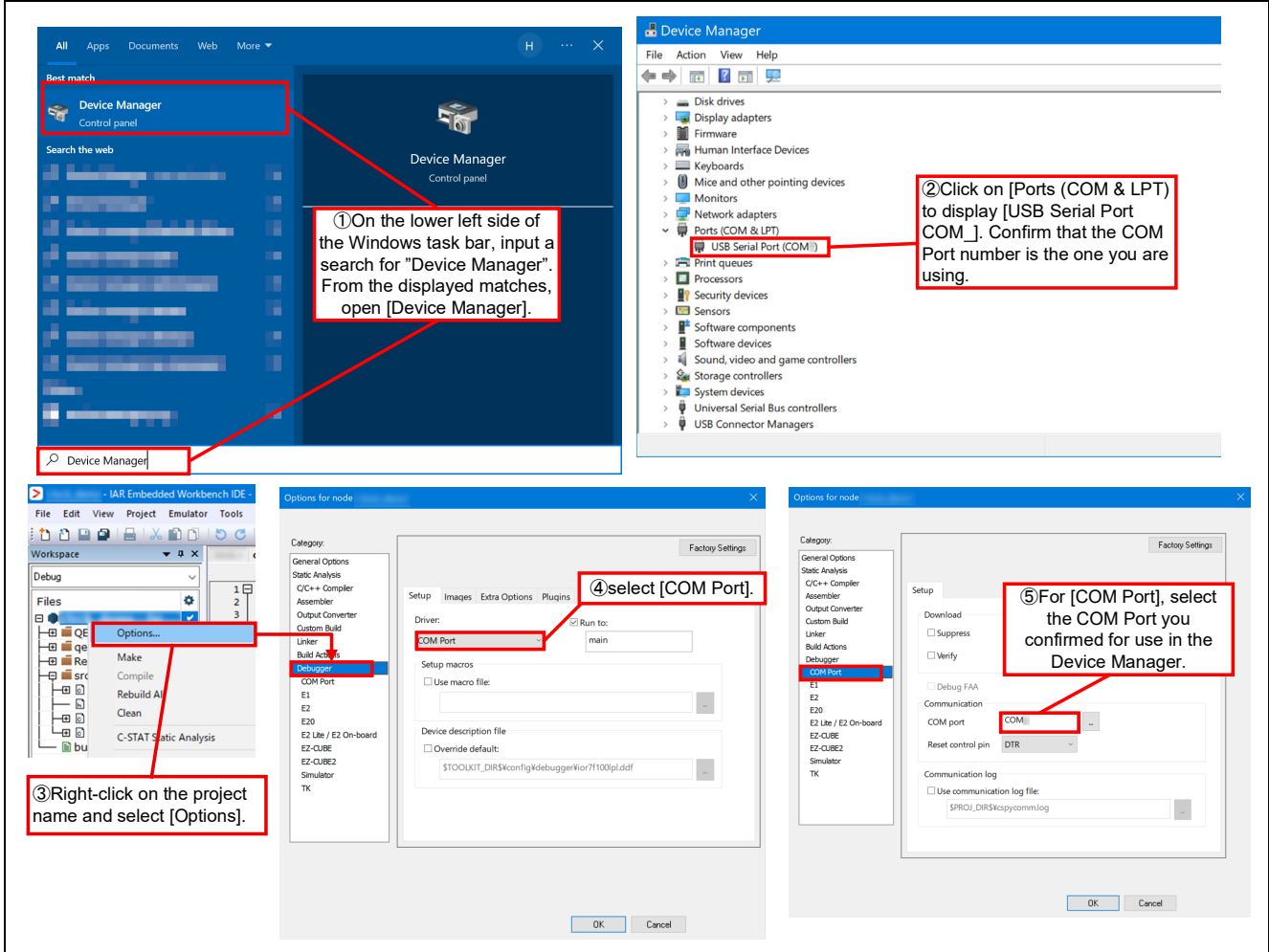
Figure 7.3 COM Port Setting with CS+



### 7.3 COM Port Setting with IAR

Connect the PC and RL78/L23 with a USB-C cable, then select a COM Port for debugging as shown in Figure 7.4.

Figure 7.4 COM Port Setting with IAR



## 8. Sample Code

Sample code can be downloaded from the Renesas Electronics website.

## 9. Reference Documents

RL78/L23 User's Manual: Hardware (R01UH1082)

RL78 Family User's Manual: Software (R01US0015)

RL78/L23 Fast Prototyping Board User's Manual (R20UT5544)

Capacitive Sensor Microcontrollers CTSU Capacitive Touch Introduction Guide (R30AN0424)

RL78 Smart Configurator User's Guide: IAREW (R20AN0581)

The latest information can be downloaded from the Renesas Electronics website.

Technical Update/Technical News

The latest information can be downloaded from the Renesas Electronics website.

## REVISION HISTORY

| Rev. | Date  | Description |  |
|------|---|-------------|--|
|      |   | Page        | Summary  |
| 1.00 | Aug.18.25   | -           | First edition issued   |
| 1.10 | Oct.24.25   | 1           | Abstract<br>Deleted description of SMS.  |
|      |   | 4           | 1 Specifications<br>Deleted description of SMS.  |
|      |   | 4           | Deleted the Item of 32-bit interval timer from Table 1-1.<br>Deleted the description of SMS from the item of Capacitive Touch Sensing Unit from Table 1-1. |
|      |   | 6           | 2. Operation Confirmation Conditions<br>Added description of the IAR environment.  |
|      |   | 21          | 4.3 Capacitive Touch Sensing Unit<br>Deleted description of SMS.   |
|      |   | 24          | 5.1 Operation Overview<br>Deleted description of SMS.  |
|      |   | 25          | Modified state transition diagram in Figure 5.1.   |
|      |   | 26,27       | Split Table 5-1 to create Table 5-2.<br>Added the note about File Composition of IAR version.  |
|      |   | 28          | 5.3 Smart Configurator Settings<br>Deleted following of setting of 32-bit interval timer.  |
|      |   | 36          | Changed CTSU driver settings in Figure 5.12.   |
|      |   | 38          | Unchecked the check box in Figure 5.15.  |
|      |   | 38          | 5.4 Capacitive Touch Setting<br>Deleted setting procedures of external trigger.  |
|      |   | 39          | Updated the values in Figure 5.16 with the re-tuned values.  |
|      |   | 40          | Changed variable name in Table 5-4.  |
|      |   | 47          | 5.9 Flowchart<br>Deleted flowcharts related to SMS.  |
|      |   | 48          | Modified flowchart in Figure 5.18.   |
|      |   | 56          | 6.3 Importing with IAR<br>Added description.   |
| 60   | 7.3 COM Port Setting with IAR<br>Added description. |             |  |
| 1.11 | Feb.2 26  | 57          | Modified missing display elements in Figure7.1   |

## General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

### 1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

### 2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

### 3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

### 4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

### 5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

### 6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between  $V_{IL}$  (Max.) and  $V_{IH}$  (Min.).

### 7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

### 8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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