RL78 Family

Flash programmer with Raspberry Pi (RL78 Protocol D)

Introduction

This application note describes a sample program for a flash programmer that writes to the flash memory of a microcontroller that supports Protocol D.

Operation Confirmation Device

RL78/F24

When applying the sample program covered in this application note to another microcomputer, modify the program according to the specifications for the target microcomputer and conduct an extensive evaluation of the modified program.

Related Document

Documents related to this application note are listed below, refer to the following documents as well.

- RL78 Microcontroller (RL78 Protocol D) Serial Programming Guide (R01AN6278)

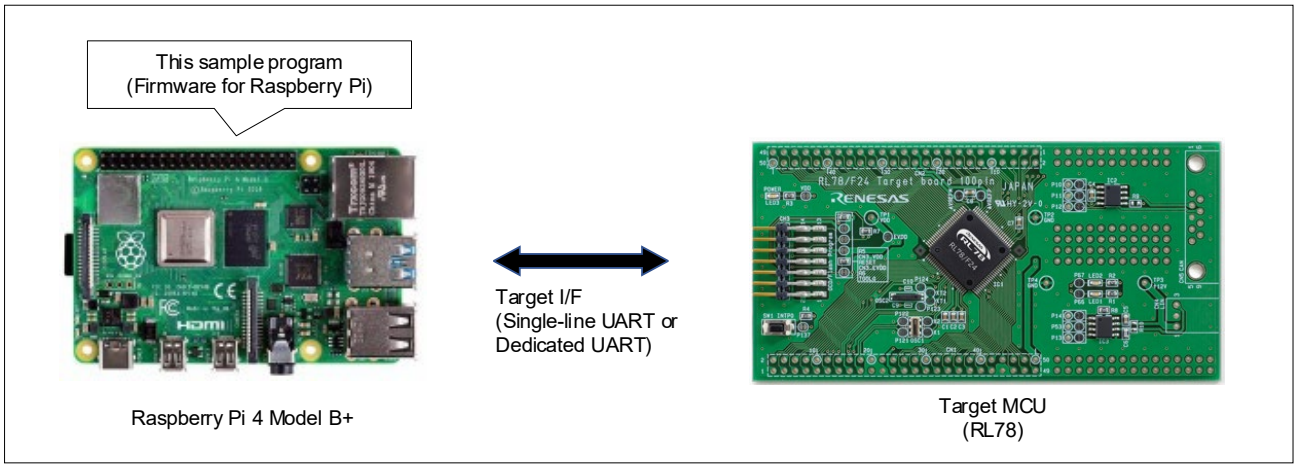
Contents

1. Overview

This sample program is a Raspberry Pi sample program for writing to the RL78 microcontroller's on-chip flash memory and has the following features.

- The writing target RL78 microcontroller (target MCU) supports the RL78 Protocol D.
- Serial programming of the RL78 Protocol D is used for writing.
- Raspberry Pi 4 Model B+ is used as the programmer hardware.
- Program files (written data) must conform to Motorola S format.

Figure 1-1 Image Diagram



2. Development Environments

The operation of the sample program provided with this application note has been tested under the following conditions. There are several ways to connect the flash programmer (Raspberry Pi4 Model B+), including remote connection using a PC and standalone connection by directly connecting peripherals such as a monitor.

Table 2-1 Operation Confirmation Conditions

Development tools	Description
Flash programmer	Raspberry Pi4 Model B+ (On-chip RAM 4GB)
OS	Raspberry Pi OS 64-bit(version 6.12.25)
Language	C99
Software build environment	gcc：12.2.0 (Debian 12.2.0-14+deb12u1)
Compiler	make：GNU Make 4.3
Shared library	ldd：2.28(Debian GLIBC 2.28-10+rpi1)

Caution: It may not work with versions other than those listed above.

3. Raspberry Pi Settings

3.1 Raspberry Pi environment (config.txt) Setting

To be able to use UART2 to 5, add the following items under [all] in /boot/firmware/config.txt and reboot.

```
enable_uart=1
dtoverlay=uart1
dtoverlay=uart2
dtoverlay=uart3
dtoverlay=uart4
dtoverlay=uart5
```

3.2 Build Environment

If you want to update the build environment, execute the following command.

```
$ sudo apt-get upgrade
$ sudo apt-get update
```

You can check the versions of gcc and make with the following command.

```
$ gcc -v
```

3.3 Way to Build

To build, execute the following command in the directory where the makefile is located.

Build:

```
$ sudo make ALL
```

If you want to delete executable binary:

```
$ sudo make clean
```

4. Specification

This sample program executes the executable file "fp_d" on the flash programmer (Raspberry Pi4 Model B+) and writes the Motorola S format file (write data) in the flash programmer to the target MCU.

4.1 Option Specifications

Perform initial settings and communicate with the target according to the specifications below.

- If execution is successful with the specified option settings, the flash programmer sends "OK" to the terminal.
- If execution fails with the specified option settings, the flash programmer sends "ERROR:XX" to the terminal. XX is displayed as a 2-digit hexadecimal number. See Table 4-3 and Table 4-4 for more information.

Table 4-1 and Table 4-2 show details of the options, and Figure 4-1 and Figure 4-2 show examples of how to use the options.

Table 4-1 Option Specifications (1/2)

Long option	Short option	Setting	Description
--file	-f	“file name”.mot	Specify the S-Record file.
--if	-u	uart1	uart1: Performs communication with the target MCU using a single-line UART (TOOL0).
		uart2	uart2: Performs communication with the target MCU using a dedicated UART (TOOL0, TOOLTxD, TOOLRxD). When omitted: It is the same result as when the uart1 is specified.
--speed	-b	115200 250000 500000 1000000	Specifies the transmission rate (bps) set by the Baud Rate Set command of the RL78 Protocol D. When omitted: It is the same result as when 115200 is specified.
--vdd	-d	x.x (1-digit decimal integer, first decimal place)	Specifies the VDD voltage (V) set by the Baud Rate Set command of the RL78 Protocol D. Set the VDD voltage supplied to the programmer board and the target MCU. When omitted: It is the same result as when the 3.3 is specified.

Table 4-2 Option Specifications (2/2)

Long option	Short option	Setting	Description
-id	-i	Security ID code (Hexadecimal, 16 bytes)	Specifies the security ID code (16-byte hexadecimal) in a character string. When omitted: It is the same result as when the 00000000000000000000000000000000 is specified.
--verify	-v	-	If this option is specified, verification is performed additionally.
--checksum	-s	-	If this option is specified, a checksum value is acquired additionally.
--skip-df	-k	-	Operations on Data Flash(erase, write, verify and checksum) are excluded from the scope. Please specify this when a data flash erase error occurs due to the security option byte settings. For details, refer to 4.4 Cautions.

Figure 4-1 Example of using long options (executable file name: fp_d)

```
>  sudo ./fp_d --file=test.mot --if=uart1 –vdd=3.3 --id=00112233445566778899AABBCCDDEEFF --verify --checksum
OK:connect
OK:erase
OK:program,verify
OK:checksum
 code flash:xxxx
 data flash:xxxx
```

Figure 4-2 Example of using short options (executable file name: fp_d)

```
>  sudo ./fp_d -ftest.mot -uuart1 -i00112233445566778899AABBCCDDEEFF -d3.3 -v -s
OK:connect
OK:erase
OK:program,verify
OK:checksum
 code flash:xxxx
 data flash:xxxx
```

4.2 Error Code Specifications

When the execution of the executable file fails, an error message in the format "Error:XX" is displayed on the terminal. If XX is a 2-digit hexadecimal number, see error code.

Table 4-3 and Table 4-4 show the error codes.

Table 4-3 List of Error Codes (1/2)

Error Code (Hexadecimal)	Description
04	Command number error This error occurs when a command number error of the RL78 Protocol D status code is received from the target MCU.
05	Parameter error This error occurs when a parameter error of the RL78 Protocol D status code is received from the target MCU.
07	Checksum error This error occurs when a checksum error of the RL78 Protocol D status code is received from the target MCU.
0F	Verification error This error occurs when a verification error of the RL78 Protocol D status code is received from the target MCU.
10	Protection error This error occurs when a protection error of the RL78 Protocol D status code is received from the target MCU.
15	NACK This error occurs when a NACK of the RL78 Protocol D status code is received from the target MCU.
1A	Erasure error This error occurs when an erasure error of the RL78 Protocol D status code is received from the target MCU.
1B	Blank error This error occurs when a blank error of the RL78 Protocol D status code is received from the target MCU.
1C	Write error This error occurs when a write error of the RL78 Protocol D status code is received from the target MCU.
23	Frequency error This error occurs when a frequency error of the RL78 Protocol D status code is received from the target MCU.
24	ID authentication error This error occurs when an ID authentication error of the RL78 Protocol D status code is received from the target MCU.
25	Security system error This error occurs when a security system error of the RL78 Protocol D status code is received from the target MCU.

Table 4-4 List of Error Codes (2/2)

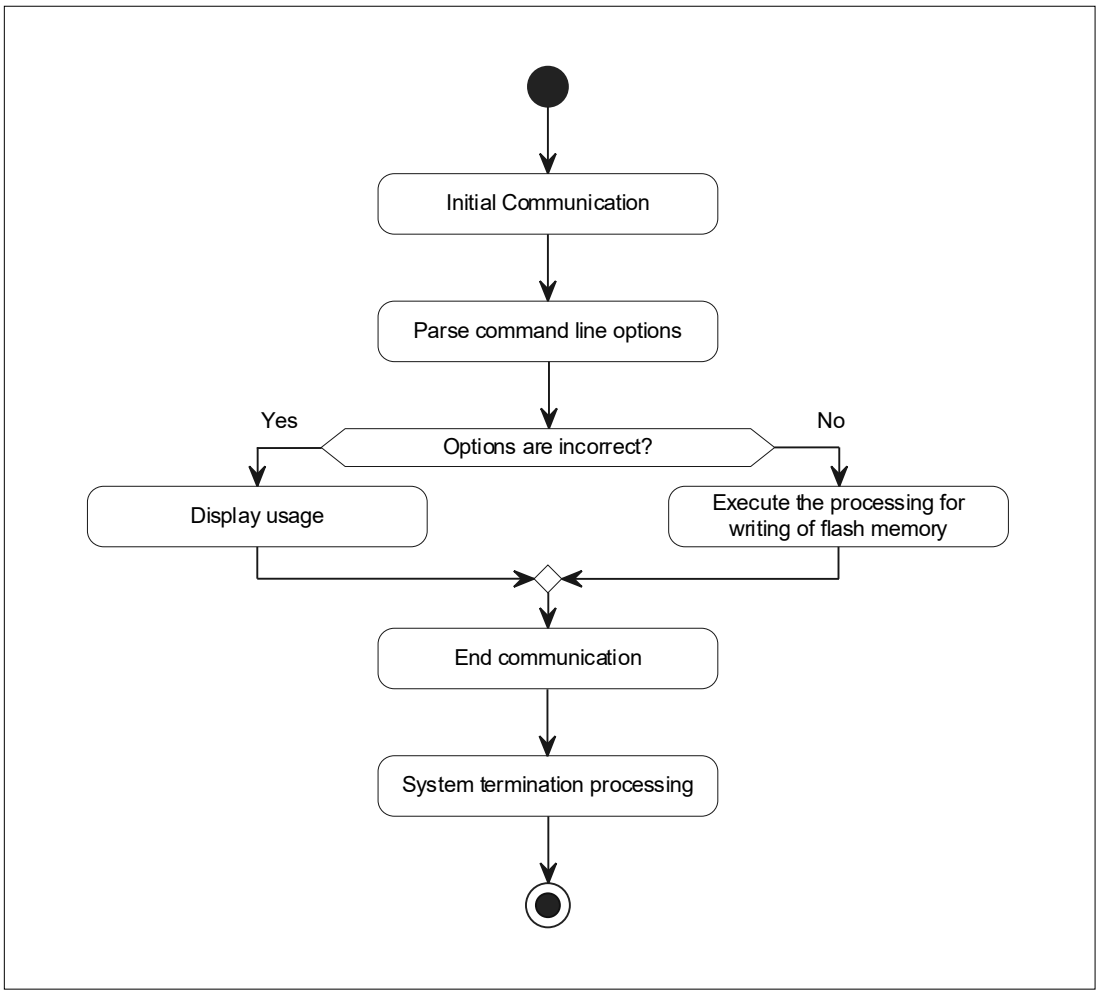
Error Code (Hexadecimal)	Description
F9	Incorrect command or parameter data This error occurs when an incorrect command or parameter data is received from the host PC.
FA	Buffer overrun error This error occurs when the data is received from the host PC and the buffer overruns. Confirm that the flow control with the host PC is set to Software (Xon/Xoff).
FB	Invalid Motorola S-format data This error occurs if Motorola S-format data sent to the target MCU is invalid. This error occurs even when Motorola S-format data is not in ascending order of address.
FC	Target MCU communication timeout This error occurs if a timeout occurs during communication between the programmer board and the target MCU.
FD	Invalid address of Motorola S-format file This error occurs if an out-of-range address is included in the Motorola S-format file sent to the target MCU.
FE	Command communication data error This error occurs if an invalid packet format is received from the target MCU.
FF	System error This error occurs if the program does not work correctly.

4.3 Flowchart

4.3.1 Main Routine (main function)

Figure 4-3 shows the operation of the main routine.

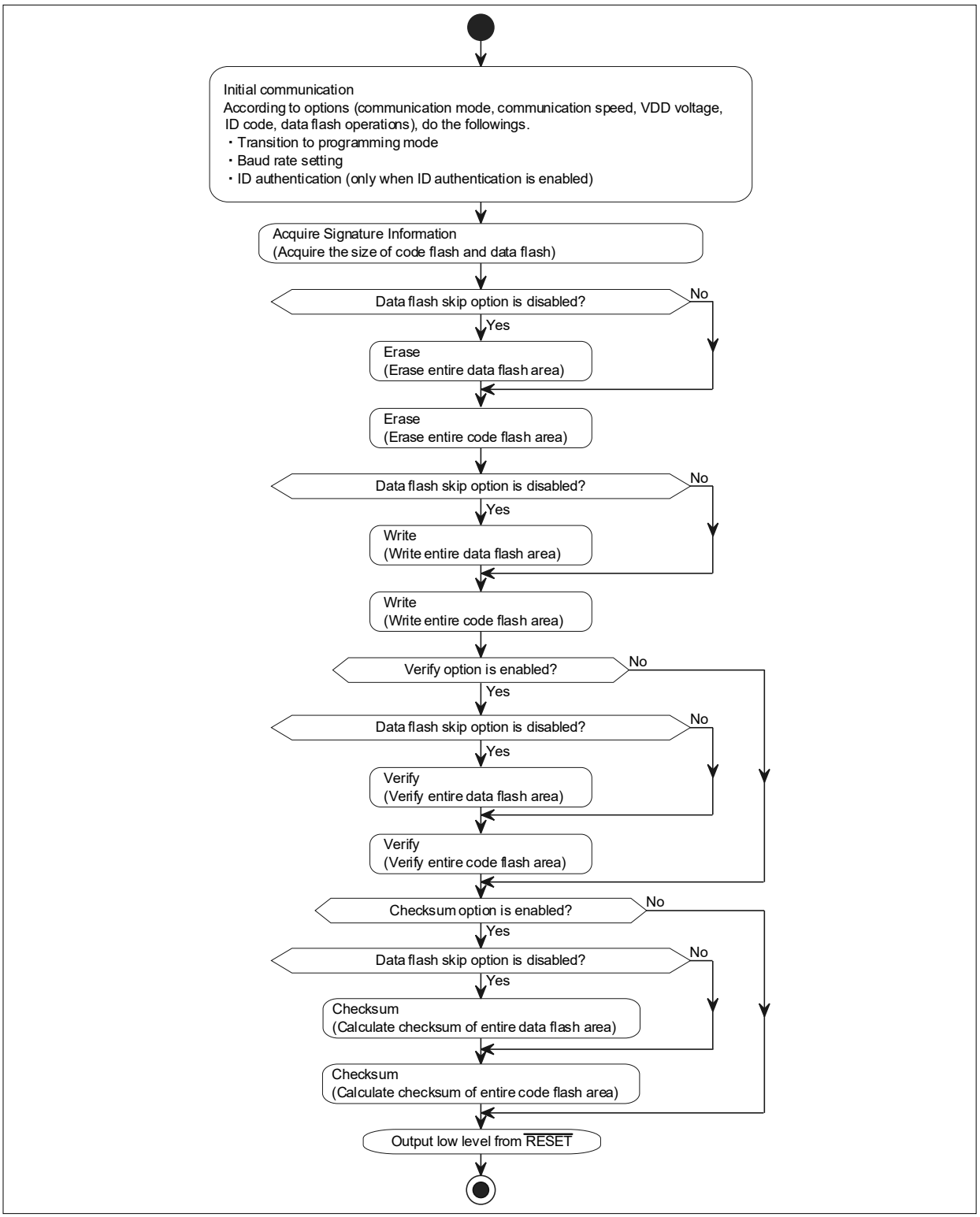
Figure 4-3 Main routine



4.3.2 Memory Write Processing

Figure 4-4 shows the operation of the memory write processing.

Figure 4-4 Memory write processing



4.4 Cautions

To erase RL78/F2x data flush, bit 0 of the security option byte must be set to 0. If you perform data flash erase without following this procedure, an error may occur during erase.

In this sample program, data flash and code flash are erased to comply with this specification.

This section explains how to eliminate a data flash erase error for a microcomputer whose security option byte bit 0 is 1.

1. setup command is set to exclude the data-flush process (-skip-df option specified).
2. Writes a program file in which bit 0 of the security option byte (000C4H/040C4H) is set to 0.
3. Reset MCU.
4. Use setup command to specify the data-flush process (without specifying skip-df option).

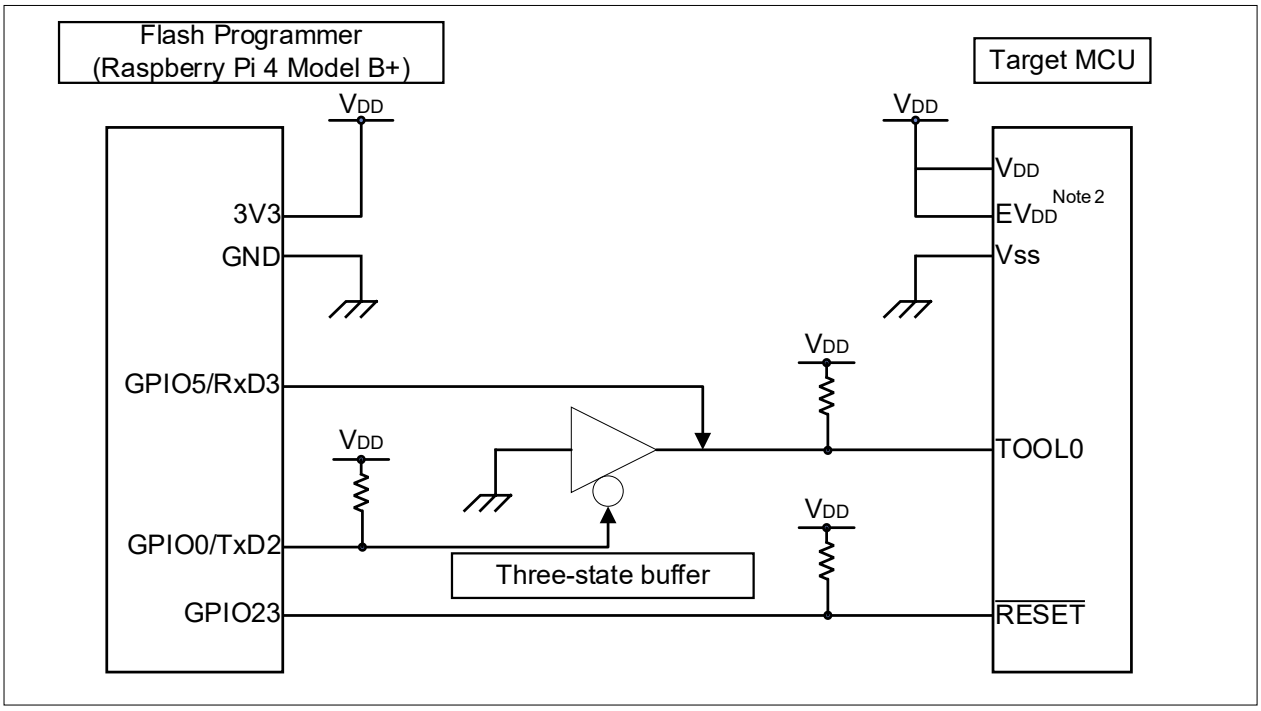
5. Hardware Descriptions

5.1 Target Interface Specifications

The following figures show how to connect the flash programmer to the target MCU.

5.1.1 Single Line UART

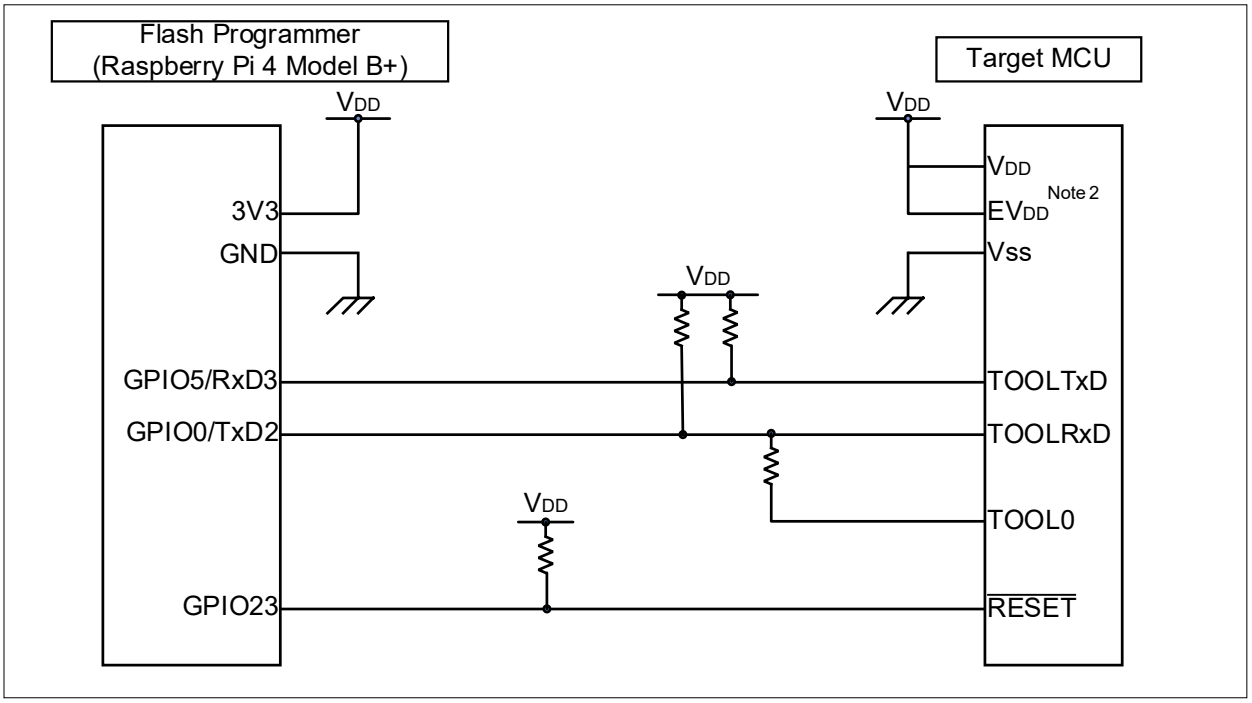
Figure 5-1 Single Line UART (V_{DD}=EV_{DD})[Note 1]



Note 1. This is a connection example when V_{DD} is 3.3V.
Note 2. If V_{DD} and EV_{DD} are different, EV_{DD} must be powered externally.

5.1.2 Dedicated UART

Figure 5-2 Dedicated UART (V_{DD}=EV_{DD})[Note 1]



Note 1. This is a connection example when V_{DD} is 3.3V.
Note 2. If V_{DD} and EV_{DD} are different, EV_{DD} must be powered externally.

5.2 List of Pins Used

Table 5-1 shows the flash programmer pins and functions used in the sample program.

Table 5-1 List of Pins Used

Pin Name	I/O	Function
TxD2	Output	Target interface communication transmit pin (UART2) Note 1
RxD3	Input	Target interface communication receive pin (UART3)
GPIO23	Output	Target MCU $\overline{\text{RESET}}$ control pin

Caution In this application note, only the used pins are handled. When creating your circuit, apply appropriate handling to the pins and design the circuit to meet the electrical characteristics.

Note 1. In the RL78 protocol D communication specification, the stop bits for transmission and reception are different, but in the UART communication settings of the Raspberry Pi OS, the stop bits for transmission and reception are the same, so the UARTs used for transmission and reception are separated.

6. Software Descriptions

6.1 List of Files

The following is list of files used in the sample program.

Table 6-1 shows the files provided by the Raspberry Pi4 OS, and Table 6-2 shows the files provided by the sample program.

Table 6-1 List of Files Provided by Raspberry Pi4 OS

Directory	File name	Description
/dev/	mem	Memory mapped I/O file
/dev/	tyAMA1 or ttyAMA2 Note 1	Serial communication port file For UART2
/dev/	ttyAMA2 or ttyAMA3 Note 1	Serial communication port file For UART3
/boot/	config.txt firmware/config.txt	config file for RPi4

Note 1. It depends on the OS version.

Table 6-2 List of Files Provided by Sample Program

Directory	File name	Descriprion
./	fp_d	Executable file of the program created with make (This file was created in the environment described in 2. Development Environments.) If you copy and use it from another location, you may need to grant execute permission using the following command: $ chmod a+x <file name>"
./	makefile	Sample makefile (Text file describing the procedure to be executed with the make command)
./	main.c	Main function processing
common/	protocol_d.c protocol_d.h	Protocol D command processing
common/	terminal_com.c terminal_com.h	Terminal command processing
common/	utility.h	Utility functions processing
driver/	r_cg_driver.c r_cg_driver.h	System initialization function processing
driver/	config_gpio.c config_gpio.h	Device driver for GPIO
driver/	config_systemtimer.c config_systemtimer.h	Device driver for System Timer
driver/	config_uart.c config_uart.h	Device driver for UART

6.2 List of Functions

Table 6-3 and Table 6-4 show the sample program function. No call graph is shown for functions that are not called.

Table 6-3 List of Functions (1/2)

Function Name	Description	Source file
main	main function	main.c
read_arguments	Parsing arguments of option	main.c
system_init	System initialization processing	config_driver.c
system_term	System termination processing	config_driver.c
config_gpio_create	Acquiring memory mapped I/O of GPIO register	config_gpio.c
config_gpio_destroy	Discarding memory mapped I/O of GPIO register	config_gpio.c
config_gpio_p23_output_start	Setting GPIO23 to output	config_gpio.c
config_gpio_p23_output_stop	Returning GPIO23 to initial setting	config_gpio.c
config_gpio_p0_txd2_start	Setting GPIO0 to TXD2	config_gpio.c
config_gpio_p0_txd2_stop	Setting GPIO0 to output	config_gpio.c
config_gpio_control_reset	HI/LO control of GPIO23($\overline{\text{RESET}}$)	config_gpio.c
config_gpio_control_tool0	HI/LO control of GPIO0(TOOL0)	config_gpio.c
config_systemtimer_create	Acquiring memory mapped I/O of SystemTimer resister	config_systemtimer.c
config_systemtimer_destroy	Discarding memory mapped I/O of SystemTimer resister	config_systemtimer.c
config_systemtimer_get_count	Acquiring count value of SystemTimer	config_systemtimer.c
config_systemtimer_wait_ms	Waiting in ms unit	config_systemtimer.c
config_systemtimer_wait_us	Waiting in us unit	config_systemtimer.c
config_uart_create	Acquiring memory mapped I/O of UART register	config_uart.c
config_uart_destroy	Discarding memory mapped I/O of UART register	config_uart.c
config_uart2_start	Initial setting for UART2	config_uart.c
config_uart2_stop	Discarding UART2 setting	config_uart.c
config_uart3_start	Initial setting for UART3	config_uart.c
config_uart3_stop	Discarding UART3 setting	config_uart.c
config_uart2_send	Sending data from TXD2	config_uart.c
config_uart2_send_with_wait	Sending data from TXD2 (with waiting time between data transmission)	config_uart.c
config_uart3_receive	Receiving data from RXD3	config_uart.c
config_uart23_set_baudrate	Baud rate setting of UART2, UART3	config_uart.c
fp_cmd_reset_d	Reset command sending processing	protocol_d.c
fp_cmd_verify_d	Verify command sending processing	protocol_d.c
fp_cmd_erase_d	Erase command sending processing	protocol_d.c
fp_cmd_blank_d	Block Blank Check command sending processing	protocol_d.c
fp_cmd_program_d	Programming command sending processing	protocol_d.c
fp_cmd_baudrate_d	Baud Rate Set command sending processing	protocol_d.c

Table 6-4 List of Functions (2/2)

Function Name	Description	Source file
fp_cmd_sec_id_auth_d	Security ID Authentication command sending processing	protocol_d.c
fp_cmd_sec_set_d	Executing Security Set command	protocol_d.c
fp_cmd_sec_get_d	Executing Security Get	protocol_d.c
fp_cmd_sec_release_d	Executing Security Release	protocol_d.c
fp_cmd_checksum_d	Checksum command sending processing	protocol_d.c
fp_cmd_signature_d	Silicon Signature command sending processing	protocol_d.c
fp_cmd_sec_program_d	Secure Programming command sending processing	protocol_d.c
fp_cmd_common_d	Executing the common command of RL78 Protocol D.	protocol_d.c
fp_initial_communication	Initial processing for starting communication (Entry to Flash Memory Programming Modes)	protocol_d.c
fp_term_communication	Control of Target MCU pins	protocol_d.c
fp_get_signature	Executing Silicon Signature command and acquiring each parameter	protocol_d.c
fp_erase_program	Code/data flash rewrite processing	protocol_d.c
fp_security_flag_write	Security flag rewrite processing	protocol_d.c
fp_security_release	Security release process	protocol_d.c
fp_command_cancel	Command Cancel Processing	protocol_d.c
terminal_command_init	Initialization of each parameter	terminal_com.c
terminal_command_init_dev	Initialization of device-dependent parameters	terminal_com.c
terminal_command_offline	Executing Flash rewriting processing	terminal_com.c
send_protocol_d	Command sending processing	protocol_d.c
send_command_d	Command transmission processing	protocol_d.c
send_data_d	Data transmission processing	protocol_d.c
recv_data_d	Data receiving processing	protocol_d.c
decode_srecord	Analyzing Motorola S-format file	terminal_com.c
program_data	Writing data for the specified size	terminal_com.c
program_srecord_data	Writing Motorola S-format data	terminal_com.c

6.3 Specification of Functions

This section shows the specifications of major functions used in the sample program.

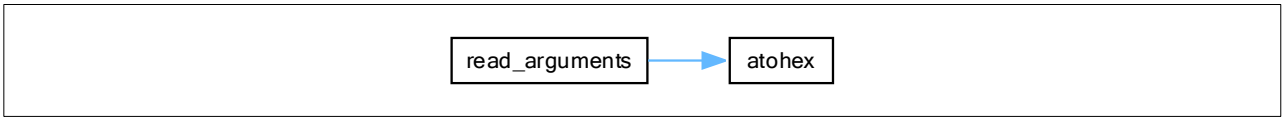
6.3.1 read_arguments

Table 6-5 shows the function description and Figure 6-1 shows the call graph.

Table 6-5 Description of read_arguments

read_arguments	
Outline	Parsing arguments of option
Declaration	static uint8_t read_arguments(st_command_data_t * cmd, int argc, char * argv[])
Argument	st_command_data_t * cmd: Option setting information int argc: Number of optional arguments char * argv[]: Optional arguments
Return Value	0: Normal 1: Abnormal (Option is not correct.)
Description	Read and parse the argument “com_data”.

Figure 6-1 Call Graph of read_arguments



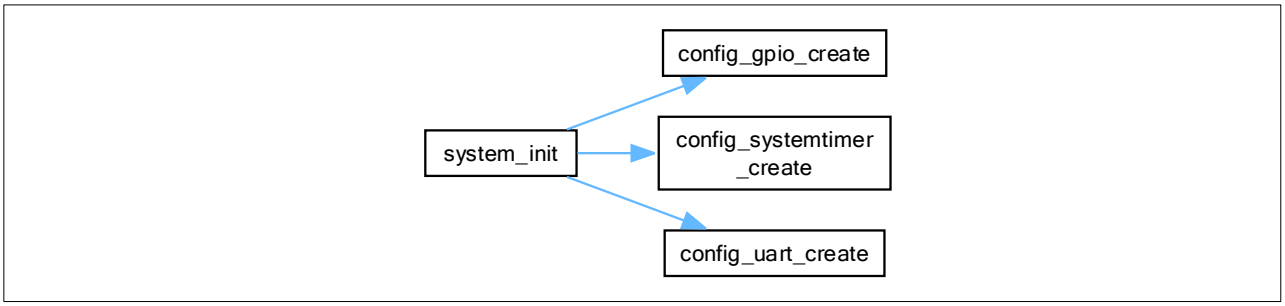
6.3.2 system_init

Table 6-6 shows the function description and Figure 6-2 shows the call graph.

Table 6-6 Description of system_init

system_init	
Outline	System initialization processing
Declaration	void system_init (void)
Argument	None
Return Value	None
Description	Initialize system settings.

Figure 6-2 Call Graph of system_init



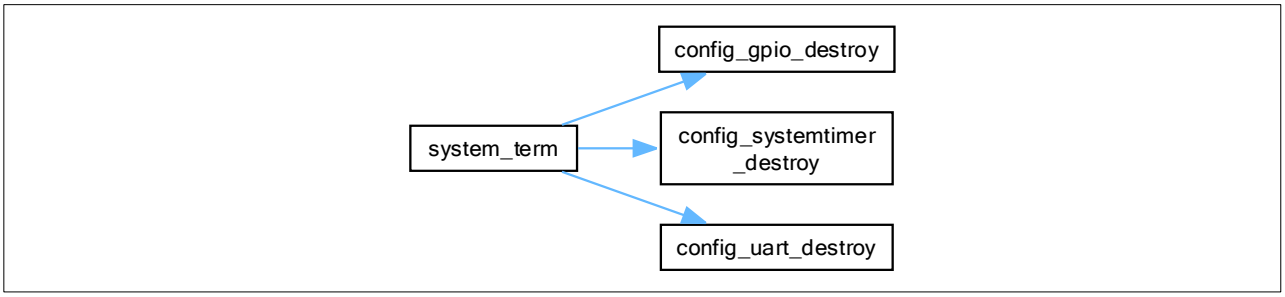
6.3.3 system_term

Table 6-7 shows the function description and Figure 6-3 shows the call graph.

Table 6-7 Description of system_term

system_term	
Outline	System termination processing
Declaration	void system_term (void)
Argument	None
Return Value	None
Description	Terminate the system.

Figure 6-3 Call Graph of system_term



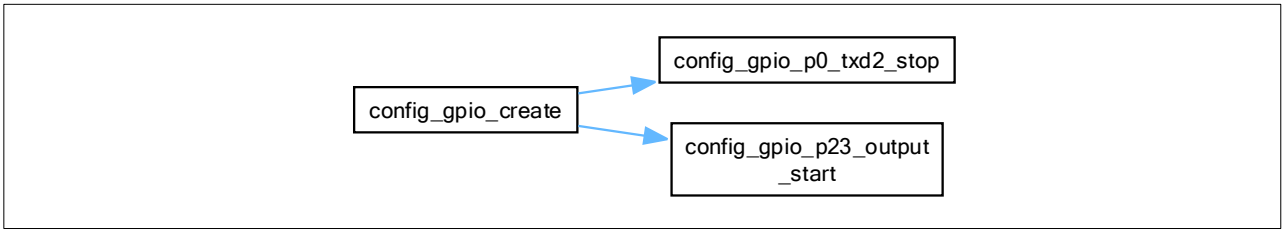
6.3.4 config_gpio_create

Table 6-8 shows the function description and Figure 6-4 shows the call graph.

Table 6-8 Description of config_gpio_create

config_gpio_create	
Outline	Acquiring memory mapped I/O of GPIO register
Declaration	void config_gpio_create (int32_t mem_fd)
Argument	int32_t mem_fd: MMIO file descriptor
Return Value	None
Description	Acquire the memory mapped I/O of the GPIO register and set the GPIO ports.

Figure 6-4 Call Graph of config_gpio_create



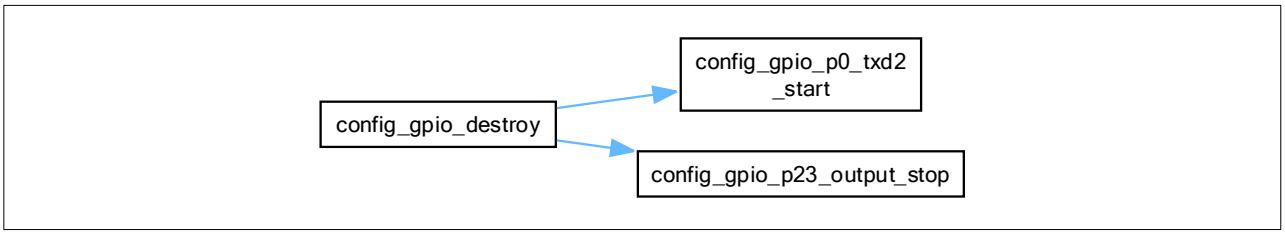
6.3.5 config_gpio_destroy

Table 6-9 shows the function description and Figure 6-5 shows the call graph.

Table 6-9 Description of config_gpio_destroy

config_gpio_destroy	
Outline	Discarding memory mapped I/O of GPIO register
Declaration	void config_gpio_destroy (void)
Argument	None
Return Value	None
Description	Discard the memory mapped I/O of the GPIO register to return the GPIO port to its state before program execution.

Figure 6-5 Call Graph of config_gpio_destroy



6.3.6 config_gpio_p23_output_start

Table 6-10 shows the function description.

Table 6-10 Description of config_gpio_p23_output_start

config_gpio_p23_output_start	
Outline	Setting GPIO23 to output pin
Declaration	void config_gpio_p23_output_start (void)
Argument	None
Return Value	None
Description	Set the GPIO23 of Raspberry Pi to output.

6.3.7 config_gpio_p23_output_stop

Table 6-11 shows the function description.

Table 6-11 Description of config_gpio_p23_output_stop

config_gpio_p23_output_stop	
Outline	Returning GPIO23 to initial setting
Declaration	void config_gpio_p23_output_stop (void)
Argument	None
Return Value	None
Description	Return the GPIO23 of Raspberry Pi to its initial state.

6.3.8 config_gpio_p0_txd2_start

Table 6-12 shows the function description.

Table 6-12 Description of config_gpio_p0_txd2_start

config_gpio_p0_txd2_start	
Outline	Setting GPIO0 to TXD2
Declaration	void config_gpio_p0_txd2_start(void)
Argument	None
Return Value	None
Description	Set the GPIO0 of Raspberry Pi to the TxD2.

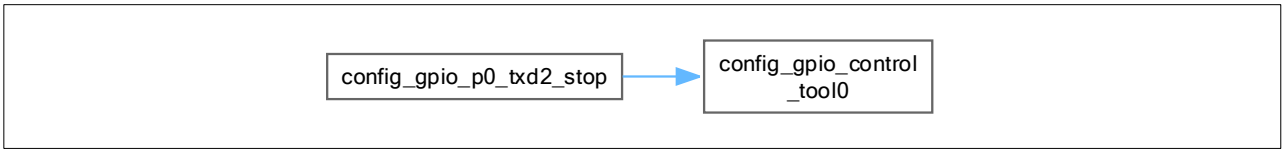
6.3.9 config_gpio_p0_txd2_stop

Table 6-13 shows the function description and Figure 6-6 shows the call graph.

Table 6-13 Description of config_gpio_p0_txd2_stop

config_gpio_p0_txd2_stop	
Outline	Setting GPIO0 to output
Declaration	void config_gpio_p0_txd2_stop(void)
Argument	None
Return Value	None
Description	Set the GPIO0 of Raspberry Pi to output.

Figure 6-6 Call Graph of config_gpio_p0_txd2_stop



6.3.10 config_gpio_control_reset

Table 6-14 shows the function description.

Table 6-14 Description of config_gpio_control_reset

config_gpio_control_reset	
Outline	HI/LO control of GPIO23(RESET)
Declaraiton	void config_gpio_control_reset(uint8_t enabled)
Argument	uint8_t enabled: Reset information (0: Release of a reset 1: Reset)
Return Value	None
Description	Control HI/LO of the GPIO23(RESET) of Raspberry Pi. The reset is released by executing “config_gpio_control_reset(0)” and RESET signal is changed to HI. The target MCU is in the reset state by execution “config_gpio_control_reset(1)” and RESET signal is changed to LO.

6.3.11 config_gpio_control_tool0

Table 6-15 shows the function description.

Table 6-15 Description of config_gpio_control_tool0

config_gpio_control_tool0	
Outline	HI/LO control of GPIO0(TOOL0)
Declaration	void config_gpio_control_tool0(uint8_t enabled)
Argument	uint8_t enabled: HI/LO information of GPIO0 (0: LO 1: HI)
Return Value	None
Description	Control HI/LO of GPIO0(TOOL0) of Raspberry Pi.

6.3.12 config_systemtimer_create

Table 6-16 shows the function description.

Table 6-16 Description of config_systemtimer_create

config_systemtimer_create	
Outline	Acquiring memory mapped I/O of SystemTimer register
Declaration	void config_systemtimer_create(int32_t mem_fd)
Return Value	int32_t mem_fd: MMIO file descriptor
Argument	None
Description	Acquire the memory mapped I/O of the SystemTimer register.

6.3.13 config_systemtimer_destroy

Table 6-17 shows the function description.

Table 6-17 Description of config_systemtimer_destroy

config_systemtimer_destroy	
Outline	Discarding memory mapped I/O of SystemTimer register
Declaration	void config_systemtimer_destroy (void)
Argument	None
Return Value	None
Description	Discard the memory mapped I/O of the SystemTimer register.

6.3.14 config_systemtimer_get_count

Table 6-18 shows the function description.

Table 6-18 Description of config_systemtimer_get_count

config_systemtimer_get_count	
Outline	Acquiring count value of SystemTimer
Declaration	uint64_t config_systemtimer_get_count(void)
Argument	None
Return Value	The count value of SystemTimer
Description	Acquire the count value of the SystemTimer.

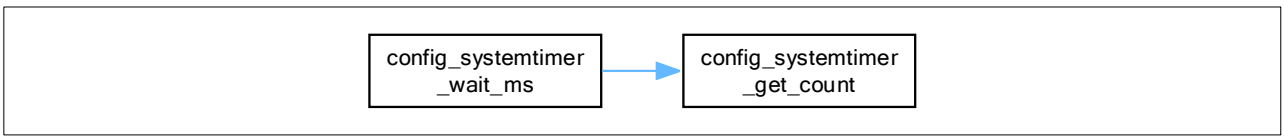
6.3.15 config_systemtimer_wait_ms

Table 6-19 shows the function description and Figure 6-7 shows the call graph.

Table 6-19 Description of config_systemtimer_wait_ms

config_systemtimer_wait_ms	
Outline	Waiting in ms unit
Declaration	void config_systemtimer_wait_ms(const uint16_t wait_count)
Argument	const uint16_t wait_count: Wait time [ms]
Return Value	None
Description	Wait for the specified time (ms unit).

Figure 6-7 Call Graph of config_systemtimer_wait_ms



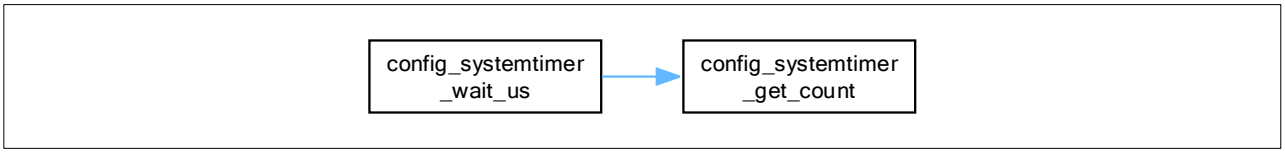
6.3.16 config_systemtimer_wait_us

Table 6-20 shows the function description and Figure 6-8 shows the call graph.

Table 6-20 Description of config_systemtimer_wait_us

config_systemtimer_wait_us	
Outline	Waiting in us unit
Declaration	void config_systemtimer_wait_us(const uint16_t wait_count)
Argument	const uint16_t wait_count: Wait time [us]
Return Value	None
Description	Wait for the specified time (us unit).

Figure 6-8 Call Graph of config_systemtimer_wait_us



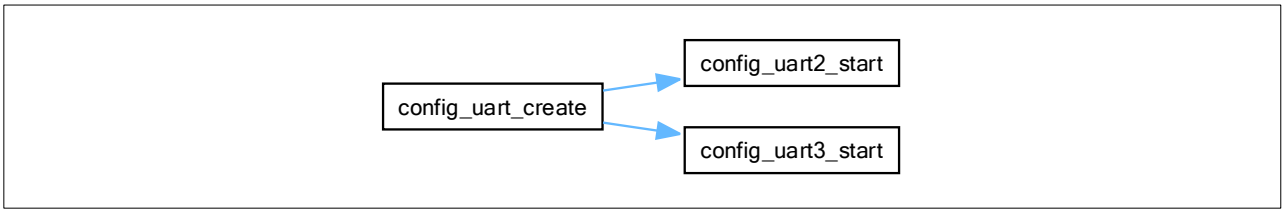
6.3.17 config_uart_create

Table 6-21 shows the function description and Figure 6-9 shows the call graph.

Table 6-21 Description of config_uart_create

config_uart_create	
Outline	Acquiring memory mapped I/O of UART register
Declaration	void config_uart_create(int32_t mem_fd)
Argument	int32_t mem_fd: MMIO file descriptor
Return Value	None
Description	Acquire the memory mapped I/O of the UART register.

Figure 6-9 Call Graph of config_uart_create



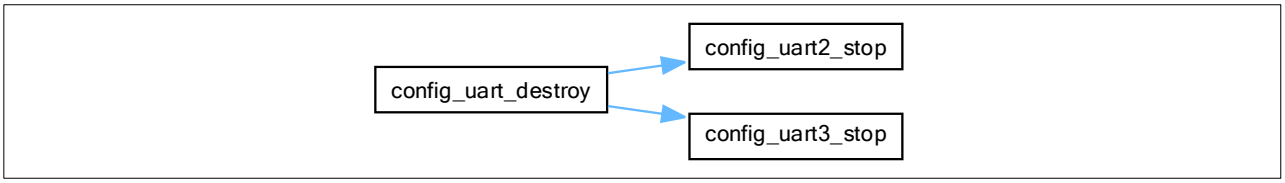
6.3.18 config_uart_destroy

Table 6-22 shows the function description and Figure 6-10 shows the call graph.

Table 6-22 Description of config_uart_destroy

config_uart_destroy	
Outline	Discarding memory mapped I/O of UART register
Declaration	void config_uart_destroy(void)
Argument	None
Return Value	None
Description	Discard the memory mapped I/O of the UART register.

Figure 6-10 Call Graph of config_uart_destroy



6.3.19 config_uart2_start

Table 6-23 shows the function description.

Table 6-23 Description of config_uart2_start

config_uart2_start	
Outline	Initial setting for UART2
Declaration	void config_uart2_start(void)
Argument	None
Return Value	None
Description	Initialize the UART2. (In the Raspberry Pi4, the stop bit set by this function is common to both transmission and reception, so UART2 is used for transmission in this sample.)

6.3.20 config_uart2_stop

Table 6-24 shows the function description.

Table 6-24 Description of config_uart2_stop

config_uart2_stop	
Outline	Discarding UART2 setting
Declaration	void config_uart2_stop(void)
Argument	None
Return Value	None
Description	Discard the UART2 settings.

6.3.21 config_uart3_start

Table 6-25 shows the function description.

Table 6-25 Description of config_uart3_start

config_uart3_start	
Outline	Initial setting for UART3
Declaration	void config_uart3_start(void)
Argument	None
Return Value	None
Description	Initialize the UART3. (In the Raspberry Pi4, the stop bit set by this function is common to both transmission and reception, so UART3 is used for reception in this sample.)

6.3.22 config_uart3_stop

Table 6-26 shows the function description.

Table 6-26 Description of config_uart3_stop

config_uart3_stop	
Outline	Discarding UART3 setting
Declaration	void config_uart3_stop(void)
Argument	None
Return Value	None
Description	Discard the UART3 settings.

6.3.23 config_uart2_send

Table 6-27 shows the function description.

Table 6-27 Description of config_uart2_send

config_uart2_send	
Outline	Sending data from TXD2
Declaration	e_md_status_t config_uart2_send(uint8_t * const tx_buf, uint16_t tx_num)
Argument	uint8_t * const tx_buf: Data for transmission uint16_t tx_num: Size of data for transmission
Return Value	MD_OK: Normal MD_ARGERROR: Argument error MD_TXERROR: Transmission error
Description	Send data from the TXD2. If the data is too long, write as much as possible and then write the remaining data again. Repeat this until all data is sent. If there is a transmission error, the write() function returns a negative value, so this is used to detect errors.

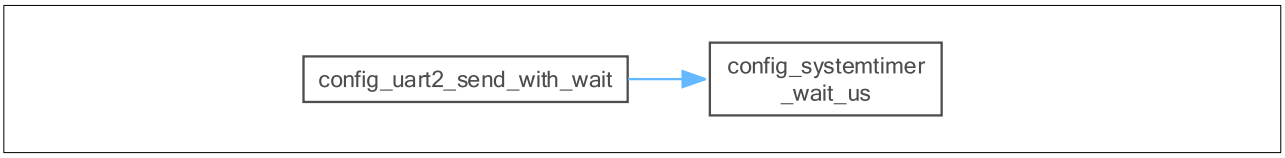
6.3.24 config_uart2_send_with_wait

Table 6-28 shows the function description and Figure 6-11 shows the call graph.

Table 6-28 Description of config_uart2_send_with_wait

config_uart2_send_with_wait	
Outline	Sending data from TXD2 (with waiting time between data transmission)
Declaration	e_md_status_t config_uart2_send_with_wait(uint8_t * const tx_buf, uint16_t tx_num)
Argument	uint8_t * const tx_buf: Data for transmission uint16_t tx_num: Size of data for transmission
Return Value	MD_OK: Normal MD_ARGERROR: Argument error MD_TXERROR: Transmission error
Description	Send data from the TXD2. Add a wait for each 1 byte transmission and repeat until all data is sent.

Figure 6-11 Call Graph of config_uart2_send_with_wait



6.3.25 config_uart3_receive

Table 6-29 shows the function description.

Table 6-29 Description of config_uart3_receive

config_uart3_receive	
Outline	Receiving data from RXD3
Declaration	e_md_status_t config_uart3_receive(uint8_t * const rx_buf, uint16_t rx_num, uint16_t timeout_ms, uint8_t is_echobacked, uint16_t * p_top_pos)
Argument	uint8_t * const rx_buf: Data for reception uint16_t rx_num: Size of data for reception uint16_t timeout_ms: Timeout time uint8_t is_echobacked: Eliminating single-line UART echoback uint16_t * p_top_pos: First data of received data
Return Value	MD_OK: Normal MD_ARGERROR: Argument error MD_RXERROR: Reception error MD_RXTIMEOUT: Receive timeout error
Description	Receive data from the RXD3. If the received data is insufficient (for example, not all data has been received), read as much as possible and read it again. Repeat this until all data is read. If it cannot be received to the end, select() will fail at some stage and a timeout will occur. Setting timeout_ms to 0 causes the system to wait until all data has been received.

6.3.26 config_uart23_set_baudrate

Table 6-30 shows the function description.

Table 6-30 Description of config_uart23_set_baudrate

config_uart23_set_baudrate	
Outline	Baud rate setting of UART2, UART3
Declaration	void config_uart23_set_baudrate(e_uart_baudrate_t baudrate)
Argument	e_uart_baudrate_t baudrate: Baud rate
Return Value	None
Description	Set the baud rate for UART2 and UART3.

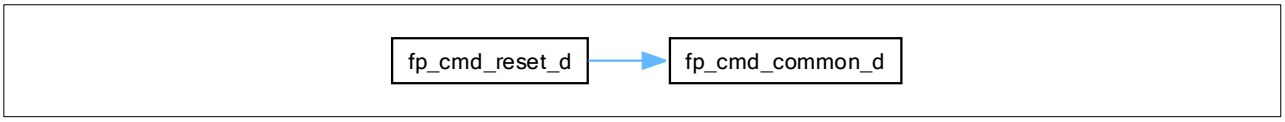
6.3.27 fp_cmd_reset_d

Table 6-31 shows the function description and Figure 6-12 shows the call graph.

Table 6-31 Description of fp_cmd_reset_d

fp_cmd_reset_d	
Outline	Reset command sending processing
Declaration	uint8_t fp_cmd_reset_d(void)
Argument	None
Return Value	0: Normal end Other than 0: Abnormal (refer to 4.2 Error Code Specifications)
Description	Execute the Reset command of RL78 protocol D.

Figure 6-12 Call Graph of fp_cmd_reset_d



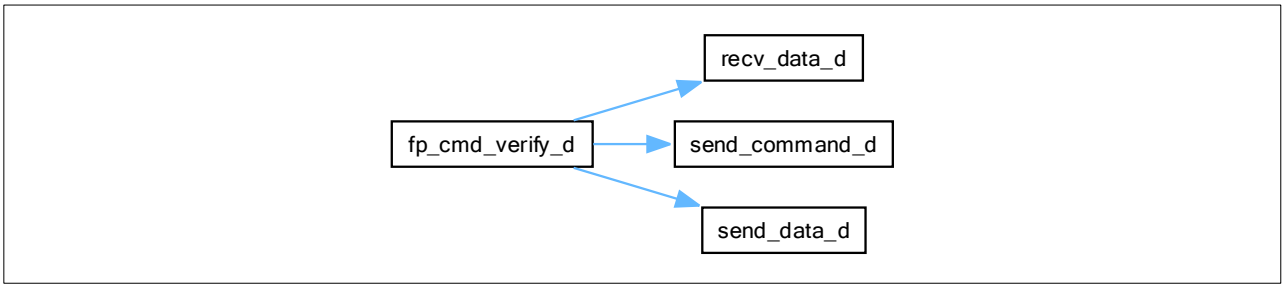
6.3.28 fp_cmd_verify_d

Table 6-32 shows the function description and Figure 6-13 shows the call graph.

Table 6-32 Description of fp_cmd_verify_d

fp_cmd_verify_d	
Outline	Verify command sending processing
Declaration	uint8_t fp_cmd_verify_d(const uint32_t start, const uint32_t end, const uint8_t * data)
Argument	const uint32_t start: Start address for verify const uint32_t end: End address for verify const uint8_t * data: Comparison data for verify
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Verify command of RL78 protocol D.

Figure 6-13 Call Graph of fp_cmd_verify_d



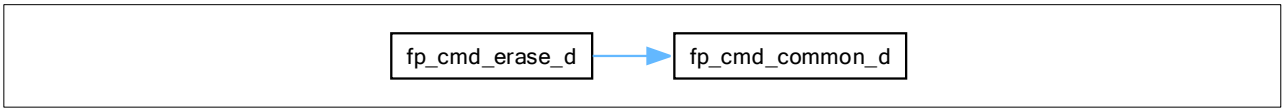
6.3.29 fp_cmd_erase_d

Table 6-33 shows the function description and Figure 6-14 shows the call graph.

Table 6-33 Description of fp_cmd_erase_d

fp_cmd_erase_d	
Outline	Erase command sending processing
Declaration	uint8_t fp_cmd_erase_d(const uint32_t addr)
Argument	const uint32_t addr: Address of block for erase
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Block Erase command of RL78 protocol D.

Figure 6-14 Call Graph of fp_cmd_erase_d



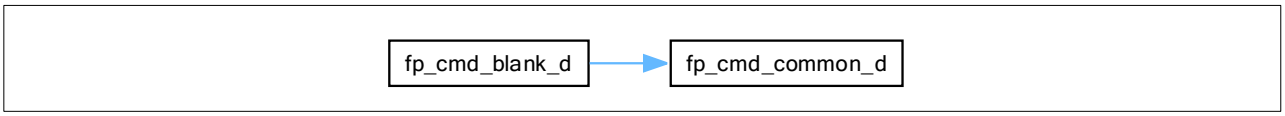
6.3.30 fp_cmd_blank_d

Table 6-34 shows the function description and Figure 6-15 shows the call graph.

Table 6-34 Description of fp_cmd_blank_d

fp_cmd_blank_d	
Outline	Block Blank Check command sending processing
Declaration	uint8_t fp_cmd_blank_d(const uint32_t start, const uint32_t end, const uint8_t tar)
Argument	const uint32_t start: Start address for blank check const uint32_t end: End address for blank check const uint8_t * tar: Target area
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Block Blank Check command of RL78 protocol D.

Figure 6-15 Call Graph of fp_cmd_blank_d



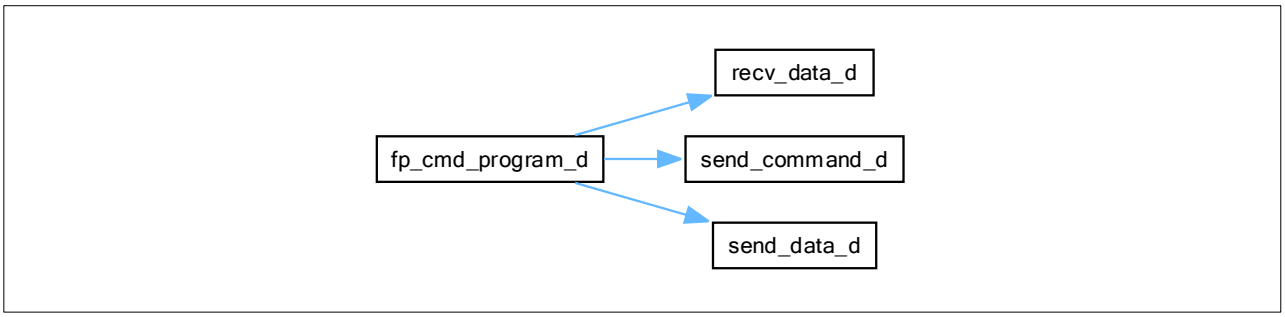
6.3.31 fp_cmd_program_d

Table 6-35 shows the function description and Figure 6-16 shows the call graph.

Table 6-35 Description of fp_cmd_program_d

fp_cmd_program_d	
Outline	Program command sending processing
Declaration	uint8_t fp_cmd_program_d(const uint32_t start, const uint32_t end, const uint8_t * data)
Argument	const uint32_t start: Start address for rewriting const uint32_t end: End address for rewriting const uint8_t * data: Data for rewriting
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Programming command of RL78 protocol D.

Figure 6-16 Call Graph of fp_cmd_program_d



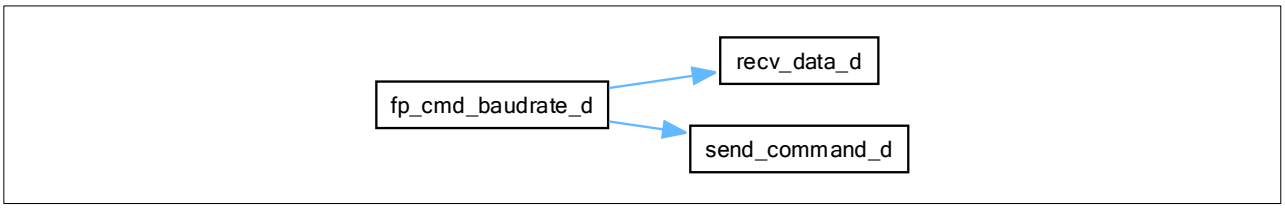
6.3.32 fp_cmd_baudrate_d

Table 6-36 shows the function description and Figure 6-17 shows the call graph.

Table 6-36 Description of fp_cmd_baudrate_d

fp_cmd_baudrate_d	
Outline	Baud Rate Set command sending processing
Declaration	uint8_t fp_cmd_baudrate_d(const e_uart_speed_t baudrate, const uint16_t vdd, uint8_t * frq, uint8_t * fpm)
Argument	const UART_SPEED baudrate: Communication baud rate UART_SPEED_DEFAULT: 115200 bps UART_SPEED_250000: 250000 bps UART_SPEED_500000: 500000 bps UART_SPEED_1000000: 1000000 bps const uint16_t vdd: V_{DD} voltage to be applied (in 100-mV units, truncate the digits after the decimal point.) uint8_t * frq: CPU operating frequency [MHz] (Acquire it from target MCU) uint8_t * fpm: Flash memory rewriting mode (Acquire it from target MCU)
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Baud Rate Set command of RL78 protocol D.

Figure 6-17 Call Graph of fp_cmd_baudrate_d



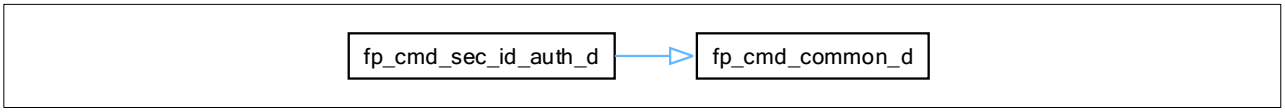
6.3.33 fp_cmd_sec_id_auth_d

Table 6-37 shows the function description and Figure 6-18 shows the call graph.

Table 6-37 Description of fp_cmd_sec_id_auth_d

fp_cmd_sec_id_auth_d	
Outline	Security ID Authentication command sending processing
Declaration	uint8_t fp_cmd_sec_id_auth_d(const uint8_t * sec_id)
Argument	const uint8_t * sec_id: Security ID code
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Security ID Authentication command of RL78 protocol D.

Figure 6-18 Call Graph of fp_cmd_sec_id_auth_d



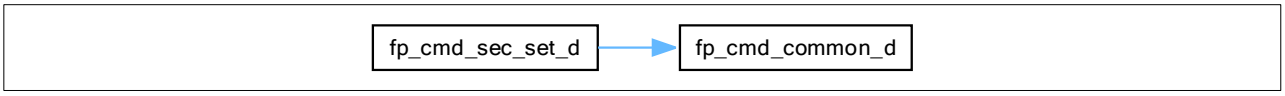
6.3.34 fp_cmd_sec_set_d

Table 6-38 shows the function description and Figure 6-19 shows the call graph.

Table 6-38 Description of fp_cmd_sec_set_d

fp_cmd_sec_set_d	
Outline	Executing Security Set command
Declaration	uint8_t fp_cmd_sec_set_d(const uint8_t * sf)
Argument	const uint8_t * sf: Security flag
Return Value	0: Normal end Other than 0: Normal end (refer to 4.2 Error Code Specifications)
Description	Execute the Security Set command of the RL78 protocol D.

Figure 6-19 Call Graph of fp_cmd_sec_set_d



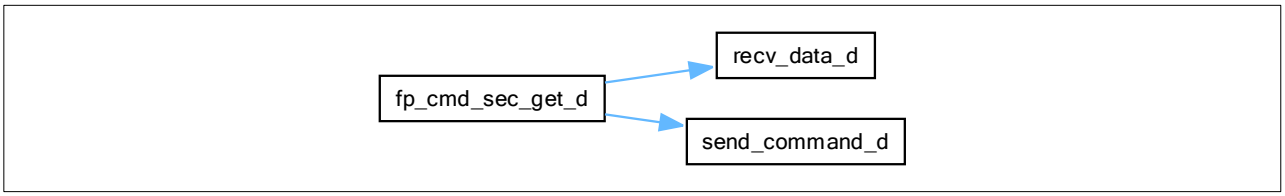
6.3.35 fp_cmd_sec_get_d

Table 6-39 shows the function description and Figure 6-20 shows the call graph.

Table 6-39 Description of fp_cmd_sec_get_d

fp_cmd_sec_get_d	
Outline	Executing Security Get command
Declaration	uint8_t fp_cmd_sec_get_d(uint8_t * sf)
Argument	const uint8_t * sf: Security flag
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Security Get command of the RL78 protocol D.

Figure 6-20 Call Graph of fp_cmd_sec_get_d



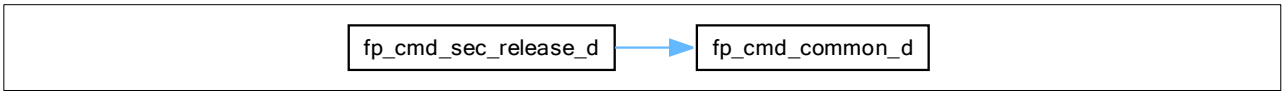
6.3.36 fp_cmd_sec_release_d

Table 6-40 shows the function description and Figure 6-21 shows the call graph.

Table 6-40 Description of fp_cmd_sec_set_d

fp_cmd_sec_release_d	
Outline	Executing Security Release command
Declaration	uint8_t fp_cmd_sec_release_d (void);
Argument	None
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Security Release command of the RL78 protocol D.

Figure 6-21 Call Graph of fp_cmd_sec_set_d



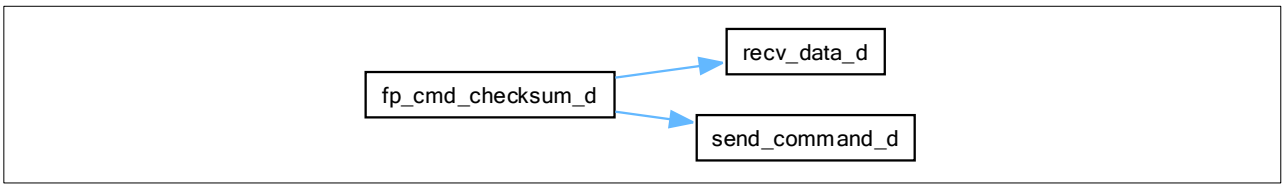
6.3.37 fp_cmd_checksum_d

Table 6-41 shows the function description and Figure 6-22 shows the call graph.

Table 6-41 Description of fp_cmd_checksum_d

fp_cmd_checksum_d	
Outline	Checksum command sending processing
Declaration	uint8_t fp_cmd_checksum_d(const uint32_t start, const uint32_t end, uint16_t * checksum)
Argument	const uint32_t start: Start address for checksum const uint32_t end: End address for checksum const uint16_t * checksum: Acquired checksum data
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Checksum command of the RL78 protocol D.

Figure 6-22 Call Graph of fp_cmd_checksum_d



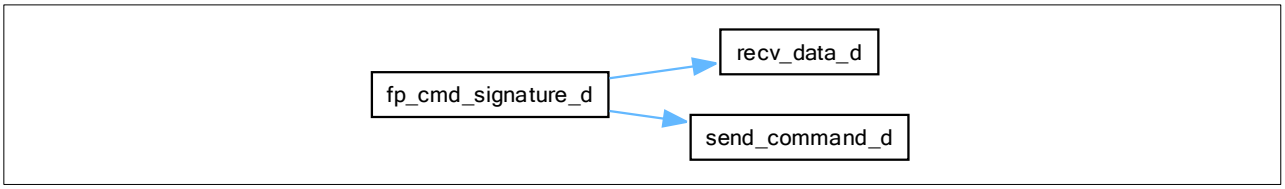
6.3.38 fp_cmd_signature_d

Table 6-42 shows the function description and Figure 6-23 shows the call graph.

Table 6-42 Description of fp_cmd_signature_d

fp_cmd_signature_d	
Outline	Silicon Signature command sending processing
Declaration	uint8_t fp_cmd_signature_d(uint8_t * dvc, uint8_t * dev, uint32_t * cfe, uint32_t * dfe, uint8_t * fwv)
Argument	uint8_t * dvc: Device function code (Acquire it from target MCU) uint8_t * dev: Device name (Acquire it from target MCU) uint32_t * cfe: Last address of code flash memory area (Acquire it from target MCU) uint32_t * dfe: Last address of data flash memory area (Acquire it from target MCU) uint8_t * fwv: Boot firmware version (Acquire it from target MCU)
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Silicon Signature command of RL78 protocol D.

Figure 6-23 Call Graph of fp_cmd_signature_d



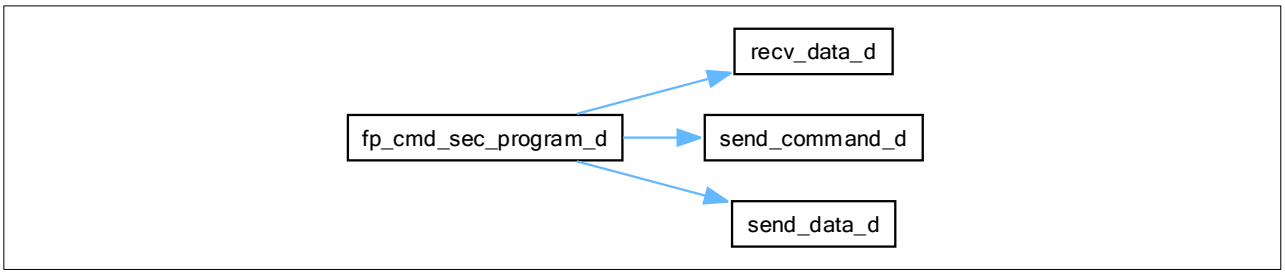
6.3.39 fp_cmd_sec_program_d

Table 6-43 shows the function description and Figure 6-24 shows the call graph.

Table 6-43 Description of fp_cmd_sec_program_d

fp_cmd_sec_program_d	
Outline	Secure Programming command sending processing
Declaration	uint8_t fp_cmd_program_d(const uint32_t start, const uint32_t end, const uint8_t * sec_data1, const uint8_t * sec_data2, const uint8_t __far * data)
Argument	const uint32_t start: Start address for rewriting const uint32_t end: End address for rewriting const uint8_t * sec_data1: Registration data for secure data 1 const uint8_t * sec_data2: Registration data for secure data 2 const uint8_t * data: Writing data
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the Secure Programming command of the RL78 protocol D.

Figure 6-24 Call Graph of fp_cmd_sec_program_d



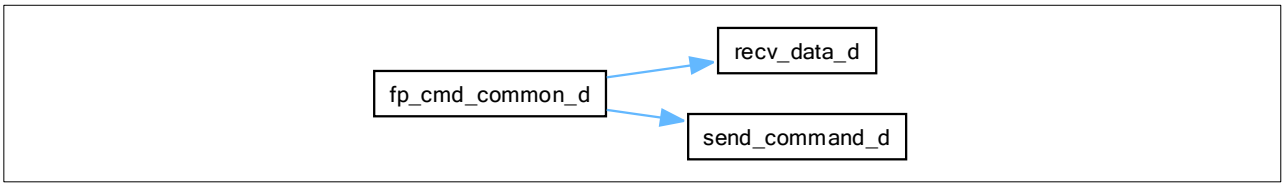
6.3.40 fp_cmd_common_d

Table 6-44 shows the function description and Figure 6-25 shows the call graph.

Table 6-44 Description of fp_cmd_common_d

fp_cmd_common_d	
Outline	Common command of RL78 protocol D processing.
Declaration	uint8_t fp_common_d(const uint8_t cmd, const uint8_t *data, const uint16_t data_len)
Argument	const uint8_t cmd: Communication packet command code const uint8_t *data: Communication packet command code const uint16_t data_len: Communication packet data size
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute the common command of the RL78 protocol D.

Figure 6-25 Call Graph of fp_cmd_common_d



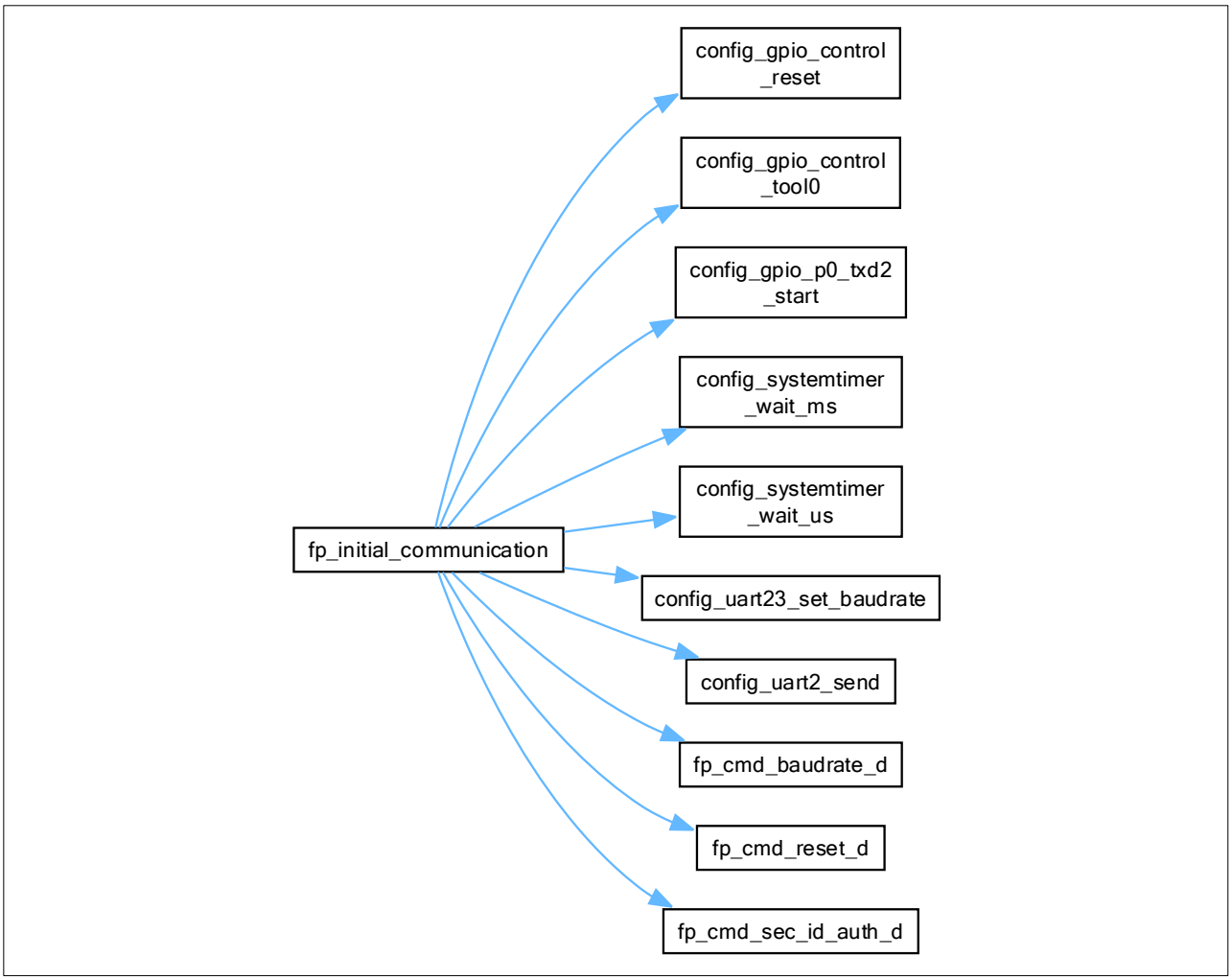
6.3.41 fp_initial_communication

Table 6-45 shows the function description and Figure 6-26 shows the call graph.

Table 6-45 Description of fp_initial_communication

fp_initial_communication	
Outline	Initial processing for starting communication (Communication establishment, authentication phase)
Declaration	uint8_t fp_initial_communication(st_command_data_t * command)
Argument	st_command_data_t * command: Setting information for command
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Perform initial communication with the target MCU according to the RL78 protocol D. Release a reset, send the mode information, and execute the Baud Rate Set command, the Reset command and the Security ID Authentication Note 1 command. Note1. If the command number error occurs when executing the Reset command.

Figure 6-26 Call Graph of fp_initial_communication



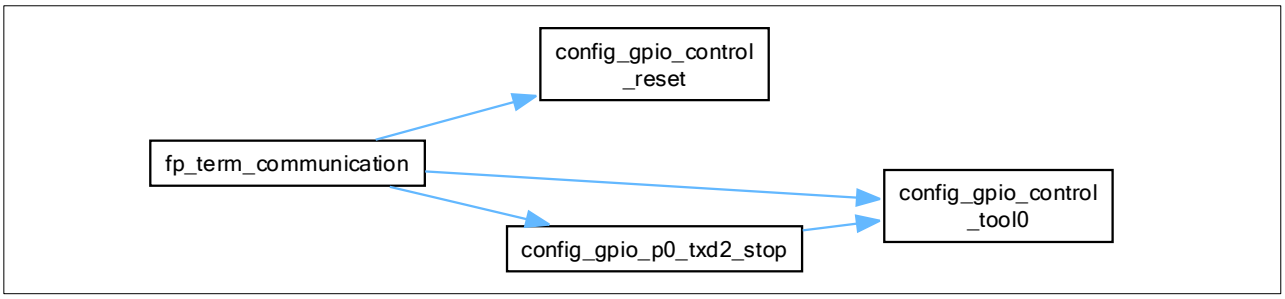
6.3.42 fp_term_communication

Table 6-46 shows the function description and Figure 6-27 shows the call graph.

Table 6-46 Description of fp_term_communication

fp_term_communication	
Outline	Control of target MCU pins
Declaration	void fp_term_communication(void)
Argument	None
Return Value	None
Description	Set the TOOL0 pin of the target MCU to a low output and the reset pin to a high output.

Figure 6-27 Call Graph of fp_term_communication



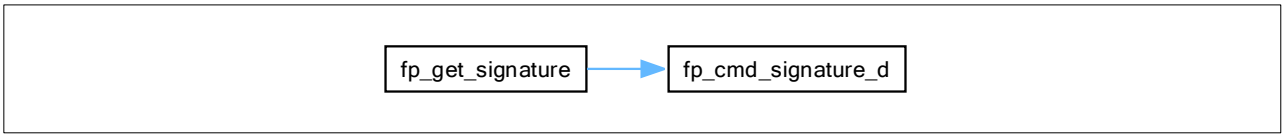
6.3.43 fp_get_signature

Table 6-47 shows the function description and Figure 6-28 shows the call graph.

Table 6-47 Description of fp_get_signature

fp_get_signature	
Outline	Executing Silicon Signature command and acquiring each parameter
Declaration	uint8_t fp_get_signature (st_command_data_t * command)
Argument	st_command_data_t * command: Setting information for command
Return value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Acquire the signature information of the target MCU and set it in the argument com_data.

Figure 6-28 Call Graph of fp_get_signature



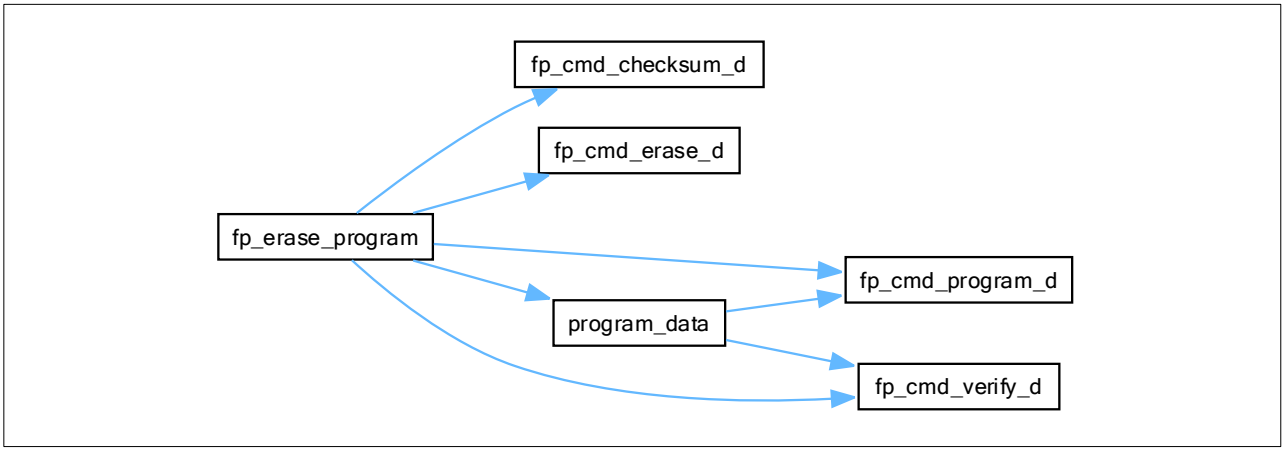
6.3.44 fp_erase_program

Table 6-48 shows the function description and Figure 6-29 shows the call graph.

Table 6-48 Description of fp_erase_program

fp_erase_program	
Outline	Codedata flash rewrite processing
Declaration	uint8_t fp_erase_program(const uint32_t start_addr, const uint32_t end_addr, const uint8_t * program_data, st_command_data_t * command)
Argument	const uint32_t start_addr: Start address const uint32_t end_addr: End address const uint8_t * program_data: Writing data COMMAND_DATA * command: Command data
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Perform the code/data flash rewriting process for the RL78 protocol D.

Figure 6-29 Call Graph of fp_erase_program



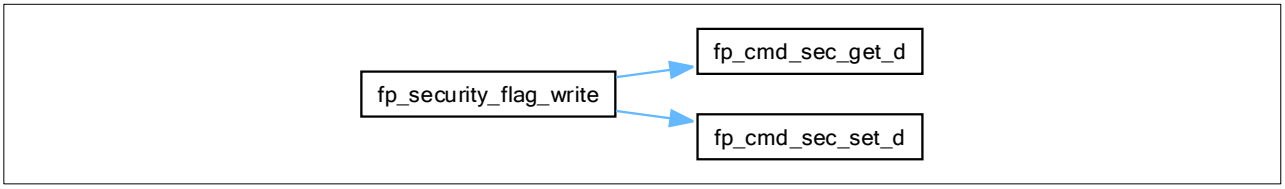
6.3.45 fp_security_flag_write

Table 6-49 shows the function description and Figure 6-30 shows the call graph.

Table 6-49 Description of fp_security_flag_write

fp_security_flag_write	
Outline	Security flag rewrite processing
Declaration	uint8_t fp_security_flag_write(const uint8_t sf1, const uint8_t sf2, const uint8_t rsv)
Argument	const uint8_t sf1: Security flag 1 const uint8_t sf2: Security flag 2 const uint8_t rsv: Reserved (fixed to 0x00)
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Perform a rewrite of the security flag in the RL78 protocol D.

Figure 6-30 Call Graph of fp_security_flag_write



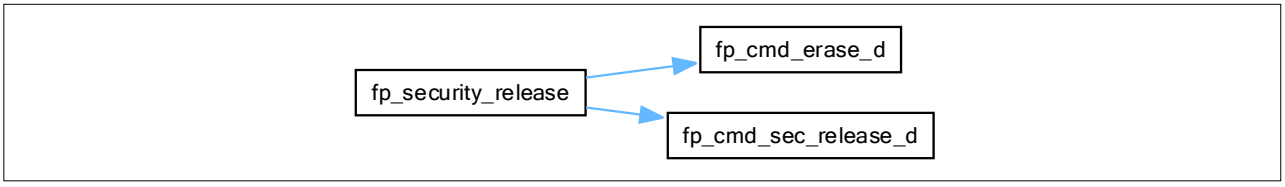
6.3.46 fp_security_release

Table 6-50 shows the function description and Figure 6-31 shows the call graph.

Table 6-50 Description of fp_security_release

fp_security_release	
Outline	Security release processing
Declaration	uint8_t fp_security_release(const uint32_t cf_start, const uint32_t cf_end, const uint32_t df_start, const uint32_t df_end)
Argument	const uint32_t cf_start: Start address of code flash memory const uint32_t cf_end: End address of code flash memory const uint32_t df_start: Start address of data flash memory const uint32_t df_end: End address of data flash memory
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Perform the security release procedure for the RL78 protocol D.

Figure 6-31 Call Graph of fp_security_release



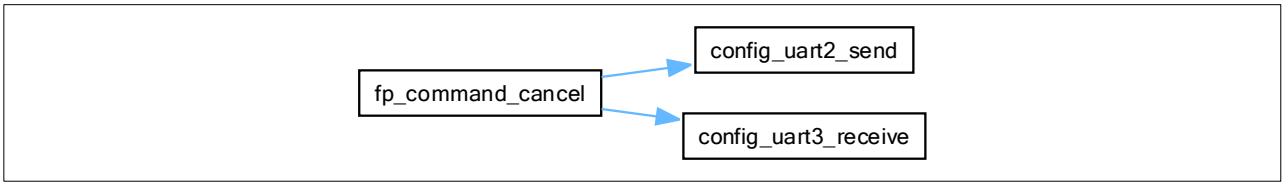
6.3.47 fp_command_cancel

Table 6-51 shows the function description and Figure 6-32 shows the call graph.

Table 6-51 Description of fp_command_cancel

fp_command_cancel	
Outline	Command cancel processing
Declaration	void fp_command_cancel(void)
Argument	None
Return Value	None
Description	Performing processing when a command is canceled in RL78 protocol D.

Figure 6-32 Call Graph of fp_command_cancel



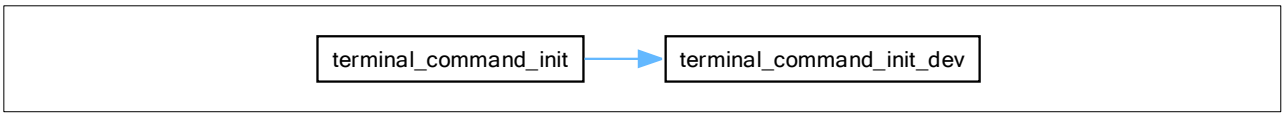
6.3.48 terminal_command_init

Table 6-52 shows the function description and Figure 6-33 shows the call graph.

Table 6-52 Description of terminal_command_init

terminal_command_init	
Outline	Initialization of each parameter
Declaration	void terminal_command_init(st_command_data_t * com_data)
Argument	st_command_data_t * com_data: Setting information for command
Return Value	None
Description	Initialize the argument com_data.

Figure 6-33 Call Graph of terminal_command_init



6.3.49 terminal_command_init_dev

Table 6-53 shows the function description.

Table 6-53 Description of terminal_command_init_dev

terminal_command_init_dev	
Outline	Initialization of each device-dependent parameter
Declaration	void terminal_command_init_dev(st_command_data_t * com_data)
Argument	st_command_data_t * com_data: Settings information for command
Return Value	None
Description	Initialize the signature information of the com_data argument.

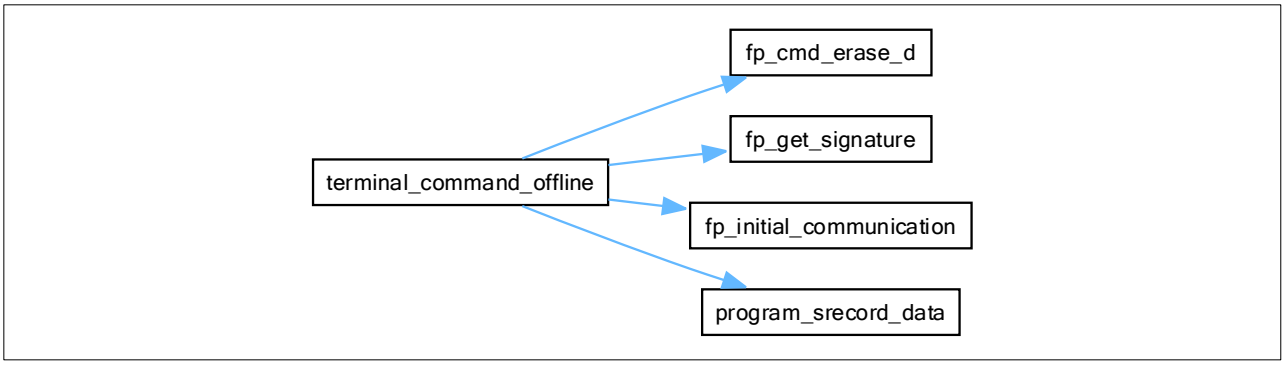
6.3.50 terminal_command_offline

Table 6-54 shows the function description and Figure 6-34 shows the call graph.

Table 6-54 Description of terminal_command_offline

terminal_command_offline	
Outline	Executing Flash rewriting processing
Declaration	void terminal_command_init(st_command_data_t * com_data)
Argument	st_command_data_t * com_data: Settings information for command
Return Value	None
Description	Read S-record data from a file and execute the flash memory rewriting process.

Figure 6-34 Call Graph of terminal_command_offline



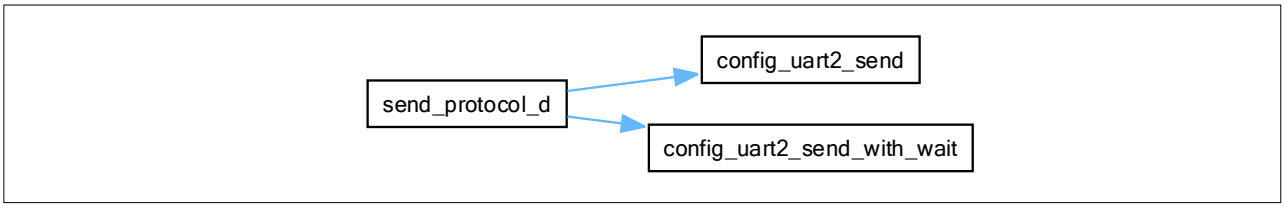
6.3.51 send_protocol_d

Table 6-55 shows the function description and Figure 6-35 shows the call graph.

Table 6-55 Description of send_protocol_d

send_protocol_d	
Outline	Command transmission processing
Declaration	static e_md_status_t send_protocol_d(const uint16_t st_data, const uint8_t cmd, const uint8_t * data, const uint16_t data_len, const uint16_t et_data)
Argument	const uint16_t st_data: First data of transmit packet const uint8_t cmd: Command code const uint8_t * data: Transmit data const uint16_t data_len: Transmit data size const uint16_t et_data: Last data of packet
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute command transmission processing of RL78 protocol D.

Figure 6-35 Call Graph of send_protocol_d



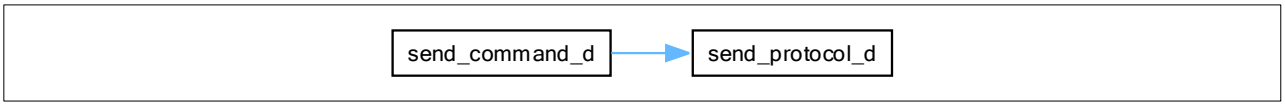
6.3.52 send_command_d

Table 6-56 shows the function description and Figure 6-36 shows the call graph.

Table 6-56 Description of send_command_d

send_command_d	
Outline	Command transmission processing
Declaration	static e_md_status_t send_command_d(const uint8_t cmd, const uint8_t * data, const uint16_t data_len)
Argument	const uint8_t cmd: Command code const uint8_t * data: Transmit data const uint16_t data_len: Transmit data size
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Create packets according to RL78 protocol D and execute command transmission processing.

Figure 6-36 Call Graph of send_command_d



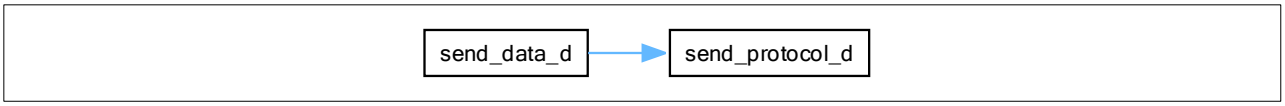
6.3.53 send_data_d

Table 6-57 shows the function description and Figure 6-37 shows the call graph.

Table 6-57 Description of send_data_d

send_data_d	
Outline	Data transmission processing
Declaration	static e_md_status_t send_data_d(const uint8_t * data, const uint16_t data_len, const uint16_t et_data)
Argument	const uint8_t * data: Transmit data const uint16_t data_len: Transmit data size const uint16_t et_data: Last data of transmit packet
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute data transmission processing of RL78 protocol D.

Figure 6-37 Call Graph of send_data_d



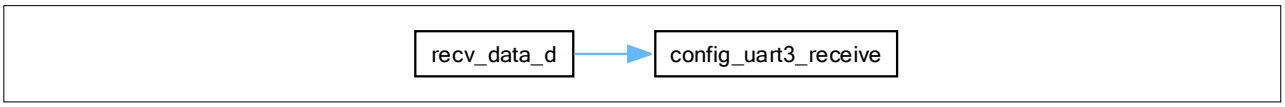
6.3.54 recv_data_d

Table 6-58 shows the function description and Figure 6-38 shows the call graph.

Table 6-58 Description of recv_data_d

recv_data_d	
Outline	Data reception processing
Declaration	static uint8_t recv_data_d(uint8_t ** data, uint16_t data_len, uint8_t * et_data, const uint16_t timeout)
Argument	uint8_t ** data: Reception data uint16_t data_len: Reception data size uint8_t * et_data: Last data of reception packet const uint16_t timeout: Timeout time
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Execute data reception processing of RL78 protocol D.

Figure 6-38 Call Graph of recv_data_d



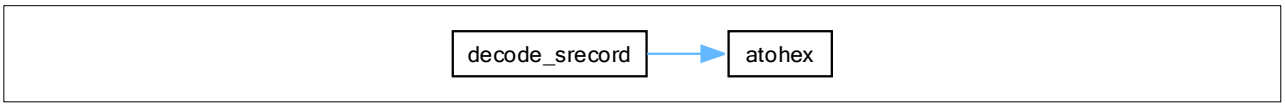
6.3.55 decode_srecord

Table 6-59 shows the function description and Figure 6-39 shows the call graph.

Table 6-59 Description of decode_srecord

decode_srecord	
Outline	Analyzing Motorola S-format file
Declaration	static void decode_srecord(uint8_t * srecord_str, uint16_t len, st_srecord_data_t * srecord_data)
Argument	uint8_t * srecord_str: Record data storage buffer uint16_t len: Length of record data st_srecord_data_t * srecord_data: Record data information
Return Value	None
Description	Analyze 1 line of the Motorola S-format file and acquires the record type, address, data, and data size as the record data information.

Figure 6-39 Call Graph of decode_srecord



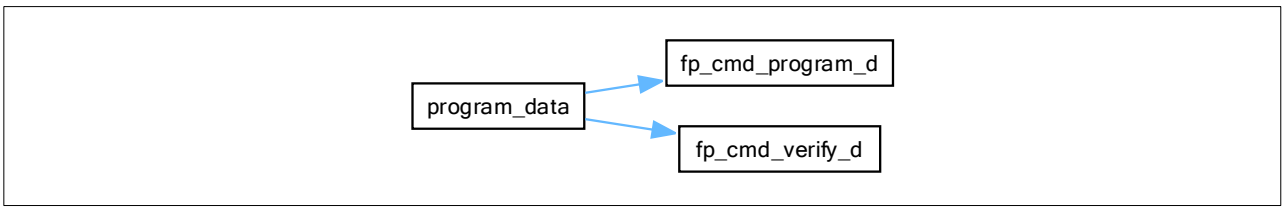
6.3.56 program_data

Table 6-60 shows the function description and Figure 6-40 shows the call graph.

Table 6-60 Description of program_data

program_data	
Outline	Writing data of the specified size
Declaration	static e_md_status_t program_data(uint32_t start_addr, uint32_t end_addr, uint8_t * data, uint8_t is_verified)
Argument	uint32_t start_addr: Start address for writing uint32_t end_addr: End address for writing uint8_t * data: Writing data uint8_t is_verified: Verify flag
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Write data only within the range defined by the start and end addresses.

Figure 6-40 Call Graph of program_data



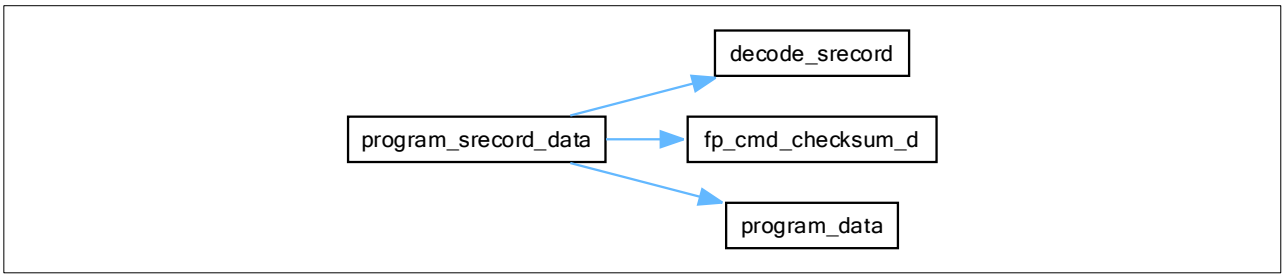
6.3.57 program_srecord_data

Table 6-61 shows the function description and Figure 6-41 shows the call graph.

Table 6-61 Description of program_srecord_data

program_srecord_data	
Outline	Writing Motorola S-format data
Declaration	static e_md_status_t program_srecord_data(st_command_data_t * com_data)
Argument	st_command_data_t * com_data: Setting information for command
Return Value	0: Normal end Other than 0: Abnormal end (refer to 4.2 Error Code Specifications)
Description	Write the parsed Motorola S-format data.

Figure 6-41 Call Graph of program_srecord_data



7. Reference Document

RL78 Microcontroller (RL78 Protocol D) Serial Programming Guide (R01AN6278)

The latest versions can be downloaded from the Renesas Electronics website.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	2025/10/27	-	First edition

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)
   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.
2. Processing at power-on
   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.
3. Input of signal during power-off state
   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.
4. Handling of unused pins
   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.
5. Clock signals
   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.
6. Voltage application waveform at input pin
   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).
7. Prohibition of access to reserved addresses
   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.
8. Differences between products
   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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