

Instructions for ICS5xx Demo Board

The ICS5xx demo board provides a way to quickly evaluate the performance of ICS' family of low cost 8 pin, compatible oscillator replacement clock generators. The connector on the bottom of the board fits a standard oscillator footprint, or it can be easily modified to fit a half size footprint.

Frequency Select Jumpers

The resistor locations R2, R3, R4, R5, R7, and R8 provide a location to jumper the input select pins to V_{DD} or GROUND. Refer to the device datasheet for frequency select tables. Small pieces of wire or 0 ohm chip resistors can be used for jumpers. R6 can be mounted if the Output Enable (tri-states the clock output) is to be used.

Output Termination

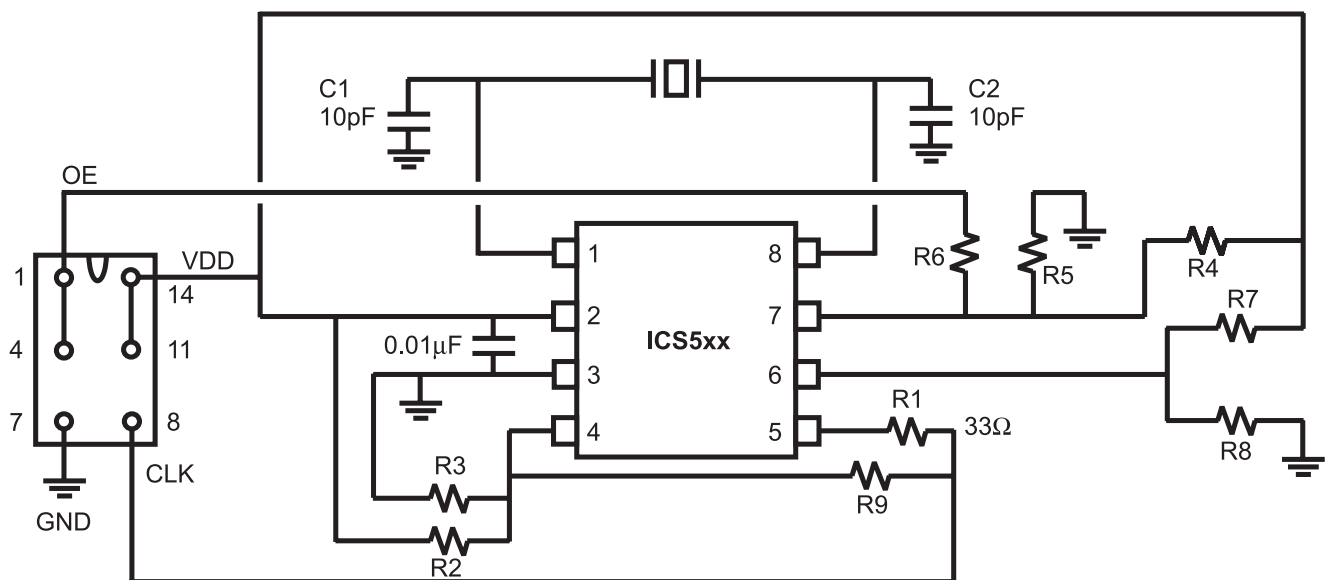
Resistor R1 is a 33 ohm series termination resistor on the clock output, which can be moved to the R9 location to evaluate parts which provide a second clock output on pin 4.

Crystal Tuning

Some of the ICS5xx family have on-chip crystal load capacitors which will provide accurate frequency output with 13 pF parallel resonant crystals. Consult datasheet for particular device. Crystals with any load capacitance value, or even series resonant crystals, will operate, but the frequency will be off by up to 500 ppm (0.05 %). To get an accurate frequency output by adjusting the board to a crystal with a different value of load capacitance, use the formula:

$$C1, C2 = 2(C_{L \text{ (crystal)}} - 13)$$

The 10 pF capacitors on the board adjust the load capacitance of the part so that the frequency is correct when used with an 18 pF parallel resonant crystal.



IMPORTANT NOTICE AND DISCLAIMER

RENESAS ELECTRONICS CORPORATION AND ITS SUBSIDIARIES ("RENESAS") PROVIDES TECHNICAL SPECIFICATIONS AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS OR IMPLIED, INCLUDING, WITHOUT LIMITATION, ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, OR NON-INFRINGEMENT OF THIRD-PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for developers who are designing with Renesas products. You are solely responsible for (1) selecting the appropriate products for your application, (2) designing, validating, and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. Renesas grants you permission to use these resources only to develop an application that uses Renesas products. Other reproduction or use of these resources is strictly prohibited. No license is granted to any other Renesas intellectual property or to any third-party intellectual property. Renesas disclaims responsibility for, and you will fully indemnify Renesas and its representatives against, any claims, damages, costs, losses, or liabilities arising from your use of these resources. Renesas' products are provided only subject to Renesas' Terms and Conditions of Sale or other applicable terms agreed to in writing. No use of any Renesas resources expands or otherwise alters any applicable warranties or warranty disclaimers for these products.

(Disclaimer Rev.1.01)

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact Information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit www.renesas.com/contact-us/.

Trademarks

Renesas and the Renesas logo are trademarks of Renesas Electronics Corporation. All trademarks and registered trademarks are the property of their respective owners.