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# 16

H8S/2215 USB Function Module Human Interface Devices (HID) Class

**Application Note** 

Renesas 16-Bit Single-Chip Microcomputer H8S Family / H8S/2200 Series



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### Preface

These application notes describe the HID class firmware that uses the USB Function Module in the H8S/2215. They are provided to be used as a reference when the user creates USB Function Module firmware.

These application notes describe a system configuration example for HID class communications based on the USB Function Module, and do not guarantee the contents of the configuration.

In addition to these application notes, the manuals listed below are also available for reference when developing applications.

[Related manuals]

- Universal Serial Bus Specification Revision 1.1
- Universal Serial Bus Device Class Definition for Human Interface Devices (HID)
- H8S/2215 Group Hardware Manual
- H8S/2215 Solution Engine CPU Board (MS2215CP01-C/S) Instruction Manual
- H8S Family E6000 Emulator User's Manual
- [Caution] The sample programs described in these application notes do not include firmware related to bulk transfer and isochronous transfer, which are USB transfer types. When using these transfer types (see section 15.5.6 to section 15.5.9 of the H8S/2215 Group Hardware Manual), the user needs to create the programs for them.

Also, the hardware specifications of the H8S/2215 and H8S/2215 Solution Engine, which will be necessary when developing the system described above, are described in these application notes, but more detailed information is available in the H8S/2215 Group Hardware Manual and the H8S/2215 Solution Engine Instruction Manual.

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# Section 1 Overview

This application note describes how to use the USB Function Module that is built into the H8S/2215, and contain examples of firmware programs.

The features of the USB Function Module contained in the H8S/2215 are listed below.

- An on-chip UDC (USB Device Controller) conforming to USB 1.1
- Automatic processing of USB protocol
- Automatic processing of USB standard commands for endpoint 0 (some commands need to be processed through the firmware)
- Full-speed (12 Mbps) transfer supported
- Various interrupt signals needed for USB transmission and reception are generated.
- Internal system clock (16 MHz) multiplied by three or external input clock (48 MHz) can be selected as the USB operating clock by the USB clock selector in the clock pulse generator.
- An on-chip bus transceiver
- Endpoint configuration selectable

| Endpoint Name          | Name | Transfer Type        | Max. Packet<br>Size | FIFO Buffer<br>Capacity | DMA<br>Transfer |
|------------------------|------|----------------------|---------------------|-------------------------|-----------------|
| Endpoint 0             | EP0s | Setup                | 8 bytes             | 8 bytes                 | _               |
|                        | EP0i | Control-in           | 64 bytes            | 64 bytes                | _               |
|                        | EP0o | Control-out          | 64 bytes            | 64 bytes                | _               |
| Endpoint<br>(optional) | EPn  | Interrupt (in)       | 64 bytes            | 64 bytes (variable)     |                 |
| Endpoint<br>(optional) | EPn  | Bulk-in              | 64 bytes            | 64 x 2 (128 bytes)      | Possible        |
| Endpoint<br>(optional) | EPn  | Bulk-out             | 64 bytes            | 64 x 2 (128 bytes)      | Possible        |
| Endpoint<br>(optional) | EPn  | lsochronous<br>(in)  | 128 bytes           | 128 x 2 (variable)      |                 |
| Endpoint<br>(optional) | EPn  | lsochronous<br>(out) | 128 bytes           | 128 x 2 (variable)      |                 |
| Endpoint<br>(optional) | EPn  | Bulk-in              | 64 bytes            | 64 x 2 (128 bytes)      | Possible        |
| Endpoint<br>(optional) | EPn  | Bulk-out             | 64 bytes            | 64 x 2 (128 bytes)      | Possible        |
| Endpoint<br>(optional) | EPn  | Interrupt (in)       | 64 bytes            | 64 bytes (variable)     | _               |

#### **Endpoint Configurations**



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Figure 1.1 shows an example of a system configuration.



Figure 1.1 System Configuration Example

This system is configured of the H8S/2215 Solution Engine manufactured by Hitachi ULSI Systems Co., Ltd. (hereafter referred to as the MS2215CP) and a PC containing Windows® Me/ Windows® 2000/Windows® XP operating system.

This system is an HID class firmware that automatically generates pseudo mouse data on the MS2215CP board and outputs the mouse data (hereafter called the HID data) to the host PC through the USB.

It is also possible to use the USB HID class device driver that comes as an accessory with the operating systems listed above.

This system offers the following features.

- 1. The sample program can be used to evaluate the USB module of the H8S/2215 quickly.
- 2. The sample program supports USB control transfer and interrupt transfer.
- 3. An E6000 can be used, enabling efficient debugging.
- 4. Additional programs can be created to support bulk transfer and isochronous transfer. \*
- Note: \* Bulk transfer and isochronous transfer programs are not provided, and will need to be created by the user.

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# Section 2 Overview of the USB Human Interface Devices (HID) Class

This section describes the USB Human Interface Devices (HID) Class.

We hope that it will provide a convenient reference for use when developing USB HID class devices. For more detailed information on standards, please see the following:

- Device Class Definition for Human Interface Devices (HID) Version 1.11
- HID Usage Tables Version 1.11

#### 2.1 HID Class

USB HID class is a class of standards that apply to devices through which humans operate PCs. Typical examples include mouse devices, keyboards, and joysticks.

To notify the host PC of this class of function, the bInterfaceClass filed of the Interface descriptor must be 0x03.

#### 2.2 Subclass Code

Subclasses were intended to be used to identify the specific protocols of different HID class devices. However, as there are many types of devices used by humans, subclass protocol definitions are impractical, and subclasses are not used to define most protocols in the HID class. Instead, the protocol is identified by the Report descriptor in HID class devices.

As for BIOS-support devices (boot devices), a simple method to identify the protocol is needed. For this purpose, subclasses are used to indicate devices that support the predefined protocol (boot protocol) for mouse devices or keyboards (that is, devices that can be used for boot devices).

To notify the host PC that the device supports the boot protocol, the bInterfaceSubClass filed of the Interface descriptor must be 0x01.

#### 2.3 Protocol Code

When a device supports the boot protocol (subclass code other than 0), a protocol code is used to indicate the device type. The protocol code is 0x01 for a keyboard, and 0x02 for a mouse. Specifying the device type by the protocol code indicates that the device can use the protocol for the device type.

To notify the host PC of the device type, the bInterfaceProtocol filed of the Interface descriptor must be a value corresponding to the device type.



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#### 2.4 Descriptors for HID Class

HID class function devices need an HID descriptor, a Report descriptor, and a Physical descriptor (optional) in addition to descriptor information that other USB function devices need. Figure 2.1 shows the HID device descriptor configuration.



Figure 2.1 Descriptor Configuration

#### 2.5 HID Descriptor

The HID descriptor combines the Report descriptor and Physical descriptor (optional). Table 2.1 shows the format of the HID descriptor.

| Field             | Size (bytes) | Description  |
|-------------------|--------------|--|
| bLength           | 1            | Descriptor size (fixed to 0x09)  |
| bDescriptorType   | 1            | Descriptor type (fixed to 0x21)  |
| bcdHID            | 2            | HID version in BCD   |
| bCountryCode      | 1            | Country ID for devices specific to a particular country (0 unless necessary) |
| bNumDescriptors   | 1            | Number of class descriptors  |
| bDescriptorType   | 1            | Type of class descriptor (0x22 for HIDREPORT)                                |
| wDescriptorLength | 2            | Size of Report descriptor  |

#### Table 2.1 HID Descriptor



#### 2.6 Report Descriptor

The Report descriptor specifies the format of data to be transferred between the host PC and the device. Unlike other descriptors, the Report descriptor has no standardized format, but the length and contents of the Report descriptor vary depending on the device's report or the number of data fields required for the device's report.

The Report descriptor consists of items that provide information about the device. There are two types of items, short and long items. The following describes the short item.



Figure 2.2 Report Descriptor Item

An item consists of four fields: data, item tag, item type, and itemSize. The item uses these fields to indicate the information.

There are three item types: Main, Global, and Local. The Main item type (defining or grouping the data fields in a Report descriptor) has five types of item tags, the Global item type (describing data) has 12, and the Local item type (defining the characteristics) has ten.

By combining these item tags, the Report descriptor specifies the format of data to be transferred between the host PC and the device.



#### 2.6.1 Main Items

Table 2.2 shows five item tags for the Main item type.

| Item Tag       | bTag | bType | bSize | Description  |
|----------------|------|-------|-------|--|
| Input          | 1000 | 00    | nn    | Describes information about data provided by<br>one or more physical controls  |
| Output         | 1001 | 00    | nn    | Defines output data field  |
| Feature        | 1011 | 00    | nn    | Describes device configuration information that can be sent to the device  |
| Collection     | 1010 | 00    | nn    | Starts collecting relations between two or more data item tags (Input, Output, or Feature)                               |
| End Collection | 1100 | 00    | nn    | Ends collecting relations between two or more<br>data item tags (Input, Output, or Feature) in<br>response to Collection |

#### Table 2.2Item Tags for Main Item Type



**Input Item Tag:** The input item tag has eight parameters (data fields), which are set in 1-bit units, as shown in table 2.3.

| Bit  | Value | Contents         | Description   |
|------|-------|------------------|---|
| 0    | 0     | Data             | The item reports data   |
|      | 1     | Constant         | The item reports a constant   |
| 1    | 0     | Array            | The item reports an array data field  |
|      | 1     | Variable         | The item reports a variable   |
| 2    | 0     | Absolute         | The item reports an absolute value  |
|      | 1     | Relative         | The item reports a relative value from the last report  |
| 3    | 0     | No Wrap          | The value reported by the item does not roll over   |
|      | 1     | Wrap             | The value reported by the item rolls over (for example,<br>for a dial to output a value from 0 to 10, if dialing is<br>continued, 0 is output after 10) |
| 4    | 0     | Linear           | The item reports the state of the target control linearly   |
|      | 1     | Non Linear       | The item processes raw data and does not report the state of the target linearly  |
| 5    | 0     | Preferred State  | The item has a state to which it returns when it is not controlled by the user  |
|      | 1     | No Preferred     | The item does not have a state to which it returns when it is not controlled by the user  |
| 6    | 0     | No Null position | The item has a state in which it does not send meaningful data  |
|      | 1     | Null state       | The item does not have a state in which it does not send meaningful data  |
| 7    | 0     | Reserved         | Reserved  |
| 8    | 0     | Bit Field        | The item issues a bit field   |
|      | 1     | Buffered Bytes   | The item issues a stream fixed to 1-byte size   |
| 9-31 | 0     | Reserved         | Reserved  |

 Table 2.3
 Input Item Tag Parameters



**Output and Feature Item Tags:** The output and feature item tags have nine parameters (data fields), which are the same as the input item tag except bit 7, as shown in table 2.4.

| Bit  | Value | Contents     | Description  |  |  |  |
|------|-------|--------------|--|--|--|--|
| 1-6  | —     | —            | Same as the input item tag                                     |  |  |  |
| 7    | 0     | Non Volatile | The item value cannot change with or without host interactions |  |  |  |
|      | 1     | Volatile     | The item value can change with or without host interactions    |  |  |  |
| 8-31 |       | _            | Same as the input item tag                                     |  |  |  |

 Table 2.4
 Output and Feature Item Tag Parameters



**Collection Item Tag:** The collection item tag has eight parameters (data fields), which are set in one byte, as shown in table 2.5.

#### Value Contents Description 0x00 Used for data items collected into one. This is used for Physical devices which need to associate correct or sensed data with a single point. It does not indicate that data comes from a single device such as a keyboard. It indicates that the device reports multiple sensor positions and data comes from different sensors. 0x01 Application Identifies the Usage only used for the application level. It indicates that the collection is a functionally subordinate group of an HID device or a complex device. The operating system uses the Usage associated with this collection to link to the application or driver that controls the device. 0x02 Logical Used when data items compose a composite data structure. 0x03 Report Defines a logical collection that includes all fields. A report ID is included in this collection. An application can easily determine whether to support a certain function of the device. 0x04 Named Array Used when data items compose a composite data structure and it is named. 0x05 Usage Switch A logical collection that modifies the meaning of the included Usage. It identifies the Usage applied for logical collection to modify the purpose of the Usage being collected. 0x06 Usage Modifier Modifies the meaning of the Usage attached to the including collection. The Usage typically defines a single operating mode for control, which enables the operating method of control to be expanded. 0x07-7F Reserved Reserved. 0x80-FF Vendor-defined. Defined by the vendor.

#### Table 2.5 Collection Item Tag Parameters



#### 2.6.2 Global Items

Table 2.6 shows 12 item tags for the Global item type.

| Item Tag         | bTag | Tag bType |    | Description   |  |  |
|------------------|------|-----------|----|---|--|--|
| Usage Page       | 0000 | 01        | nn | A value specifying the current Usage Page. It defines the index to the item usage.  |  |  |
| Logical Minimum  | 0001 | 01        | nn | The minimum value to be reported by a variable<br>or array item. For example, the mouse that<br>reports an X position value from 0 to 128 will<br>have a minimum logical value of 0.                                    |  |  |
| Logical Maximum  | 0010 | 01        | nn | The maximum value to be reported by variable<br>or array items. For example, the mouse that<br>reports an X position value from 0 to 128 will<br>have a maximum logical value of 128.                                   |  |  |
| Physical Minimum | 0011 | 01        | nn | Minimum value of physical range for a variable item   |  |  |
| Physical Maximum | 0100 | 01        | nn | Maximum value of physical range for a variable item   |  |  |
| Unit Exponent    | 0101 | 01        | nn | Unit exponent in base 10  |  |  |
| Unit             | 0110 | 01        | nn | Unit value  |  |  |
| Report Size      | 0101 | 01        | nn | Unsigned value that specifies the report field size in bits   |  |  |
| Report ID        | 1000 | 01        | nn | Unsigned value that specifies the report ID   |  |  |
| Report Count     | 1001 | 01        | nn | Specifies the number of data fields for the item.<br>An unsigned integer specifies how many fields<br>can be included in the report for the particular<br>item (accordingly, how many bits are added to<br>the report). |  |  |
| Push             | 1010 | 01        | nn | Places a copy of the Global Item state table in the stack   |  |  |
| Рор              | 1011 | 01        | nn | Replaces the item state table with the top data in the stack.   |  |  |

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#### Table 2.6Item Tags for Global Item Type

#### 2.6.3 Local Items

Table 2.7 shows ten item tags for the Local item type.

| Item Tag              | bTag | bType | bSize | Description   |
|-----------------------|------|-------|-------|---|
| Usage                 | 0000 | 10    | nn    | A value specifying the current Usage. It defines the index to the items usage.  |
| Usage Minimum         | 0001 | 10    | nn    | Defines the start of Usage associated with an array or a bitmap.  |
| Usage Maximum         | 0010 | 10    | nn    | Defines the end of Usage associated with an array or a bitmap.  |
| Designator Index      | 0011 | 10    | nn    | Determines the body part used for control.  |
| Designator<br>Minimum | 0100 | 10    | nn    | Defines the start index to the designator associated with an array or a bitmap.   |
| Designator<br>Maximum | 0101 | 10    | nn    | Defines the end index to the designator associated with an array or a bitmap.   |
| String Index          | 0111 | 10    | nn    | Index to the String descriptor, which enables the string to be associated with a particular item or control                   |
| String Minimum        | 1000 | 10    | nn    | Specifies the first string index when associating<br>a group of sequential strings to the control in an<br>array or a bitmap. |
| String Maximum        | 1001 | 10    | nn    | Specifies the end string index when associating<br>a group of sequential strings to the control in an<br>array or a bitmap.   |
| Delimiter             | 1010 | 10    | nn    | Defines the start or end of a set of Local items.   |

#### Table 2.7Item Tags for Local Item Type



#### 2.6.4 Sample Report Descriptor

Figure 2.3 shows the Report descriptor of this sample program.

| Usage Page (Generic Desktop),                        | :05 01          |
|--|-----------------|
| Usage (Mouse),                                       | :09 02          |
| Collection (Application),                            | :A1 01          |
| Usage (Pointer),                                     | :09 01          |
| Collection (Physical),                               | :A1 00          |
| Usage Page (Buttons),                                | :05 09          |
| Usage Minimum (01),                                  | :19 01          |
| Usage Maximum (03),                                  | :29 03          |
| Logical Minimum (0),                                 | :15 00          |
| Logical Maximum (1),                                 | : 25 01         |
| Report Count (3),                                    | :95 03          |
| Report Size (1),                                     | : 75 01         |
| Input (Data, Variable, Absolute), ; 3 button bits    | :81 02          |
| Report Count (1),                                    | :95 01          |
| Report Size (5),                                     | :75 05          |
| Input (Constant), ; 5 bit padding                    | :81 01          |
| Usage Page (Generic Desktop),                        | :05 01          |
| Usage (X),   | :09 30          |
| Usage (Y),   | :09 31          |
| Usage (Wheel),                                       | :09 38          |
| Logical Minimum (-127),                              | :15 81          |
| Logical Maximum (127),                               | :25 7F          |
| Report Size (8),                                     | :75 08          |
| Report Count (3),                                    | :95 03          |
| Input (Data, Variable, Relative), ; 2 position bytes | (X & Y) : 81 06 |
| End Collection,                                      | : C0            |
| End Collection                                       | : C0            |
|  |                 |

Figure 2.3 Report Descriptor



#### 2.6.5 Description of Report Descriptor

Table 2.8 shows the Report descriptor used by the sample program.

#### Table 2.8Report Descriptor

| Item                                    | Value<br>(hex.) | Item<br>Classification | Description   |
|---|-----------------|------------------------|---|
| Usage Page (Generic<br>Desktop Control) | 0x05 01         | Global                 | A value specifying the Usage Page. 0x01 indicates Generic Desktop Control.  |
| Usage (Mouse)                           | 0x09 02         | Local                  | Index to the item Usage. 0x02 indicates<br>Mouse. The operating system links the device<br>as a mouse to the active application or driver.<br>The Usage type of Mouse is Collection<br>Application. |
| Collection (Application)                | 0xA1 01         | Main                   | Notifies the application of Pointer as a mouse.   |
| Usage (Pointer)                         | 0x09 01         | Local                  | Index to the item Usage. 0x01 indicates<br>Pointer. The Usage type of Pointer is<br>Collection Physical.  |
| Collection (Physical)                   | 0xA1 00         | Main                   | Collects multiple sensor positions (button, X axis, Y axis, and rotary control) to one as a pointer.  |
| Usage Page (Button)                     | 0x05 09         | Global                 | A value specifying the Usage Page. 0x09 indicates Button.   |
| Usage Minimum (1)                       | 0x19 01         | Local                  | Defines that the Usage associated with an array or a bitmap starts from 1.  |
| Usage Maximum (3)                       | 0x29 03         | Local                  | Defines that the Usage associated with an array or a bitmap ends at 3.  |
| Logical Minimum (0)                     | 0x15 00         | Global                 | The minimum value to be reported by the item is 0.  |
| Logical Maximum (1)                     | 0x25 01         | Global                 | The maximum value to be reported by the item is 1.  |
| Report Count (3)                        | 0x95 03         | Global                 | Indicates the number of data fields to be used<br>for the item. This example indicates that three<br>report fields are to be used.  |
| Report Size (1)                         | 0x75 01         | Global                 | Indicates the report field size. This example indicates that 1-bit field is to be used.   |
| Input (Data, Variable,<br>Absolute)     | 0x81 02         | Main                   | Indicates the type of input item. This example indicates that the input is variable data and reports an absolute value.   |



| Item                                    | Value<br>(hex.) | Item<br>Classification | Description   |
|---|-----------------|------------------------|---|
| Report Count (1)                        | 0x95 01         | Global                 | Indicates the number of data fields to be used<br>for the item. This example indicates that one<br>report field is to be used.  |
| Report Size (5)                         | 0x75 05         | Global                 | Indicates the report field size. This example indicates that 5-bit field is to be used.   |
| Input (Constant)                        | 0x81 01         | Main                   | Indicates the type of input item. This example indicates that the input reports a constant.   |
| Usage Page (Generic<br>Desktop Control) | 0x05 01         | Global                 | A value specifying the Usage Page. 0x01 indicates Generic Desktop Control.  |
| Usage (X)                               | 0x09 30         | Local                  | Index to the item Usage. 0x30 indicates X. The controller reports X-direction values, and when the controller moves from left to right from the user's viewpoint, a value increases linearly.   |
| Usage (Y)                               | 0x09 31         | Local                  | Index to the item Usage. 0x31 indicates Y. The controller reports Y-direction values, and when the controller moves from the far side to the near side from the user's viewpoint, a value increases linearly.   |
| Usage (Wheel)                           | 0x09 38         | Local                  | Index to the item Usage. 0x38 indicates<br>Wheel. It is different from a dial; it is a rotary<br>control that generates a variable value when<br>rotated. When the controller rotates toward the<br>front (the far side from the user), a value<br>increases. |
| Logical Minimum (-127)                  | 0x15 81         | Global                 | The minimum value to be reported by the item is -127.   |
| Logical Maximum (127)                   | 0x25 7F         | Global                 | The maximum value to be reported by the item is 127.  |
| Report Size (8)                         | 0x75 08         | Global                 | Indicates the report field size. This example indicates that 8-bit field is to be used.   |
| Report Count (3)                        | 0x95 03         | Global                 | Indicates the number of data fields to be used<br>for the item. This example indicates that three<br>report fields are to be used.  |
| Input (Data, Variable,<br>Relative)     | 0x81 06         | Main                   | Indicates the type of input item. This example indicates that the input is variable data and reports the change from the last input.  |
| End Collection                          | 0xC0            | Main                   | Indicates the end of collection of data set (physical).   |
| End Collection                          | 0xC0            | Main                   | Indicates the end of collection of data set (application).  |



#### 2.7 Physical Descriptor

The physical descriptor provides information about the human body (or a specific part of the human body) that is controlling the device. This descriptor is optional, and it is omitted in the sample program.

#### 2.8 HID Data Transfer Format

HID data is transferred between the host PC and the USB function module mainly through interrupt transfers (control transfers are also available).

The boot device can use two types of protocols: report protocol and boot protocol. Other devices can only use one protocol: report protocol.

The format of data transfer used by the report protocol is described by a Report descriptor. The format used by the boot protocol is prescribed in the USB standard.

The default protocol for the boot device is the report protocol, but a class command can select either the boot or report protocol. Figure 2.4 shows the report protocol format used by the sample program.

| Bits:  | 7 to 3 | 2               | 1               | 0              |        |        |       |
|--------|--------|-----------------|-----------------|----------------|--------|--------|-------|
| Parts: | 00000  | Wheel<br>button | Right<br>button | Left<br>button | X axis | Y axis | Wheel |
| Byte:  |        | (               | )               |                | 1      | 2      | 3     |

Figure 2.4 Report Protocol Format

#### 2.9 Class Commands

Class commands are defined by each USB class. They use control transfer.

There are six commands for the USB HID class. Table 2.9 shows the class commands.

| bRequest Field Value | Command      | Meaning of Command  |
|----------------------|--------------|---|
| 0x01                 | GET_REPORT   | Transfers HID data from the device to the host PC through control transfer        |
| 0x02                 | GET_IDLE     | Returns the current value for the rate of time for which interrupt transfer stops |
| 0x03                 | GET_PROTOCOL | Reports the current active protocol (boot protocol or report protocol)            |
| 0x09                 | SET_REPORT   | Transfers HID data from the host PC to the device through control transfer        |
| 0x0A                 | SET_IDLE     | Specifies the rate of time for which interrupt transfer stops                     |
| 0x0B                 | SET_PROTOCOL | Specifies the active protocol (boot protocol or report protocol)                  |

#### Table 2.9Class Commands

Notes: 1. All devices must support GET\_REPORT.

2. Boot devices must support GET\_PROTOCOL and SET\_PROTOCOL.

When the GET\_REPORT command is received, the function sends HID data to the host through the data stage of control transfer. The report type must be specified in the upper one byte of the wValue field in the setup data and the report ID in the lower one byte of the wValue field.

When the GET\_IDLE command is received, the function returns the time for which interrupt transfer stops. The time should be expressed in time rate in 4-ms units. The host specifies the ID for the report that the host requests in the lower one byte of the wValue field in the setup data. If this value is 0, the time rates for all interrupt transfers of the target device are returned.

When the GET\_PROTOCOL command is received, the function returns the current active protocol (boot protocol or report protocol) to the host through the data state of control transfer. Value 0 indicates the boot protocol, and value 1 indicates the report protocol.

When the SET\_REPORT command is received, the function receives HID data through the data stage of control transfer. However, the function may ignore the command from the host.

When the SET\_IDLE command is received, the function stops interrupt transfer for the time specified in the upper one byte of the wValue field in the setup data. The time is expressed in time rate in 4-ms units. The lower one byte of the wValue field specifies the report ID. If this value is

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not 0, the transfer of the specified report ID is stopped. If this value is 0, all interrupt transfers of the target device are stopped.

When the SET\_PROTOCOL command is received, the function specifies the protocol (boot protocol or report protocol) to be used from that time on. The protocol is specified in the wValue filed in the setup data (value 0 indicates the boot protocol and value 1 indicates the report protocol). Note that the report protocol is the default protocol of the function.



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## Section 3 Development Environment

This section looks at the development environment used to develop this system. The devices (tools) listed below were used when developing the system.

- H8S/2215 Solution Engine (hereafter called the MS2215CP; type number: MS2215CP01-C/S) manufactured by Hitachi ULSI Systems Co., Ltd.
- E6000 (type number: HS2214EPI61H) Emulator manufactured by Renesas Technology Corp.
- H8S/2215 Group TFP120 User System Interface Cable (hereafter called the H8S/2215 user cable; type number: HS2215ECN61H) manufactured by Renesas Technology Corp.
- PC (Windows® 95/Windows®98) equipped with an ISA, PCI, or PCMCIA slot
- PC (Windows® Me/Windows® 2000/Windows® XP) to serve as the USB host
- USB cable
- Debugging Interface (hereafter called the HDI) manufactured by Renesas Technology Corp.
- High-Performance Embedded Workshop (hereafter called the HEW) manufactured by Renesas Technology Corp.



#### 3.1 Hardware Environment

Figure 3.1 shows device connections.



Figure 3.1 Device Connections

1. MS2215CP

Some jumper settings on the MS2215CP board must be changed from those at shipment. Before turning on the power, ensure that the jumpers are set as follows. There is no need to change any other jumpers.

#### Table 3.1 Jumper Settings

| At Shipment    | After Change   | Jumper Function                             |
|----------------|----------------|---|
| J9 1-2: Closed | J9 2-3: Closed | Switches the EXTAL48 pin level (to use PLL) |

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#### 2. USB host PC

A PC with Windows® XP/Windows® 2000/Windows® Millennium Edition installed, and with a USB port, is used as the USB host. This system uses the HID class device driver installed as a standard part of the Windows® XP/Windows® 2000/Windows Millennium Edition system, and so there is no need to install new drivers.

#### 3. E6000

The ISA is used for the communication interface between the E6000 PC and the E6000 emulator.

The E6000 I/F board should be inserted into an ISA slot and connected to the E6000 via an interface cable. Then, the E6000 should be connected to the MS2215CP via an H8S/2215 user cable. After connection, start the HDI and perform emulation.



#### **3.2** Software Environment

A sample program, as well as the compiler and linker used, are explained.

#### 3.2.1 Sample Program

Files required for the sample program are all stored in the H8S2215 folder. When this entire folder with its contents is moved to a PC on which HEW and HDI have been installed, the sample program can be used immediately. Files included in the folder are shown in figure 3.2 below.

| CatHidTypedef.h             | CatProTypedef.h   | CatTypedef.h     | h8s2215.h           |
|-----------------------------|-------------------|------------------|---------------------|
| SetHidInfo.h<br>SysMemMap.h | SetMacro.h        | SetSystemSwich.h | SetUsbInfo.h        |
| DoControl.c                 | DoHidDataFormat.c | DoInterrupt.c    | DoMouse.c           |
| DoRequest.c                 | StartUp.c         | UsbMain.c        | DoRequest HIDClass. |
| ch38iop (folder)            | dwfinf (folder)   | log.txt          | InkSet1.sub         |
| sct.src                     | debugger.MOT      | debugger.MAP     | debugger.HDW        |
| debugger.HDT                | debugger.hds      | debugger.ABS     | BuildOfHew.bat      |

Figure 3.2 Files Included in the Folder

#### 3.2.2 Compiling and Linking

The sample program is compiled and linked using the following software.

High-Performance Embedded Workshop Version 1.0 (release 9) (hereafter HEW)

When HEW is installed in C:\Hew\*, the procedure for compiling and linking the program is as follows.

First, a folder named Tmp should be created below the C:\Hew folder for use in compiling. (figure 3.3)



Figure 3.3 Creating a Working Folder



Next, the folder in which the sample program is stored (H8S2215) should be copied to C:\Usr (or can be copied to any location, then "C:\Usr\h8s2215" written in the debugger.hds file should be modified to the path to the copied folder). In addition to the sample program, this folder contains a batch file named BuildOfHew.bat. This batch file sets the path, specifies compile options, specifies a log file indicating the compile and linking results, and performs other operations. When BuildOfHew.bat is executed, compiling and linking are performed. As a result, an executable file named debugger.MOT, which is a file in the Motorola S-type format, is created within the folder. At the same time, a map file named debugger.MAP and a log file named log.txt are created. The map file indicates the program size and addresses of variables. The compile results (whether there are any errors etc.) are recorded in the log file (figure 3.4).

Note: If HEW is installed in a folder other than C:\Hew, the compiler path setting and settings for environment variables used by the compiler in BuildOfHew.bat, as well as the library settings in InkSet1.sub, must be changed. Here the compiler path setting should be changed to the path of ch38.exe, the setting for the environment variable ch38 used by the compiler should be set to the folder of machine.h, and the setting of ch38tmp should specify the work folder for the compiler. The library setting should specify the path of c8s26a.lib.



Figure 3.4 Compile Results



#### **3.3** Loading and Executing the Program

Figure 3.5 shows the memory map for the sample program.



Figure 3.5 Memory Map



As shown in figure 3.5, this sample program allocates areas for vectors, P, C, and D to the on-chip ROM area (E6000 emulation memory) in area 0, and the stack, B, and R areas to the on-chip RAM. These memory allocations are specified by the InkSet1.sub file in the H8S2215 folder. When modifying the program allocation, this file must be modified.

#### 3.3.1 Loading the Program

In order to load the sample program into the MS2215CP, the following procedure is used.

- Connect the E6000 PC in which the HDI has been installed to the E6000.
- Connect the E6000 to the MS2215CP through an H8S/2215 user cable.
- Turn on the power to the E6000 PC to start up the machine.
- Execute debugger.hds in the H8S2215 folder.

Through the above procedure, the sample program can be loaded into the emulation memory in the E6000.

#### **3.3.2** Executing the Program

In order to execute the program which was loaded in section 3.3.1 above, the program counter (PC) must be set appropriately.

Select Register Window from the View menu to open the Registers window. On double-clicking the numerical area of the register (PC) in the window, a dialog box appears, and the register value can be changed. Use this dialog box to set the PC to H'0000 0200.

After making the above settings, select Go from the Run menu to execute the program.



#### 3.4 Demonstrating Mouse Pointer Movements

The sample program demonstrates movements of the host PC mouse pointer without a mouse connected.

While the program is running, connect series-B connector of the USB cable to the MS2215CP, and series-A connector to the USB host PC. After control transfer is completed, the human interface devices and USB human interface devices are displayed in the device manager window, and the host PC recognizes the MS2215CP as a mouse device.

After the MS2215CP is connected to the host PC, the system starts demonstrating mouse pointer movements. The MS2215CP sends data for mouse pointer movements to the host PC in response to interrupt-in transfer from the host PC. As a result, the mouse pointer on the USB host PC automatically starts moving.



# Section 4 Overview of the Sample Program

In this section, features of the sample program and its structure are explained. This sample program is an HID class firmware, which runs on the MS2215CP and generates data for mouse pointer movements to enable the movements to be emulated on the host PC. The sample program initiates USB transfers by means of tokens from the host PC. Of the interrupts from modules in the H8S/2215, there are three interrupts related to the USB function module: EXIRQ0, EXIRQ1, and IRQ6, but in this sample program, only EXIRQ0 is used.

Features of this program are as follows.

- Control transfer can be performed.
- Interrupt-in transfer can be used to send data of mouse pointer movements to the host PC.

#### 4.1 State Transition Diagram

Figure 4.1 shows a state transition diagram for this sample program. In this sample program, as shown in figure 4.1, there are transitions between four states.



Figure 4.1 State Transition Diagram

Reset State

Upon power-on reset and manual reset, this state is entered. In the reset state, the H8S/2215 mainly performs initial settings.

Stationary State

When initial settings are completed, a stationary state is entered in the main loop.



• USB Communication State

In the stationary state, when an interrupt from the USB module occurs, this state is entered. In the USB communication state, data transfer is performed by a transfer method according to the type of interrupt. The interrupts used in this sample program are indicated by interrupt flag registers 0 to 3 (UIFR0 to UIFR3), and there are nine interrupt types in all. When an interrupt factor occurs, the corresponding bits in UIFR0 to UIFR3 are set to 1.

• Mouse Data Generation State

In the stationary state, when a compare match interrupt from 16-bit timer TGRA\_2 occurs, this state is entered. In the mouse data generation state, data of mouse pointer movements is automatically generated. A compare match interrupt occurs every 10 ms.

#### 4.2 USB Communication State

The USB communication state can be further divided into two states according to the transfer type (see figure 4.2). When an interrupt occurs, first there is a transition to the USB communication state, and then there is further branching to a transfer state according to the interrupt type. The branching method is explained in section 5, Sample Program Operation.



Figure 4.2 USB Communication State


## 4.3 File Structure

This sample program consists of eight source files and nine header files. The overall file structure is shown in table 4.1. Each function is arranged in one file by transfer method or function type.

| File Name           | Principle Role   |  |  |
|---------------------|--|--|--|
| StartUp.c           | Microcomputer default settings                               |  |  |
| UsbMain.c           | Judging the causes of interrupts                             |  |  |
| OSDIVIAII1.C        | Sending and receiving packets                                |  |  |
| DoControl.c         | Executing control transfer                                   |  |  |
| DoInterrupt.c       | Executing interrupt-in transfer                              |  |  |
| DoRequest.c         | Processing setup commands issued by the host                 |  |  |
| DoRequestHIDClass.c | Processing HID class commands                                |  |  |
| DoHidDataFormat.c   | Formatting HID data to be transferred                        |  |  |
| DoMouse.c           | Generating mouse data  |  |  |
| CatHidTypedef.h     | Defining types and structures specific to HID class          |  |  |
| CatProType.h        | Declaring prototypes   |  |  |
| CatTypedef.h        | Defining the basic structures used in USB firmware           |  |  |
| h8s2215.h           | Defining H8S/2215 registers                                  |  |  |
| SetHidInfo.h        | Default settings of variables needed to support HID class    |  |  |
| SetMacro.h          | Defining macros  |  |  |
| SetSystemSwitch.h   | System operation settings                                    |  |  |
| SetUsbInfo.h        | Default settings of variables needed to support USB firmware |  |  |
| SysMemMap.h         | Defining MS2215CP memory map addresses                       |  |  |

Table 4.1File Structure



## 4.4 **Purposes of Functions**

Tables 4.2 to 4.9 show functions contained in each file and their purposes.

#### Table 4.2UsbMain.c

| File in Which Stored | Function Name         | Purpose   |
|----------------------|-----------------------|---|
|                      | BranchOfInt           | Discriminates interrupt factors, and calls function according to interrupt  |
|                      | GetPacket             | Writes data transferred from the host controller to RAM   |
|                      | GetPacket4            | Writes data transferred from the host controller to<br>RAM in longwords (ring buffer supported, not used<br>by this sample program)     |
|                      | GetPacket4S           | Writes data transferred from the host controller to<br>RAM in longwords (ring buffer not supported, high-<br>speed version)             |
|                      | PutPacket             | Writes data for transfer to the host controller to the USB module   |
| UsbMain.c            | PutPacket4            | Writes data for transfer to the host controller to the USB module in longwords (ring buffer supported, not used by this sample program) |
|                      | PutPacket4S           | Writes data for transfer to the host controller to the<br>USB module in longwords (ring buffer not<br>supported, high-speed version)    |
|                      | SetControlOutContents | Overwrites data with that sent from the host  |
|                      | SetUsbModule          | Makes USB module initial settings   |
|                      | ActBusReset           | Clears FIFO on receiving bus reset  |
|                      | ActBusVcc             | Pulls up D+ and controls USB module when the USB cable is connected or disconnected   |
|                      | ConvRealn             | Reads data of a specified byte length from a specified address  |
|                      | ConvReflexn           | Reads data of a specified byte length from specified addresses, in reverse order  |

In UsbMain.c, interrupt factors are discriminated by the USB interrupt flag registers, and functions are called according to the interrupt type. Also, packets are sent and received between the host controller and function modules.

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#### Table 4.3 StartUp.c

| File in Which Stored | Function Name     | Purpose   |  |
|----------------------|-------------------|---|--|
|                      | SetPowerOnSection | Sets BSC, terminals, and interrupt controller, calls initialization routines, and shifts to the main loop |  |
| StartUp.c            | _INITSCT          | Copies variables that have default settings to the RAM work area  |  |
|                      | InitMemory        | Allocates memory areas  |  |
|                      | InitSystem        | Specifies the USB clock, system interrupt masks, and timers   |  |

When a power-on reset or manual reset is carried out, the SetPowerOnSection of the StartUp.c file is called. At this point, initial settings for the H8S/2215 registers or USB clock are performed.

#### Table 4.4DoRequest.c

| File in Which Stored | Function Name       | Purpose   |
|----------------------|---------------------|---|
| DoRequest.c          | DecStandardCommands | Decodes command issued by host controller,<br>and processes standard commands |
|                      | DecVenderCommands   | Processes vendor commands   |

During control transfer, commands sent from the host controller are decoded and processed. In this sample program, a vendor ID of 045B is used. When the customer develops a product, the customer should obtain a vendor ID at the USB Implementers' Forum. Because vendor commands are not used, DecVenderCommands does not perform any action. In order to use a vendor command, the customer should develop a program.

#### Table 4.5DoRequestHIDClass.c

| File in Which Stored | Function Name       | Purpose  |
|----------------------|---------------------|--|
|                      | DecHIDClassCommands | Processes HID class commands   |
| DoRequestHIDClass.c  | ActIdleCount        | This is called by an SOF interrupt, and counts the time for which interrupt transfer stops |

These functions carry out processing according to the HID class commands (GET\_REPORT, GET\_IDLE, GET\_PROTOCOL, SET\_REPORT, SET\_IDLE, and SET\_PROTOCOL).

The GET\_REPORT command sends HID data from the device to the host PC through control transfer.

The GET\_IDLE command returns the rate for the time for which interrupt transfer stops. The GET\_PROTOCOL command returns the current active protocol (boot protocol or report protocol).



The SET\_REPORT command sends HID data from the host PC to the device through control transfer, but this sample program does not support out-direction communications of HID data and only receives data.

The SET\_IDLE command specifies the rate for the time for which interrupt transfer stops. The SET\_PROTOCOL command specifies the active protocol (boot protocol or report protocol).

| File III willch Stored | Function Manie  | Fulbose   |
|------------------------|-----------------|---|
| DoControl.c            | ActControl      | Controls the setup stage of control transfer  |
|                        | ActControlIn    | Controls the data stage and status stage of control-in transfer (transfer in which the data stage is in the IN direction)   |
|                        | ActControlOut   | Controls the data stage and status stage of control-out transfer (transfer in which the data stage is in the OUT direction) |
|                        | ActControlInOut | Sorts the data stage and status stage of control transfers and direct them to ActControlIn and ActControlOut.               |

#### Table 4.6DoControl.c

When control transfer interrupt SETUP TS is generated, ActControl obtains the command, and decoding is carried out by DecStandardCommands to determine the transfer direction. Next, when control transfer interrupt EP00 TS, EP0i TR, or EP0i TS is generated, ActControlInOut calls either ActControlIn or ActControlOut depending on the transfer direction, and the data stage and status stage are carried out by the called function.

#### Table 4.7DoInterrupt.c

#### File in Which Stored Function Name Purpose

| DoInterrupt.c ActInterruptIn | On receiving the in-token of the interrupt transfer, gets data from the data transfer buffer as soon as FIFO has an empty space and prepares for interrupt transfer |
|------------------------------|---|
|------------------------------|---|

On receiving the in-token of the interrupt transfer from the host PC, this function prepares next data to be sent as soon as the interrupt transfer buffer becomes empty.



#### Table 4.8 **DoHidDataFormat.c**

| File in Which Stored | Function Name     | Purpose  |
|----------------------|-------------------|--|
| DoHidDataFormat.c    | ActMakeHidData    | A program interface for HID data communications.   |
|                      |                   | Calls ActInterruptIn if interrupt transfer stops after<br>ActReportProtocol is called.   |
|                      | ActReportProtocol | Arranges transfer data according to the format specified<br>by the Report descriptor, and writes the data to the<br>transmit buffer. |

14/1.1.1.

These functions prepare HID data to be transmitted to the host PC.

#### Table 4.9 DoMouse.c

| File in Which Stored | Function Name             | Purpose   |
|----------------------|---------------------------|---|
| DoMouse.c            | MousePushed<br>DataInput2 | This is initiated by a timer interrupt, and generates data for mouse pointer movements according to the elapsed time. |

This function uses a timer interrupt and generates data for mouse pointer movements.

Figure 4.3 shows the interrelationship between the functions explained in tables 4.2 to 4.9. The upper-side functions can call the lower-side functions. Also, multiple functions can call the same function. In the stationary state, SetPowerOnSection calls other functions, and in the USB communication state which occurs on an interrupt, BranchOfInt calls other functions. Figure 4.3 shows the hierarchical relation of functions; there is no order for function calling. For information on the order in which functions are called, please refer to the flow charts of section 5, Sample Program Operation.





Figure 4.3 Interrelationship between Functions

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## Section 5 Sample Program Operation

In this section, the operation of the sample program is explained, relating it to the operation of the USB function module.

### 5.1 Main Loop

When the microcomputer is in the reset state, the internal state of the CPU and the registers of onchip peripheral modules are initialized. Next, the function SetPowerOnSection in StartUp.c is called, and the CPU is initialized. Figure 5.1 is a flow chart for the SetPowerOnSection function operation.



Figure 5.1 Main Loop



## 5.2 Types of Interrupts

As explained in section 4, the interrupts used in this sample program are indicated by the interrupt flag registers 0 to 3 (UIFR0 to UIFR3); there are a total of nine types of interrupts. When an interrupt factor occurs, the corresponding bits in the interrupt flag registers are set to 1, and an EXIRQ0 interrupt request is sent to the CPU. In the sample program, the interrupt flag registers are read as a result of this interrupt request, and the corresponding USB communication is performed. Figure 5.2 shows the interrupt flag registers and their relation to USB communication.





Figure 5.2 Types of Interrupt Flags

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#### 5.2.1 Method of Branching to Different Transfer Processes

In this sample program, the transfer method is determined by the type of interrupt from the USB module. Branching to each transfer method is executed by BranchOfInt in UsbMain.c. Table 5.1 shows the relations between the types of interrupts and the functions called by BranchOfInt.

| Register Name | Bit | Bit Name   | Name of Function Called |
|---------------|-----|------------|-------------------------|
|               | 7   | BRST       | ActBusReset             |
|               | 6   | _          |                         |
|               | 5   | EP1i TR    |                         |
| UIFR0         | 4   | EP1i TS    | ActInterruptIn          |
| 01110         | 3   | EP0o TS    | ActControlInOut         |
|               | 2   | EP0i TR    | ActControlInOut         |
|               | 1   | EP0i TS    | ActControlInOut         |
|               | 0   | SETUP TS   | ActControl              |
|               | 7   | CK48 READY | SetUSBModule            |
|               | 6   | SOF        | ActIdleCount            |
|               | 5   | SETC       | —                       |
| UIFR3         | 4   | SETI       | —                       |
| UFN3          | 3   | SPRSs      | —                       |
|               | 2   | SPRSi      | _                       |
|               | 1   | VBUSs      |                         |
|               | 0   | VBUSi      | ActBusVcc               |

The EP0iTS and EP0oTS interrupts are used both for control-in and control-out transfer. Hence in order to manage the direction and stage of control transfer, the sample program has three states: TRANS\_IN, TRANS\_OUT, and WAIT. For details, refer to section 5.6, Control Transfers.

In the H8S/2215 hardware manual, operation of the USB function module when an interrupt occurs, and a summary of operation on the application side are described. From the next section, details of application-side firmware are explained for each USB transfer method.

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## 5.3 USB Operating Clock Stabilization Interrupt

This interrupt occurs when the USB operating clock (48 MHz) stabilization time is automatically counted after USB module stop is canceled. After receiving the interrupt, the sample program writes the endpoint configuration information to the USB endpoint information registers (UEPIR00\_0 to 22\_4), makes necessary interrupt settings, and waits for USB cable connection.



Figure 5.3 USB Operating Clock Stabilization Interrupt

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#### 5.3.1 Endpoint Configuration Information

In the USB function module in the H8S/2215, the endpoint configuration can be specified at initialization by software. The following transfer types can be specified:

- Control transfer: One pipe
- Bulk-in transfer: Two pipes
- Bulk-out transfer: Two pipes
- Interrupt-in transfer: Two pipes
- Isochronous-in transfer: One pipe
- Isochronous-out transfer: One pipe

The endpoint number, interface number, alternate number, and maximum packet size can be specified for the above transfers (excluding control transfer) with the USB endpoint information registers (UEPIRs).

Table 5.2 shows transfer types and their corresponding UEPIRs.

| Transfer Type            | Endpoints | Corresponding UEPIRs               |  |  |
|--------------------------|-----------|------------------------------------|--|--|
| Control transfer         | 1         | 00                                 |  |  |
| Interrupt-in transfer    | 2         | 01 and 02                          |  |  |
| Bulk-in transfer         | 2         | 02 and 20                          |  |  |
| Bulk-out transfer        | 2         | 03 and 21                          |  |  |
| Isochronous-in transfer  | 1         | 04, 06, 08, 10, 12, 14, 16, and 18 |  |  |
| Isochronous-out transfer | 1         | 05, 07, 09, 11, 13, 15, 17, and 19 |  |  |

#### Table 5.2Transfer Types and UEPIRs

The H8S/2215 Hardware Manual assumes that endpoint information is configured based on the Bluetooth standard. Figure 5.4 shows the comparison between the endpoint configuration used by this sample program and the endpoint numbers described in the H8S/2215 Hardware Manual.



Figure 5.4 Endpoint Configuration in the Sample Program

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Table 5.3 shows the UEPIR00\_0 to 22\_4 settings for the endpoint configuration shown in figure 5.4. Dummy data (0) must be written to the unused endpoints.

| UEPIR | Set Value<br>(Hexadecimal) | Transfer Type   | EP No. | Interface<br>No. | Alternate<br>No. | Maximum<br>Packet Size<br>(Byte) |
|-------|----------------------------|-----------------|--------|------------------|------------------|----------------------------------|
| 00    | 00_00_40_00_00             | Control         | 0      | 0                | 0                | 64                               |
| 01    | 34_1C_08_00_01             | Interrupt In    | 3      | 0                | 0                | 8                                |
| 02    | 24_14_40_00_02             | BulkIn          | 2      | 0                | 0                | 64                               |
| 03    | 14_10_40_00_03             | BulkOut         | 1      | 0                | 0                | 64                               |
| 04    | 04_1C_00_00_04             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 05    | 04_08_00_00_05             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 06    | 04_1C_00_00_06             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 07    | 04_08_00_00_07             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 08    | 04_1C_00_00_08             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 09    | 04_08_00_00_09             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 10    | 04_1C_00_00_0A             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 11    | 04_08_00_00_0B             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 12    | 04_1C_00_00_0C             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 13    | 04_08_00_00_0D             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 14    | 04_1C_00_00_0E             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 15    | 04_08_00_00_0F             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 16    | 04_1C_00_00_10             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 17    | 04_08_00_00_11             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 18    | 04_1C_00_00_12             | Isochronous In  | 0      | 0                | 0                | 0                                |
| 19    | 04_08_00_00_13             | Isochronous Out | 0      | 0                | 0                | 0                                |
| 20    | 04_14_00_00_14             | BulkIn          | 0      | 0                | 0                | 0                                |
| 21    | 04_10_00_00_15             | BulkOut         | 0      | 0                | 0                | 0                                |
| 22    | 04_10_00_00_16             | Interrupt In    | 0      | 0                | 0                | 0                                |

### Table 5.3UEPIR Settings



## 5.4 Interrupt on Cable Connection (VBUS)

This interrupt occurs when the cable of the USB function module is connected to the host controller. On the application side, after completion of initial microcomputer settings, a general-purpose output port is employed to pull-up the USB data bus D+. By means of this pull-up, the host controller recognizes that the device has been connected (figure 5.5).



Figure 5.5 Interrupt on Cable Connection

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## 5.5 Bus Reset Interrupt (BRST)

When the host controller detects that a device has been connected to the USB data bus, it outputs a bus reset signal. When receiving this bus reset signal, the USB function module generates a bus reset interrupt.



Figure 5.6 Bus Reset Interrupt



## 5.6 Control Transfers

In control transfers, bits 0 to 3 of the interrupt flag registers are used. Control transfers can be divided into two types according to the direction of data in the data stage (figure 5.7). In the data stage, data transfers from the host controller to the USB function module are control-out transfers, and transfers in the opposite direction are control-in transfers.



Figure 5.7 Control Transfers

Control transfers consist of three stages: setup, data (no data is possible), and status (figure 5.8). Further, the data stage consists of multiple bus transactions.

In control transfers, stage changes are recognized through the reversal of the data direction. Hence the same interrupt flag is used to call a function to perform control-in or control-out transfers (table 5.1). For this reason, the firmware must use states to manage the type of control transfer currently being performed, whether control-in or control-out (figure 5.8), and must call the appropriate function. States in the data stage (TRANS\_IN and TRANS\_OUT) are determined by commands received in the setup stage.





Figure 5.8 Status in Control Transfers

#### 5.6.1 Setup Stage

In the setup stage, the host and function modules exchange commands. For both control-in and control-out transfer, the firmware goes into the WAIT state. Depending on the type of command issued, discrimination between control-in transfer and control-out transfer is performed, and the state of the firmware in the data stage (TRANS\_IN or TRANS\_OUT) is determined.

• Command for control-in transfers: GetDescriptor (Standard command)

Figure 5.9 shows operation of the sample program in the setup stage. The figure on the left shows operation of the USB function module.



Figure 5.9 Setup Stage

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#### 5.6.2 Data Stage

In the data stage, the host and function module exchange data. The firmware state becomes TRANS\_IN for control-in transfers, and TRANS\_OUT for control-out transfers, according to the result of decoding of the command in the setup stage. Figures 5.10 and 5.11 show the operation of the sample program in the data stage of control transfer.



Figure 5.10 Data Stage (Control-In Transfer)

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Figure 5.11 Data Stage (Control-Out Transfer)



#### 5.6.3 Status Stage

The status stage begins with a token for the opposite direction from the data stage. That is, in control-in transfer, the status stage begins with an out-token from the host controller; in control-out transfer, it begins with an in-token from the host controller. Figures 5.12 and 5.13 show the operation of the sample program in the status stage of control transfer.



Figure 5.12 Status Stage (Control-In Transfer)





Figure 5.13 Status Stage (Control-Out Transfer)



## 5.7 Interrupt Transfers

Interrupt transfers can also be classified into two types according to the direction of data transmission. Data transfers from the USB function module to the host controller are interrupt-in transfers, and transfers in the opposite direction are interrupt-out transfers. The H8S/2215 only supports interrupt-in transfers (figure 5.14).



Figure 5.14 Interrupt Transfers

#### 5.7.1 Interrupt-In Transfers

In interrupt-in transfers, bit 4 (EP1iTS) of interrupt flag register 0 is used. On receiving an intoken from the USB host controller, the USB function module sends the NAK handshake and sets the EP1iTR flag if no valid data is found in the EP1i FIFO. If valid data is found in the FIFO, the USB function module sends data to the USB host controller, and sets the EP1iTS flag when receiving the ACK handshake from the USB host controller.

After the EP1iTS flag is set, the USB function module executes the ActInterruptIn function. When there is HID data to be sent, this function writes the data to USB endpoint data register 1 (UEDR1i) and waits for an in-token to be sent from the USB host controller. At this point, the firmware is in either WAIT or TRANS\_IN state. Figure 5.15 shows operation of the sample program in interrupt-in transfer. The figure on the left shows operation of the USB function module.

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Figure 5.15 Interrupt-In Transfer



## 5.8 Pseudo Mouse Data Generation

As no mouse can be connected to the MS2215CP, the sample program generates pseudo data (HID data) of the USB mouse and demonstrates automatic mouse pointer movements.

To generate HID data, a 16-bit timer interrupt in the H8S/2215 is used to read data of mouse pointer movements from the data table. The generated data is passed to the ActMakeHidData function, and the HID data is sent to the host PC by using interrupt transfer. Figure 5.16 shows HID data generation of the sample program.



Figure 5.16 HID Data Generation



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# Section 6 Analyzer Data

In this section, we look at how measurement is carried out with the USB Advisor, a USB protocol analyzer manufactured by CATC (http://www.catc.com), using the USB function module in the H8S/2215, and at what happens to the data as it actually flows along the bus. The following gives the description for control transfer when a device is connected and interrupt-in transfer of HID data as examples.

Note: The Packet # found in front of each packet is the packet number used when measuring. The Idle found at the end of each packet indicates the idle between packets.

## 6.1 Control Transfer when Device is Connected

Figure 6.1 shows the measurement made, with a device connected to the host controller, while shifting from the power-on state (the power is supplied to Vbus) until the configuration state (device is ready for being used).

Though the packet scheduling may differ depending on the host controller, the command flow to the configuration state is always the same.



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Only SOF packets continue in this period.

| Packet         Sync  | Setup<br>stage  | Frame<br>(1 ms) |
|--|-----------------|-----------------|
| III2         Signed         DATA0         Data         Option         Color         Idle         Color         Data packet           Packet         F         Sync         DATA0         Data         Option         O  |                 |                 |
| 113       S       00000001       0×C3       00       05       01       00  |                 | (1 ms)          |
| I14         S         UUUUUUU         Ux44         [250 ns]         [377.200 µs]         Freest [ARC5         EOP         Idle           Packet         F         Sync         SOF         Freest [ARC5         EOP         Idle         SO   | <b>_</b>        |                 |
| I15         S         00000001         0xA5         188         0x01         216         ns         5.783         μs           Packet         F         Sync         IN         A006         EMPP         0805         EMP         1016           116         S         00000001         0x36         0         0         00x06         16         ns         ←         In-token packet  |                 | ↓               |
| 118 S 00000001 0x96 0 0 0x08 216 ns 349 ns ← In-token packet   |                 |                 |
| Packet F Sync DATA1 Data CRC18 EOP Idle ( Data packet (0 byte)   |                 | Frama           |
| PacketFSyncDATAUataURMINEUFIdle117S00000001 $0 \times D2$ $0 \times 0000$ $233$ ns $283$ ns $283$ ns   | Status<br>stage | Frame<br>(1 ms) |
| Packet         Sync         ACK         EOP         Idle           118         00000001         0x4B         216 ns         983,383 us   | $\downarrow$    | $\downarrow$    |
| Packet         F         Sync         SOF         Frame         EOP         Idle           113         S         00000001         0xA5         183         0x1E         233 ns         397.100 μs  |                 |                 |
| Transits to add<br>Only SOF packets continue in this period.   | dress state     | e, hereaft      |
| Packet         F         Sync         SÜF         Frame # ICRC5         EDP         Idle           179         S         00000001         0xA5         249         0x18         233 ns         8.350 µs  |                 |                 |
| Packet         F         Sync         SETUP         ADDR         ENDP         CR055         EDP         Idle         ←         Setup token packet           180         S         00000001         0×B4         1         0         0×17         233         ns         266         ns   | Setup           | Frame<br>(1 ms) |
| Packet         F         Sync         DATA0         Data         CR016         EOP         Idle           181         S         000000001         0x03         80.06         00.00         12.00         0x02F         233         ns         486         ns         ←         Data packet   | stage           | (1113)          |
| Packet         F         Sync         ACK         EOP         Idle           182         S        000001         0x48         250_ns         [375.317 μs]         ←         ACK handshake packet   |                 | $\downarrow$    |
| Packet         F         Sync         SOF         Frame #         ICR05         EOP         Idle           188         S         00000001         0×45         250         0×14         218         ns         9.288         µs  |                 |                 |
| Packet         F         Sync         IN         ADDR         ENDP         ORG5         EDP         Idle           184         S         00000001         0x36         1         0         0x17         216         ns         [839         ns         ←         In-token packet   |                 | Frame           |
| Packet         F         Sync         DATA1         Data         CRC16         EOP         Idle           185         00000001         0xD2         0: 12         01         10         00         00         40         58         04         10         00         00         40         58         04         10         00         00         40         58         04         10         00         00         40         58         04         10         00         00         01         00         00         10         10         10         00         00         40         58         04         10         00         00         10         10         10         10         10         00         00         40         58         04         10         00         00         10   | Data<br>stage   | (1 ms)          |
| Li6: 00 01         ↑ Data packet (18 bytes)           Packet         Pone         ADK         E0P         AIG         ACK handshake packet         (Device Descriptor information)           186         00000001         0x48         223 ns   367.783 µs         ACK         ADK         (Device Descriptor information)   | $\bot$          |                 |
| Packet         F         Sync         SOF         Frame # (SRG5         EOP         Idle           187         S         00000001         0xA5         251         0x05         8600 µs  |                 |                 |
| Pecket         P         Out         A0003         ENDP         Color         Color <thcolor< th=""> <thcolor< th=""> <thcolor< <="" td=""><td><b></b></td><td>Frame</td></thcolor<></thcolor<></thcolor<> | <b></b>         | Frame           |
| PacketFSyncOATA1 <b>Data</b> BRC16EOPIdle188\$000000010xD2 $0x0000$ $233$ ns  400 ns   | Status<br>stage | (1 ms           |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $   | Ļ               | 1               |
| Packet         F         SUP         Frame         COS         EOP         Idle           191         S         00000001         0xA5         252         0x18         997.033 µs         Frame  | (1 ms)          |                 |
| Packet         F         SUP         Frame         II (DRG)         EOP         Idle           182         \$\$         0000001         0xA5         253         0x04 (233 ns)         10.517 µs   |                 |                 |
| Packet         F         Syme         SETUP         ADDR         ENDP         CRG5         EOP         Idle           193         S         0000001         0x84         1         0         0x171         238         ns         268         ns         ✓         Setup token packet  |                 | Frame           |
| Pecket         F         Oxto         Option         CB018         E0P         Idle           134         8         0000001         0:cc3         80         06         00         22         00         00         0:x7520         233         ns         433         ns         ←         Data packet  | Setup<br>stage  | (1 ms)          |
| Packet       F       Svnc       ACK       E0P       Idle         135       00000001       0x48       [233 ns] [373.117 us] $\leftarrow$ ACK handshake packet   | Ļ               |                 |
| Packet         F         SUP         Frame         10 (C6)         EUP         Idle           196         S         00000001         0xA5         254         0x06         233 ns         8.767 μs   | <b>*</b>        |                 |
| Packet         F         Syne         IN         ADDR         ENDP         CR055         EDP         Idle           187         \$\$         00000001         0x36         1         0         0x17         233 ns         493 ns         ←         In-token packet  | <b></b>         | Frame           |
| Packet F Sync DATA1 <sup>4</sup> Data CRC18 EOP Idle  CData packet (9 bytes)   | Data<br>stage   | (1 ms)          |
| Iss         Sime         ACK         EUF         Ide         Configuration         Déscriptor           Packet         F         Syme         ACK         EUF         Ide         Configuration         Déscriptor           198         S00000001         0x48         1283 ns]         ACK         ACK handshake packet         Ide  | ↓               | $\bot$          |
| Packet         F         Syne         SOF         Frame         ICR05         EOP         Idle           200         S         0000001         0x45         255         0x13         233 ns         8.267 us   |                 |                 |
| Packet         F         Sync         OUT         ADDR         ENDP         CRG5         EDP         Idle           201         \$         00000001         0:x87         1         0         0:x17         288         ns         Courtoken packet  |                 | Frame           |
|  | Status<br>stage | (1 ms)          |
| Packet F Sync DATA1 10ata CRC18 EOP Idle ← Data packet (0 byte)  | Stage           |                 |
| Packet F Sync DATA1 10ata CRC16 EOP Idle   Data packet (0 byte)  | u stage         |                 |

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Control transfer (Set\_Address)

Control transfer (Get\_Descriptor (Device))

Control transfer (Get\_Descriptor (Configuration))

| 205         B         00000001         0x45         257         0x03/216         nsl         7.700 µs           Packet         F         Symc         0510/1         AD02         ED0         Id1e         C         Setup token packet           206         S         0000010         0x44         1         0 x17/233 ms         256 ms         C         Setup token packet           Packet         F         Symc         DATA0         Data         SR016         EDP         Id1e         C  | st 🔺   |
|--|--|
| Packet F Sync DATAO Data CRC16 EOP Idle 		 Data  |  |
| 207 S 00000001 0xC3 80 06 00 02 00 00 FF 00 0x3/25 233 ns 400 ns   | ata packet Setup Frai<br>stage (1 n  |
| Packet E Sync ACK EOP Idle   ACK ACK   | L I  |
| 208         S         00000001         0×48         233         ns         875.387         us           Packet         F         Sync         SOF         Frame #         BR05         EOP         Idle  | ¥  |
| 209 S 00000001 0x45 258 0x01 233 ns 8.767 µs<br>Packet S Synce 10 4008 ENDP 08006 00 5 10 10 10 10 10 10 10 10 10 10 10 10 10  | <b></b>  |
| Packet F Sync DATA1 4 Data   | CRC16 EOP Idle Data (1 ms  |
| 211 0 0000000 0x02 0: 09 02 22 00 01 01 00 00 32 09 04 00 00 01 03<br>16: 02 00 09 21 00 01 00 01 22 34 00 07 05 89 03<br>32: 00 08  | 01 0x1695 250 ns 249 ns stage<br>04 stage  |
| Contract of the second sec | scriptor information)  |
| Packet         F         Sync         SOF         France         SOROE         Local         Idle           218         S00000010         0x45         258         0x1E         238 nc)         987.117 µs   | Frame (1 ms)   |
| 218         Control of the second                             | ¥  |
| Packet F Sync OUT ADDR ENDP CRC5 EDP Idle Out talian marging   | <b>▲</b>   |
| Packet F Sync DATA1 Data CRC16 EOP Idle  | Status Frar<br>stage (1 m  |
| 216         00000001         0xD2         0x0000 [216 ns]         466 ns]           Packet         F         Syme         ACK         EOP         Idle         ←         ACK         handshake packet  |  |
| 217         ©         00000001         0.x4B         250 nsl         977.500 μs           Packet         F         Sync         SOF         Frame         I GR05         EOP         Idle  | ¥  |
| 218 S 00000001 0×A5 281 0×1F 233 ns 997.100 μs   |  |
| E Only SOF packets of  | continue in this period.   |
| Packet         F         Sync         SOF         Frame 1         CRC5         EOP         Idle           441         S         00000001         0xA5         484         0x06 (233 ns)         8.017 μs   | <b>↑</b>   |
| Packet         F         Sync         SETUP         A00R         ENDP         GR65         EOP         Idle         Composition         Setup token packet           442         \$         00000001         0x84         1         0         0x17         233         ns         ←         Setup token packet   | - Fran   |
| Packet         F         Sync         DATA0         1         Data         DR016         E0P         1dle         ←         Data           443         S         00000001         0xxx3         80         06         00         01         00         01/2         02         0xx0/2+         233         ns         483         ns   | ata packet Setup (1 m  |
| Packet 7 Sync ACK E0P Idle ← ACK handshake packet  | ↓  |
| Packet E         Sync         SOF         Frame \$         DRDS         EOP         Idle           445         S         00000001         0x45         485         0x13         233 ns         9.267 μs  | ¥  |
| Packet         F         Sync         IN         FADDR         ENDP         ENDP         ENDP         Idle           446         6         00000001         0x96         1         0         0x17         233         ns         Instants         Instants   | <b>_</b>   |
| Pecket F Sync DATA1 * Data<br>447 S 00000001 0x02 0: 12 01 10 01 00 00 00 40 58 04 10 00 00 01 00  | CRC16         EOP         Idle         Data         (1 ms)           00         0x7AC9         233 ns         800 ns         stage |
| Packet E Sync ACK EOP Idio ACK I ACK I ACK   | Data packet (18 bytes)<br>evice Descriptor information)  |
| 448         S         00000001         0x4B         233 ns  967.867 μs           Packet         F         Sync         SOF         Frame 1         OR05         EOP         Idle   |  |
| 449         IS         00000001         0 x45         486         0 x1B         233 ns         6.850 μs           Packet         F         Syna         OUT         ADDR         ENDP         GR05         EOP         Idle  |  |
| 450 S 00000001 0x87 1 0 0x17 233 ns 266 ns ← Out-token packet  | T Fran<br>Status (1 m  |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $   | stage  |
| 452         S         00000001         0.x48         250 ns         382.183 us         0           Packet         F         Symc         SOF         Frame # DR05         EOP         Idle   | <u> </u>   |
| 453 S 00000001 0xA5 487 0x04 233 ns 997.117 μs   | Frame (1 ms)   |
| Packet         F         Sync         SDF         Freese         CR051         EOP         Idle           454         \$\star{0}\$         0000001         0.v45         438         0.v14/233 ns         7.350 us           Packet         F         Sync         SETUP         ADOR         EOP         Idle         C         Setup         Idle  | <b>_</b>   |
| 455 S 00000001 0xB4 1 0 0x17 233 ns 266 ns   | Fran   |
| 456 S 00000001 0xC3 80 06 00 02 00 00 00 0x7520 233 ns 448 ns  | ata packet Setup (1 m<br>stage   |
| Packet         II         Sunc         ACK         50?         Id1e         ACK         handshake packet           457         S         00000001         0x48         233 ns   376.350 µs         →<  | <u> </u>   |
| Packet         F         Supple         Frame         DR065         ECP         Idle           456         00000001         0xA5         489         0x08         233 ns         6.100 µs  | <b>1</b>   |
| Packet         F         Sync         IN         A008         ENDP         ENDP         ENDP         Idle           458         S         0000001         0x96         1         0         0x17         238 ns         386 ns         ←         In-token packet  | Data packet (9 bytes) Data Fram  |
|  | Data packet (9 bytes) Data Fran<br>Configuration Descriptor stage (1 m<br>nformation)  |
| Packet         F         Sync         ACK         E0P         Idle         ←         ACK handshake packet           481         S         00000001         0x48         238_ns         976.933 µs         µs   | <u> </u>   |
| Packet         F         Sync         SOF         Frame 1         GR65         EOP         Idle           482         S         00000001         0×A5         490         0×09         233 ns         6.517 μs   | <b>↑</b>   |
| Pecket         F         Sync         OUT         ADDR         ENDP         GR055         EOP         Idle           488         00000001         0x87         1         0         0x17         238         ns         266         ns         ←         Out-token packet   | Status Fran  |
| Pecket         F         Sync         DATA1         Posta         DRG15         EOP         Idle         ←         Data packet (0 byte)           484         \$3         00000001         0xD2         0x0000 [233 ns]         483 ns   | Status (1 m<br>stage   |
| Pecket         F         Sync         ACK         E0F         Idle         ←         ACK         handshake         packet           485         00000001         0x48         233 ns         332.483 µs         485  | ↓  |
| Packet F Sync SOF Frame \$ CRC5 EOP Idle   | <b>-</b>   |

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|         |          | Packet   | F  | Sync   | SOF  | Frame # CRC5 EOP Idle  |                 |                 |
|---------|----------|--|--|--|--|--|-----------------|-----------------|
|         |          | 467  | S  | 00000001   | 0×A5   | 492 0x08 216 ns 8.533 μs   |                 | Т               |
|         | $\int$   | Packet<br>468  | FS   | Sync<br>00000001   | SETUP<br>0×B4  | ADDR         ENDP         GR03         EOP         Idle           1         0         0x17         218         ns  | <b></b>         |                 |
|         |          | Packet<br>469  | F  | Sync<br>UUUUUUU1   | DATA0<br>U×C3  | •         Data         CRC16         EOP         Idle         Data packet           80         06         00         02         00         02         00         UXUU21         216         ns         433         ns  | Setup<br>stage  | Frame<br>(1 ms) |
|         |          | Packet   | F  | Sync   | ACK  | EOP Idle ← ACK handshake packet  | T               |                 |
|         |          | 470<br>Packet  | E E  | 00000001<br>Sync   | 0×4B<br>SOF  | 233 ns 975-200 μs<br>Frame # CRC5 EOP Idle   |                 | <b>_</b>        |
|         |          | 471  | S  | 00000001   | 0×A5   | 493 0×17 233 ns 8.933 μs   |                 | T               |
|         |          | Packet<br>472  | F  | Sync<br>00000001   | IN<br>0×96   | ADDR         ENDP         CRC63         EOP         Idle           1         0         0x17         233 ns         386 ns $\leftarrow$ In-token packet   |                 | Fromo           |
|         | )        | Packet<br>473  | F  | Sync<br>00000001   | DATA1<br>0×D2  | Data         CRC18         EOP         Idle           0: 09 02 22 00 01 01 00 C0 32 09 04 00 00 01 03 01         0x1635         250 ms         316 ms  | l<br>Data       | Frame<br>(1 ms) |
| 5       | \        |  |  |  |  | 16: 02 00 09 21 00 01 00 01 22 34 00 07 05 83 03 04<br>32: 00 08   | stage           | , I             |
|         |          | Packet<br>474  | F  | Sync<br>00000001   | ACK<br>0×4B  | EOP Idle<br>233 ns 957.450 us<br>Configuration<br>Describtor information)  | $\perp$         |                 |
|         |          | 4/4<br>Packet  | ه<br>F   | Svnc   | 0×4B<br>SOF  | 200 ns 307.400 μs  |                 | <b>—</b>        |
|         |          | 475  | S  | 00000001   | 0×A5   | 494 0×15 233 ns 6.017 μs   |                 | Т               |
|         |          | Packet<br>476  | FS   | Sync<br>00000001   | 0UT<br>0×87  | ADDRENDPCRC5EDPIdle10 $0 \times 17$ 233ns266ns $\leftarrow$ Out-token packet   | <b>†</b>        | Frame           |
|         |          | Packet<br>477  | F  | Sync<br>00000001   | DATA1<br>0×D2  | Conta CRCIB EOP Idle ← Data packet (0 byte)     0x0000 233 ns 450 ns   | Status<br>stage | (1 ms)          |
|         |          | Packet   | F  | Sync   | ACK  | <b>EOP</b> Idle $\leftarrow$ ACK handshake packet  | Ĭ               |                 |
|         | $\sim$   | 478  |  |  |  |  |                 |                 |
|         |          | Packet   | F  | 00000001   | 0×4B   | 233 ns 383.017 μs  | <u> </u>        |                 |
|         |          | Packet<br>479  | F  | Sync<br>00000001   | 0×48<br><b>SOF</b><br>0×A5   |  | Frame (1 ms     |                 |
|         |          |  | F<br>S<br>F  | Sync   | SOF  | Frame # CRC5 EUP Idle  | Frame (1 ms     | )               |
|         | <i>C</i> | 479<br>Packet<br>480<br>Packet   | F<br>S<br>F<br>S   | Sync<br>00000001<br>Sync<br>00000001<br>Sync   | <b>SOF</b><br>0×A5<br><b>SOF</b><br>0×A5<br>SETUP  | Frame         I OR05         EUP         I die           495         0x0A         216         ns         997.133         us           Frame         0x05         EOP         I die         6.000         us           496         0x10         233         ns         6.000         us           A008         ENDP         0x05         EOP         I die  | Frame (1 ms     | <b>X</b>        |
|         | ſ        | 479<br>Packet<br>480   | F<br>S<br>F<br>S<br>F  | Sync<br>00000001<br>Sync<br>00000001<br>Sync<br>00000001<br>Sync   | <b>SOF</b><br>0×A5<br><b>SOF</b><br>0×A5   | Frame         I (BR05)         EUP         I die           495         0x0A (216 ns)         997.133 us         Image: Second sec | Setup           | Frame<br>(1 ms) |
|         | ſ        | 478<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482   | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S   | Sync           00000001           Sync           00000001           Sync           00000001           Sync           00000001           Sync           00000001  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3  | Frame         I (BR05)         EUP         I die           495         0x0A (216 ns)         997.133 us         Image: Second sec |                 | Frame           |
|         | $\int$   | 479<br>Packet<br>480<br>Packet<br>481<br>Packet  | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S   | Sync<br>00000001<br>Sync<br>00000001<br>Sync<br>00000001<br>Sync   | SOF           0×A5           SOF           0×A5           SETUP           0×B4   | Frame         I (BR05)         EUP         I die           495         0x0A (216 ns)         997.133 us         Image: Second sec | Setup           | Frame           |
| <       | ſ        | 479<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482<br>Packet   | F<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S  | Sync           00000001           Sync           00000001           Sync           00000001           Sync           00000001           Sync           00000001           Sync           00000001  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK  | Frame         I CROS         EUP         I die           495         10x04         218         ns         997.133         us           Frame         I CROS         EOP         I die         I die           436         0x10         233         ns         6.600         us           4000         EUPO         I die         ←         Setup token packet           1         0         0x17         233 ns         266 ns           •         Data         ORCIS         EOP         I die           0         0.00         00         00         00         00         00           00         0.00         00         00         00         00         00         00         00         00         486 ns            EOP         I die         ←         ACK handshake packet   | Setup           | Frame           |
| $\prec$ | $\int$   | 478<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482<br>Packet<br>483<br>Packet<br>484<br>Packet                                   | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S   | Sync           00000001  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK           U×4B           SOF           0×A5  | Frame         CRC5         EUP         Idie           495         0x0A         216         ns         997.133         us           Frame         1005         EOP         Idie            496         0x1D         233         ns         6.600         us           4006         EUP         Idie              1         0         0x17         233         ns         266            1         0         0x17         233         ns              10         0x17         233         ns               10         0x17         233         ns               100         03         01         00.00         00         00         00         00             233         ns         148         ns         486         ns            233         ns         18         ////////////////////////////////////   | Setup           | Frame (1 ms)    |
| $\prec$ | $\int$   | 478<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482<br>Packet<br>483<br>Packet<br>484<br>Packet<br>485                            | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F      | Sync           00000001  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK           U×4B           SOF           0×A5           IN           0×96  | Frame         CR05         EUP         Idie           495         0x0A (216 ns)         997.133 µs)  | Setup<br>stage  | Frame           |
| $\prec$ |          | 473<br>Packet<br>480<br>Packet<br>482<br>Packet<br>482<br>Packet<br>483<br>Packet<br>483<br>Packet<br>485<br>Packet<br>485           | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S                     | Sync           00000001  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK           U×44           SOF           0×A5           IN           0×36           DATA1           0×D2                               | Frame         0 (2005)         EUP         Idie           435         0 x0A (216 ns)         937.133 us         Image: state | Setup<br>stage  | Frame<br>(1 ms) |
| $\prec$ |          | 473<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482<br>Packet<br>482<br>Packet<br>484<br>Packet<br>485<br>Packet                  | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S | Sync           00000001           Sync           Sync           00000001           Sync  | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK           U×4B           SOF           0×A5           IN           0×96           DATA1  | Frame         10 (805)         EUP         Idie           435         0 x04 216 ns         937.133 us         us           Frame         1 0000         EUP         Idie         6.600 us           A008         EN0P         Idie         ← Setup token packet           1 0         0 x17 (233 ns) (266 ns)         ← Setup token packet         ← Data packet           00 09 01 00 00 00 00 00 00 00 00 xE444 (233 ns) 466 ns         ← ACK handshake packet         ← Data packet           233 ns) 87/.108/ us         ← ACK handshake packet         ← ACK handshake packet         ← ACK handshake packet           1 0         0 x01 72 233 ns)         × 7.517 us         ← ACK handshake packet         ← ACK handshake packet  | Setup<br>stage  | Frame<br>(1 ms) |
| ~       |          | 473<br>Packet<br>480<br>Packet<br>481<br>Packet<br>482<br>Packet<br>483<br>Packet<br>484<br>Packet<br>484<br>Packet<br>488<br>Packet | F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S<br>F<br>S | Sync           00000001           Sync           Sync           Sync           Sync           Sync | SOF           0×A5           SOF           0×A5           SETUP           0×B4           DATA0           0×C3           ACK           U×4B           SOF           0×A6           NO×65           IN           0×96           DATA1           0×D2           ACK | Frame         CROS         EUP         Idie           495         0x0A         216         ns         997.133         us           Frame         CROS         EOP         Idie         Idie           495         0x1D         233         ns         8.600         us           4006         E002         E055         EOP         Idie         ←           1         0         0x17         233         ns         266 ns         ←           0         0y17         233         ns         E00         Idie         ←         Data           0         0y17         233         ns         466 ns         ←         CACK handshake packet           233         ns         13         0x00         200         000         0x00         0x00 <td>Setup<br/>stage</td> <td>Frame<br/>(1 ms)</td>   | Setup<br>stage  | Frame<br>(1 ms) |

Only SOF packets continue in this period.



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Control transfer (Get\_Descriptor (Configuration))

Control transfer (Set\_Configuration)



|   | ( | 517 S 000<br>Packet F S | ync         SOF           00001         0×A5           ync         SETUP | Frame I         DR051         EOP         Idle           513         0x1C         233         ns         11.017         µs           ADDR         ENDP         ORG5         EOP         Idle         ←         Setup token packet |   |                 |        |
|---|---|-------------------------|--|---|---|-----------------|--------|
| 2   |   | Packet F S              | 00001 0×B4<br>ync DATAU<br>00001 0×C3                                    | 1         0         0×17         233         ns         286         ns         1000000000000000000000000000000000000  | Setup<br>stage  | Frame<br>(1 ms) |        |
|   |   |                         | <mark>ync ACK</mark><br>00001 0×4B                                       | EOP Idle ← ACK handshake packet   | <u> </u>  | $\downarrow$    |        |
| eport   |   |                         | vne <u>SOF</u><br>00001 0×A5   | Frame         Itele           520         0x00C         233         ns         8.683         us   |   |                 |        |
| or (Re  |   | 522 S 000               | <mark>ync IN</mark><br>00001 0×96  | ADDR     ENDP     CROS     EUP       1     0 $0 \times 17$ 233 ns     400 ns $\leftarrow$ In-token packet   |   | Frame           |        |
| Control transfer (Get_Descriptor (Report))<br>A | Ś | ļ                       |  | ync DATA1<br>00001 0×D2   | U:         U5         U1         U9         U2         A1         U1         U5         U1         U1         U3         U3 <thu3< th="">         U3         U3         U3<!--</td--><td>Data<br/>stage</td><td>(1 ms)</td></thu3<> | Data<br>stage   | (1 ms) |
| (Get  |   |                         | ync ACK<br>UUUUI U×4B  | EOP         Idle         ← ACK handshake packet         (Report<br>Descriptor information)  | <b>↓</b>  | $\downarrow$    |        |
| Isfer   |   |                         | <mark>ync SOF</mark><br>00001 0×A5                                       | Frame # DR05         EOP         Idle           521         0x18         233         ns         987.117         μs  | Frame (1 ms)  | Ţ               |        |
| ol trai   |   |                         | ync <u>SOF</u><br>00001 0×A5   | Frame         # CRC5         EOP         Idle           522         0x11         233 ns         10.287 us   |   | Ť               |        |
| Contre  |   | 527 S 000               | ync OUT<br>00001 0×87  | ADDR         ENDP         CRC5         EOP         Idle           1         0         0×17         233 ns         286 ns         ←         Out-token packet   | Status  | <br>Frame       |        |
| 0   |   | 528 S 000               | ync DATA1<br>00001 0×D2  | Source         Source         Idle         ←         Data packet (0 byte)           0x0000         233         ns         416         ns  | stage   | (1 ms)<br>I     |        |
|   | Ĺ |                         | y <mark>nc ACK</mark><br>00001 0×4B                                      | LEOP Idle ← ACK handshake packet  | <b>—</b>  | $\downarrow$    |        |
|   |   |                         | ync <u>SOF</u><br>00001 0×A5   | Frame # CRC5         EOP         Idle           523         0x0E         233 ns         997.117 us  |   |                 |        |

Figure 6.1 Control Transfer when Device is Connected



## 6.2 Interrupt-In Transfer of HID Data

Figure 6.2 shows the measurement results when HID data is sent from the device to the USB host controller through interrupt-in transfer. In response to the interrupt-in transfer from the USB host controller, the device returns a NAK if no data can be sent. If there is data to be sent, the device sends 4-byte HID data. On receiving HID data, the USB host controller issues an ACK.



Figure 6.2 Interrupt-In Transfer of HID Data

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Renesas Technology Europe Limited. Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, United Kingdom Tel: <44> (1628) 585 100, Fax: <44> (1628) 585 900

Renesas Technology Europe GmbH Dornacher Str. 3, D-85622 Feldkirchen, Germany Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852- 2265-6688, Fax: <852> 2375-6836

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Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd. 1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65-6213-0200, Fax: <65-6278-8001

# H8S/2215 USB Function Module Human Interface Devices (HID) Class Application Note



Renesas Electronics Corporation 1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan

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