

E1 On-Chip Debugging Emulator Introductory Guide for the RX610 Group

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Outline

This introductory guide is intended for first-time users of the E1 emulator. It provides simple instructions to help the user along the path from unpacking the E1 emulator to running and stopping a program without getting lost along the way.

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1. Introduction

This guide assumes that the user who purchased the E1 emulator as a unit is using a CPU board on which an RX610-group MCU has been installed as the user system. However, the same operating procedures for the E1 emulator can also be used with systems which incorporate other RX610-group MCUs.

By following the procedures in this guide, you can get some practical experience of installing the program and simple usage of the E1 emulator.

This guide assumes that the machines and tools in use are as follows.

- (1) Host computer
- (2) E1 emulator
- (3) CPU board on which an RX610-group MCU has been installed (R0K556100C000BE): This CPU board is included in the Renesas Starter Kit for the RX610 (R0K556100S000BE).



2. Product Overview

Figure 2.1 shows the configuration of the E1 emulator system.



Figure 2.1 System Configuration with the Emulator

2.1 Items Included in the E1 Emulator Package

2.1.1 E1 Emulator

The E1 is an on-chip debugging emulator from Renesas that provides a highly-efficient debugging environment in combination with the High-performance Embedded Workshop and E1/E20 emulator debugger.

After debugging has been completed, the E1 is also usable as a flash-memory programmer in combination with the Flash Development Toolkit (FDT).

2.1.2 USB Interface Cable

This cable is used to connect the emulator and host computer.

2.1.3 User-System Interface Cable

This cable is used to connect the emulator and user system.

2.1.4 E1/E20 Emulator Software

The E1/E20 emulator software consists of the following items.

(1) High-performance Embedded Workshop

The High-performance Embedded Workshop is a common GUI in which various tools such as a C compiler, assembler, emulator software, and editor are integrated for the more efficient development of software. Debugging with the E1 emulator is always controlled via this GUI.

(2) E1/E20 emulator debugger

The E1/E20 emulator debugger runs on the host computer and communicates with the MCU via the E1 emulator, providing an advanced debugging environment. Users are not directly aware of the presence of the emulator debugger because it operates as part of the High-performance Embedded Workshop. The features of the E1 emulator debugger are given below.

- Basic debugging functions including running and stopping a program, breaks in execution, and watching
- Acquiring trace information on 256 branches or cycles
- Measuring performance between two points (i.e. performance measurement facility)
- Automatic updating of the display of data in the [Memory] window by using a cycle-stealing transfer mode

These enhanced debugging functions facilitate the analysis of problems during debugging and evaluation, greatly reducing times required for debugging.



(3) Auto-update utility

The auto-update utility automatically acquires update information on Renesas' microcomputer development tools via the Internet to ensure that required updates are applied to the tools.

For details, see the Renesas AutoUpdate Manual, which is opened from the corresponding menu item shown in Figure 2.2.



Figure 2.2 Opening the Renesas AutoUpdate Manual

2.2 Items That Must be Prepared by the User

The following items must be prepared by the user.

- Host computer
- User system (CPU board)
- RX family C/C++ compiler package*

*Note: There are two versions of the RX family C/C++ compiler package: official product and free evaluation software. For details on the evaluation version of the C compiler, refer to section 5.2, Evaluation Version of the RX Family C/C++.

2.2.1 Host Computer

The versions of OS and host computer we recommend for running the software products included in the E1 emulator package are given in Table 2.1.

	Operating Environments		
Item	Windows [®] 2000 or	32-Bit Editions of Windows Vista®	
	32-Bit Editions of Windows [®] XP		
Host computer	IBM PC/AT compatible machine with	USB 2.0 (Full-Speed/High-Speed*).	
CPU	At least a 1-GHz Pentium [®] III is	At least a 3-GHz Pentium [®] 4 or	
	recommended.	1-GHz Core™ 2 Duo is	
		recommended.	
Minimum memory	1 Gbyte or more is recommended	1.5 Gbyte or more is recommended	
capacity	(at least 10 times the size of the	(at least 10 times the size of the	
	load module file).	load module file).	
Hard-disk capacity	Installation disk capacity: 600 Mbytes or more.		
	Prepare a swap area taking up at least double the memory capacity (four		
times or more is recommended).			
Pointing device such as			
mouse	Windows [®] XP, or Windows Vista [®] .		
Display	Monitor resolution: 1024 × 768 or higher		
CD-ROM drive	Required to install the emulator software or refer to the emulator user's		
	manual.		

Table 2.1Operating Environments

*Note: Although the emulators are also connectable to a host computer with USB 1.1, we recommend USB 2.0 (High-Speed) as more suitable in terms of emulator performance.



2.2.2 User System

The E1 emulator must always be used with a user system on which an MCU has been installed. The user system must have a connector for the E1.

In this guide, the CPU board (R0K556100C000BE) included in the Renesas Starter Kit for the RX610 (R0K556100S000BE) is used instead of a user system.

The same procedures as are given in this guide apply to any CPU board on which a RX610-group MCU has been installed.

2.2.3 RX Family C/C++ Compiler Package

The compiler is not included in the E1 emulator package. To compile source files, prepare the RX family C/C++ compiler package.

The RX family C/C++ compiler package is capable of generating debugging information files from C- and assembly-language programs.



3. Installation

The following products must be installed if the E1 emulator is to be used.

- E1/E20 emulator software (install this from the CD included in the E1 emulator package) This product includes the E1 emulator debugger, integrated development environment (High-performance Embedded Workshop), and auto-update utility.
- RX family C/C++ compiler package This product includes the C/C++ compiler suite, simulator/debugger, integrated development environment (High-performance Embedded Workshop), and auto-update utility.

3.1 Before Installation

- (1) Do not connect the E1 emulator to the host computer before the emulator software is installed. Installing the emulator software involves installation of the E1-specific USB driver on the host computer. This automatically provides guidance when new hardware is detected. If the E1 emulator is connected to the host computer before the emulator software is installed, the host computer will not be able to recognize the E1 emulator because the E1-specific USB driver is not present. In such cases, use the Windows' Device Manager to delete the unidentified USB device set up in response to the connection. After that, install the emulator software and reconnect the E1 emulator.
- (2) If you have both the official product and evaluation version of the software, use the product version. Also use the product version if you have installed the product version of the RX-series C compiler package or purchased the compiler package along with the E1 emulator.

3.2 Installing the Emulator Software

- (1) Insert the CD-ROM included in the E1 emulator package into the host computer. An HTML file giving instructions will automatically be opened (if this does not happen, open README_E. HTM on the CD-ROM). Click on the [Install] link shown in Figure 3.1.
- (2) The contents of a folder are displayed as shown in Figure 3.1. Execute HewInstMan.exe.



Figure 3.1 Introductory Screen for Installation



(3) Executing HewInstMan.exe opens the [High-performance Embedded Workshop Install Manager] window shown in Figure 3.2.

On the first installation, [Install Manager Help] is also displayed, so confirm its content. After that, click on [Standard Install (Recommended)].

Renesas Everywhere you imagine. Multi installation	Read first Standard Install (Recommended) Multi Install
Active High-performance Workshop Switch over!	Maintenance Detail The Install Manager is the utility that guides you through steps as you install the integrated development environment High-performance Embedded Workshop. It permits you to install two or more instances of the High-performance Embedded Workshop in a single PC.
Non-active	Exit

Figure 3.2 High-performance Embedded Workshop Installation Manager

- Note: Although this guide explains the standard installation, you can alternatively select [Multi Install] to install multiple High-performance Embedded Workshops on a single host computer.
- (4) The [Select destination folder] dialog box appears (Figure 3.3). To change the destination folder, click on the [Change] button. Check the folder name and click on the [Next] button. The default destination folder is used in this guide.

Select destination folder		
Destination folder		
Click Next to install to this folder, or click Change to install to a differnt folder.		
Destination Folder		
C:\Program Files\Renesas\Hew Change		
Exit		

Figure 3.3 [Select destination folder]



(5) The [Choose software] dialog box appears (Figure 3.4). Select the software products that you wish to install and click on the [Install] button.

The auto-update utility, which automatically acquires update information on Renesas' microcomputer development tools via the Internet to ensure that required updates are applied to the tools, is selected in this guide.

♦ Choose software
Please choose software to install. Plase choose software to install. High-performance Embedded Workshop Concerned to the software of the sof
Detail Please choose from the list (it will installed one-by-one by this order)
Please click [Install] after choosing software to install.
Install

Figure 3.4 [Choose software]



(6) Installation of the High-performance Embedded Workshop starts automatically. Follow the procedures shown in Figure 3.5.



Figure 3.5 Installing the High-performance Embedded Workshop

(7) Installation of the E1/E20 emulator software starts. Follow the procedure shown in Figure 3.6.



Figure 3.6 Installing the E1/E20 Emulator Software



(8) Installation of the auto-update utility starts. Follow the procedure shown in Figure 3.7.

Renesas AutoUpdate Utility V.1.05.00 - InstallShield Wizard Welcome Repair or remove the program.	Renesas AutoUpdate Utility V.1.05.00 - InstallShield Wizard Installation options Select the options you want.
Welcome to the Renessas AutoUpdate Utility V.1.05.00 Setup Maintenance program. This program lets you modify the current installation. Click. one of the options below. Image: Constant of the options of the options below. Image: Constant of the option of the options below.	Select the options. Click the Next to continue.
Maintenance Complete InstallShield Wizard has finished performing maintenance operations on Renesas AutoUpdate Utility V.1.05.00.	
Make sure that the installation is complete and click on [Finish].	
< Back Finish Cancel	

Figure 3.7 Installing the Auto-Update Utility

(9) The dialog box shown in Figure 3.8 appears when the installation of all software products has been completed. Click on the [Exit] button to finish this process.

High-performance Embedded W	forkshop Install Manager	
RENESAS Everywhere you imagine. Multi installation		
Active High-performance Embedded Switch over!		
Part and a second secon	Exit	

Figure 3.8 Installation Completed



3.3 Installing the RX Family C/C++ Compiler Package

(1) To install the evaluation version of the compiler, download the executable file (e.g. ccrxv100r00_ev.exe) and double-click on the filename or icon. In the dialog box shown in Figure 3.9, click on the [Next] button. To install an official product-version compiler, insert the CD into the computer drive. The installation process starts with the screen shown in Figure 3.10.

For details on downloading of the evaluation version of the compiler, refer to section 5.2.2, Downloading the Evaluation-Version C Compiler.



Figure 3.9 Starting to Install the Evaluation-Version C Compiler

(2) The dialog box shown in Figure 3.10 appears. Select [Standard Install (Recommended)].

High-performance Embedded W RENESAS Everywhere you imagine. Multi installation	Vorkshop Install Manager Read first Standard Install (Recommended) Multi Install Maintenance	X	
Active High-performance Embedded Workshop Switch over!	Detail The Install Manager is the utility that guides you through steps as you install the integrated development environment High-performance Embedded Workshop. It permits you to install two or more instances of the High-performance Embedded Workshop in a single PC.		
Non-acure	Exit		

Figure 3.10 High-performance Embedded Workshop Installation Manager



(3) Select the software products that you wish to install and click on the [Install] button.

At least [C/C++ Compiler Package for RX family] must be selected.

Installation of the auto-update utility is optional. Although there is no need to select the auto-update utility if it has already been installed on the host computer, selecting it causes no problems since the newer version of the auto-update utility will then be on the host computer.

Choose software Please choose software to install. C/C++ Compiler Package for RX family V.1.00 Release 00 AutoUpdate	
Detail Please choose from the list (it will installed one-by-one by this order)	
Please click [Install] after choosing software to install.	

Figure 3.11 Selecting Software Products to be Installed



C/C++ compiler package for RX family V.1.00 Release 00	C/C++ compiler package for RX family V.1.00 Release 00
	License Agreement Please read the following license agreement carefully.
Welcome to the InstallShield Wizard for C/C++ compiler package for RX family The InstallShield Wizard will install C/C++ compiler package for RX family on your computer. To continue, click Reat High-performance Embedded Workshop*	High-performance Workshop
Click on [Next].	Do you accept all the terms of the pre-oding License Agreement? If you piets the setup will be to install CPC+ compiler package for PSC family, you must accept this agreement.
C/C compiler package for RX family V.1.00 Release 00 Region Selection	C/C++ compiler package for RX family V.1.00 Release 00 Start Copying Files Review settings before copying files.
Migh-performance Workshop? Migh-performance Workshop? Select [Europe, USA, Africa or Middle East] and click on [Next].	Stabulation Stabulation
C/C++ compiler package for RX family V.1.00 Release 00	
InstallShield Wizard Complete The InstallShield Wizard Longbete The InstallShield Wizard has successfully installed C/C++ complete package for FX family. Clock Make sure that the installation is complete and click on [Finish].	

(4) Installation of the C/C++ compiler package starts. Follow the procedure shown in Figure 3.12.

Figure 3.12 Installing the C/C++ Compiler Package



- (5) The auto-update utility is also automatically installed when it has been selected in the [Choose software] dialog box. Follow the instructions shown on the screen. Details on the installation process are omitted here.
- (6) The dialog box shown in Figure 3.13 appears when the installation of the compiler package has been completed. Click on the [Exit] button to finish this process.



Figure 3.13 Installation Completed

3.4 Installing the USB Driver

(1) Connect the E1 emulator and host computer via a USB interface cable. When the dialog box shown in Figure 3.14 appears, follow the instructions on the screen to install the USB driver. The pictures below are from a computer running Windows[®] XP. The display varies with the operating system.



Figure 3.14 Found New Hardware Wizard



3.5 Checking That the Emulator Debugger and Compiler Have been Installed Follow the procedure below to check that the emulator debugger and compiler have successfully been installed.

(1) Select [Multiple Install Manager] from the start menu as shown in Figure 3.15.



Figure 3.15 Starting up the Multiple Install Manager

(2) When [Multiple Install Manager] is started up, the [Maintenance] dialog box appears (Figure 3.16). Read the contents and check that all of the required products have been installed. If anything is missing, re-install it.



Figure 3.16 Checking the Products That Have been Installed



3.6 Checking That the USB Driver Has been Installed

To check that the USB driver is working correctly, see the state of the ACT LED on the E1 emulator after connecting the emulator and host computer via a USB interface cable. Table 3.1 shows the states of the ACT LED and their meanings.

ACT LED	Communication between the Host Computer and Emulator	Checkpoint
Illuminated	The E1 emulator is usable.	The USB driver has correctly been installed.
Blinking	The E1 emulator is not usable yet because the USB driver has not been recognized.	Check whether the USB driver has correctly been installed (by following the procedure below).
Not illuminated	Communication has not been established.	Check whether the USB interface cable is correctly connected, the host computer and emulator are supplied with power, and no items are damaged.

Follow the procedures below to check whether the USB driver has successfully been installed.

- (1) Open the properties window for [My Computer] ([System Properties] dialog box).
- (2) Open the [Hardware] tabbed page and click on the [Add Hardware Wizard] button as shown in Figure 3.17.



Figure 3.17 [System Properties] Dialog Box



(3) Check that [Renesas E-Series USB Driver] is shown under the [Renesas Emulator] category (Figure 3.18).

Device Manager Device Manager	Ports (COM & LPT) Processors Renesas Emulator Renesas E-Series USB Driver Sound, video and game controllers System devices Universal Serial Bus controllers
Check that the USB driver has been installe	d.

Figure 3.18 Device Manager

Some drivers under the [Renesas Emulator] category may have a [!] or [?] mark attached to the icon. These drivers may not have been successfully installed, so delete and then re-install them by following the procedure given in section 3.1, Before Installation.



Figure 3.19 Display Indicating Failed Instillation of a USB Driver

If you cannot find any driver under the [Renesas Emulator] category, check that the emulator software has been installed and repeat the procedure given in section 3.4, Installing the USB Driver.

If you have found [Renesas E-Series USB Driver] under the [Renesas Emulator] category but the ACT LED is blinking (i.e. the E1 emulator is not usable), delete [Renesas E-Series USB Driver] and then re-install it.



4. Let's Try Using the E1 Emulator

This section describes the basic usage of the E1 emulator with regard to the tutorial program installed in the host computer during installation of the E1 emulator software.

4.1 Emulator Operating Procedure

This section describes the emulator operations (High-performance Embedded Workshop activation, tutorial program downloading, and program execution) and usage of the main debugging functions of the E1 emulator.

Table 4.1 shows the operating procedures (this assumes that the necessary installation is completed).

Step	Operation	Reference Section in This Guide		
1	Connect the E1 emulator to the host computer through the USB interface cable	4.2 Connecting the Emulator		
2	Connect the user system			
3	Activate the High-performance Embedded Workshop	4.3 Activating the High-performance Embedded Workshop and Loading the Tutorial		
4	Open the workspace			
5	Set up the E1 emulator during booting up	4.4 Setting up the E1 Emulator		
6	Configuration property			
7	Open a source file	4.5 Opening a Source File		
8	Build (compile and link)	4.6 Building a Program (Compiling and Linking)		
9	Download a program	4.7 Downloading a Program		
10	Reset the CPU	4.8 Resetting the CPU		
11	Start and stop the program	4.9 Starting and Stopping Program Execution		
12	Break functions	4.10 Break Functions		
13	Refer to or modify C-language variables	4.11 Referring to or Modifying C-Language Variables		
14	Refer to or modify data through the memory window	4.12 Referring to or Modifying Data through the [Memory] Window		
15	Refer to or modify registers	4.13 Referring to or Modifying Register Values through the [Register] Window		
16	Refer to or modify I/O registers	4.14 Referring to or Modifying I/O Register Values through the [I/O] Window		
17	Analyze the performance	4.15 Analyzing Performance		
18	Trace function	4.16 Trace Function		
19	Terminate debugging	4.17 Terminating Debugging		
20	Operate the user board as a stand-alone unit	4.18 Operating the User Board as a Stand-Alone Unit		

Table 4.1 Tutorial Program Execution Procedure



4.2 Connecting the Emulator

Figure 4.1 shows the configuration of the E1 emulator system.



Figure 4.1 System Configuration with the Emulator

Connect the E1 emulator system through the following steps.

- (1) Connect the E1 emulator to the host computer through the USB interface cable.
- (2) Connect the E1 emulator to the user system (CPU board) through the user-system interface cable.
- Note: When using a separate power supply for the user system, be sure to check that the power is not supplied before connecting the E1 emulator.

4.3 Activating the High-performance Embedded Workshop and Loading the Tutorial

Activate the High-performance Embedded Workshop and open the tutorial workspace through the following steps.

(1) From [All Programs] in the [Start] menu, select [Renesas -> High-performance Embedded Workshop -> High-performance Embedded Workshop] as shown in Figure 4.2 to activate the High-performance Embedded Workshop.



Figure 4.2 Activating the High-performance Embedded Workshop



- (2) The [Welcome!] dialog box will appear on the High-performance Embedded Workshop screen as shown in Figure 4.3. Select the [Browse to another project workspace] radio button and click on the [OK] button.
- (3) The [Open Workspace] dialog box shown in Figure 4.3 will appear. Specify "Tutorial.hws" and click on the [Select] button.
 When the software of this product has been successfully installed, workspace "Tutorial.hws" is stored in the folder shown below.

<Drive where the OS is installed>:

\WorkSpace\Tutorial\E1E20\RX600\Tutorial_LittleEndian

ER Wee Prints Mall Delig Situs Tool Tool Year Wides Hele	Open Workspace	?×
We knowed Create a new priject workspace Create a new priject workspace Create and the priject workspace Create and the priject workspace	Look jn: Control LittleEndian	. ← È ☆ E.
2. 이 14 AT 월 일T 2 월 월 후	File <u>pame:</u> Tutorial.hws Files of <u>type:</u> HEW Workspaces (".hws)	▼ Cancel
العلم المعامل ا 		

Figure 4.3 Selecting a Workspace in the [Welcome!] Dialog Box

If the version of the High-performance Embedded Workshop used for creating the installed tutorial workspace is older than what is currently used in your computer, the dialog box shown in Figure 4.4 will appear.

In this case, to update the workspace version to the current High-performance Embedded Workshop version, click on the [OK] button.

High-performance Embedded Workshop					
1	The Workspace you are about to open was created with an earlier version of HEW. The data files for the workspace, projects and sessions will be updated. Once updated this workspace cannot be opened by an older version of HEW. Backup versions of your old files will be created in the workspace and project directories with the prefix 'old_version_xxx'. Do you wish to continue ?				
Cancel					

Figure 4.4 Dialog Box Shown for Old-Version Workspace



4.4 Setting up the E1 Emulator during Booting up

(1) The [Initial Settings] dialog box shown in Figure 4.5 will appear. Make the necessary settings as shown in the figure and click on the [OK] button.

When the [Initial Settings] dialog box does not appear, switch the session and click on the [Connect] button as shown in Figure 4.6.

Initial Settings Device Communication MCU group: RX610 Group Device: R5F56108 Mode	Select the MCU actually mounted on the user system. In the sample user system used in this guide, select as follows: MCU group: RX610 Group Device: R5F56107 When using the E1 emulator for debugging, select [Debugging mode]; when using the E1 emulator as a simple flash-memory programmer, select [Writing the on-chip flash memory mode].
Execute the user program after ending the debugger. Power supply ✓ Power target from the emulator. [MAX 200mA] ⓒ 3.3V ○ 5.0V Communication Emulator. Entresh	In this example, select [Debugging mode]. To supply power from the emulator to the user system, select the power supply. Only 3.3 V can be selected for the RX610 group. In this example, select the power supply as shown, to supply 3.3-V power to the user system.
OK Cancel Do not show this dialog box again.	The serial number of the current emulator is displayed. If no serial number is displayed, click on the [Refresh] button. If the serial number is still not displayed, check the connection of the USB interface cable and the settings of the USB driver.
JTAG Clock: 165 MHz	Specify the clock for the JTAG interface between the MCU and the E1 emulator. In most cases, the default setting (16.5 MHz) should be used.
OK Cancel Do not show this dialog box again.	

Figure 4.5 Initial Settings



□ ■ × Debug • SessionRX600_E1_E20_• > DefaultSession SessionRX600_E1_E20_SYS
When SessionRX600_R1_E20_SYSTEM is not displayed, select SessionRX600_R1_E20_SYSTEM.
Tutorial - High-performance Embedded Worke
- File Edit View Project Build Debug Setyp Tool: □ □ ☞ □ ☞ □ ☞ 중 ▷ ☞ 문 ↔
When the initial setting dialog box is not displayed even if SessionRX600_R1_E20_SYSTEM is selected, click on the [Connect] button.

Figure 4.6 Session Switching and [Connect] Button

While the connection processing is in progress, the [Connecting] dialog box shown in Figure 4.7 is displayed.

Connecting	
E1 Rev.02 FPGA Info. FPGA Status CONFIGURED FPGA Status CONFIGURED FPGA Version 08 Communication FPGA Version 08 User VCC 3.29 V Target Board CONNECTED Setting of debugging information. Setting of debugging information. Setting of JTAG clock. Connecting to MCU	
Close the dialog box when the connection is completed.	

Figure 4.7 Dialog Box Shown during Emulator Connection



(2) The [Configuration Properties] dialog box shown in Figure 4.8 will appear. Check that the options in the [MCU] and [System] tabs are set as shown in Figure 4.8 and click on the [OK] button.

Configuration Properties Internal flash memory overwrite External flash memory MCU System Operating mode Mode: Single-chip mode Endian: Input clock (EXTAL): 12.5000 MHz External memory areas Area Endian BUS Width	Mode: Select the MCU operating mode used for the target program to be debugged. In this example, select the single-chip mode. Endian: The endian specified in the user system is automatically displayed. Input clock: Specify the frequency of the clock signal connected to the XTAL pin of the MCU. In the RX610 CPU board used for this example, a 12.5-MHz clock signal is connected.
Work BAM start address (0x400 3000 bytes used): OK Cancel	Specify the work RAM area to be used for the emulator. In most cases, the default setting should be used. However, when using the default area as the DMA or DTC transfer source or destination, specify another area. In this example, use the default setting.
Configuration Properties	The E1 emulator does not support the realtime RAM monitoring function, and the radio button for the function cannot be selected.
Debug function	Select this function when debugging a program that erases or overwrites the on-chip flash memory contents. The tutorial program does not overwrite the on-chip flash memory contents; do not select this function in this example.
	The other options should be left as the default settings. For details of the other options, refer to the E1/E20 Emulator User's Manual.
OK Cancel	

Figure 4.8 [Configuration Properties] Dialog Box

(3) The [Connecting] dialog box will appear.



(4) When the emulator has been connected successfully, "Connected" will be displayed in the output window and the debugging-related buttons in the upper toolbar will become active as shown in Figure 4.9.

76	OrderAll - High-performance Embedded Workshop ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Window Help ● GR Were Project Dubi Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Tools Text Were Project Debug ● GR Were Project Debug Schup Text Text Were Project Debug ● GR Were Project Debug Schup Text Text Text Text Text Text Text Text
	Debugging-related buttons become active.
	Connected" is displayed
	⊈ ⊥ ∫ Đưới λ Dechug √ Find in Files 1 → Find in Files 2 → Macro → Test → Version Control /
	rmal - 100.000.000.000 余 adv - 回聞 開 回 Defusit 2 desitop

Figure 4.9 Confirming Emulator Connection

4.5 Opening a Source File

To open a source fie, double-click on the source file name in the workspace window. For example, to open Tutorial.c, double-click on the file name as shown in Figure 4.10.

The window for displaying the source code will open; it is called the editor window because the source code can be modified in this window.



Figure 4.10 Opening a Source File



4.6 Building a Program (Compiling and Linking)

To build a program, click on the [Build] button on the toolbar.

When the build process has been completed, the build results are displayed in the output window. In this tutorial program, some warning messages will appear but ignore them.

Note: When the build process is done for the first time, the standard library is also compiled and the process takes some time. In the second or later time, compiling the standard library is not necessary and the processing time will be reduced unless any option is modified.



Figure 4.11 Build Execution and Results

The workspace for the tutorial program is set up so that the program is automatically downloaded after the build process. When the dialog box shown in Figure 4.12 appears, click on the [Yes] button to start downloading the program.

Confirmation Request
0K to download module: C:\WorkSpace\Tutorial\E1E20\RX600\Tutorial_LittleEndian\Tutorial\Debug\Tutorial.abs
\square Don't ask this question again
Yes No Yes to all No to all Cancel

Figure 4.12 Confirmation of Downloading



4.7 Downloading a Program

In addition to using the automatic downloading function after a build process, a program can be downloaded through the following procedure.

(1) Double-click on the load module file name ("Tutorial.abs" in this example) displayed under [Download module] in the workspace window as shown in Figure 4.13.



Figure 4.13 Downloading a Program

(2) When loading is completed, a down arrow is added on the file icon as shown in Figure 4.14.



Figure 4.14 Program Downloading Completed

4.8 Resetting the CPU

To reset the CPU, select [Reset CPU] from the [Debug] menu or click on the [Reset CPU] button on the toolbar.

This will reset the registers to the initial values and set the program counter to the reset vector address



Figure 4.15 [Reset CPU] Button



4.9 Starting and Stopping Program Execution

Click on the [Go] button shown in Figure 4.16 to start program execution.

The [Reset Go] button is also available to start program execution after resetting the CPU.

[Go] button			[Reset Go] button	
# 1 4			1] 타 아 아 🚥 다 😽	
		(Reset Go	
Line	Source Ad	0 S	Source	
63			//#endif	
64				
65			<pre>#pragma section ResetPRG</pre>	
66				
67			<pre>#pragma entry PowerON_Reset_PC</pre>	
68	FFFF8000	_	void PowerON Reset PC(void)	
70	rrroooo	"	(
71	FFFF800E		<pre>set intb(sectop("C\$VECT"));</pre>	
72	FFFF8017		<pre>set fpsw(FPSW init);</pre>	
73				
74	FFFF801E		_INITSCT();	
75				
76			// _INIT_IOLIB();	// Use SIM I/
77			// errno=0;	// Remove the
79			// srand((UINT)1);	// Remove the
.80			// sintr=NIILL:	
•				
Tut	orial.c 🧇	resetpri	a.c	

Figure 4.16 [Go] Buttons

The [Halt] button shown in Figure 4.17 is active during program execution.

Click on this button to stop program execution.



Figure 4.17 [Halt] Button

Figure 4.18 shows the editor window after program execution stops.

The line highlighted in yellow corresponds to the program counter address when execution stops.





Figure 4.18 Editor Window after Program Execution Stops

4.10 Break Functions

The E1 emulator provides two types of break function: software break function and on-chip break function.

Table 4.2 shows the characteristics of the software break and on-chip break functions. Please refer to the table to choose break functions suitable for each debugging purpose.

		Specifiable		Flash Memory	Availability During Program
Bre	eak Function	Points	Break Condition	Overwriting	Execution
So	ftware break				
		256 points	Specified address	Overwritten	Specifiable
					(only in the on-chip RAM area)
Or	n-chip break				
	Pre-PC break	8 points	Specified address	None	Specifiable
	Event break (execution address)		Specified address	None	Specifiable
	Event break (data access)	4 points	Data access	None	Specifiable
	Trace full break	-	Trace buffer becomes full	None	Not specifiable

Table 4.2 Break Function Specifications

Software break can be specified at many points, but this function is implemented by modifying the contents of the specified addresses to dedicated break instructions; extra time is needed to modify the contents during program execution after breakpoints are specified. Accordingly, the on-chip break function is recommended in most cases and the software break function should be used only for the addresses where breakpoints will not be deleted and reset repeatedly.

This section describes the procedure for specifying the on-chip break function, which does not require flash-memory overwriting.



(1) Set a breakpoint.

Double-click on the on-chip break column shown in Figure 4.19 and a blue-filled circle will appear. A breakpoint can be specified only for a line where an address is shown in the [Source Address] column.



Figure 4.19 Setting a Breakpoint



(2) Click the [Reset Go] button as shown in Figure 4.20 to start program execution. Program execution will stop at the breakpoint specified in the previous step. The line corresponding to the program counter address where execution stops is highlighted in yellow as shown in Figure 4.20 and ":Before break" is displayed in the output window, which indicates that execution has stopped due to a break.



Figure 4.20 Execution Stopped at a Breakpoint

(3) To clear a breakpoint setting, double-click on the blue-filled circle as shown in Figure 4.21.



Figure 4.21 Clearing a Breakpoint Setting



4.11 Referring to or Modifying C-Language Variables

The emulator provides the following functions for referring to or modifying C-language variables.

- 1 [Watch] window for referring to or modifying variables The values of any variables used in a program can be referred to or modified through this function. This function is available while program execution is in progress as well as when execution is stopped.
- 2 [Local] window for referring to or modifying variables The local variables used in the function pointed to by the current PC value are automatically displayed and can be modified.
- 3 Tooltip watch function for referring to variables The value of a variable in the source file displayed in a window can be easily checked by simply placing the cursor on the displayed variable name.

4.11.1 Using the [Watch] Window to Refer to or Modify Variables

The E1 emulator provides the [Watch] window, through which the values of any variables used in a program can be monitored.

This section describes how to use the watch function to check the changes in the value of global variable "int-type g_IntBuf" used in sample program sort.c

Note: When a local variable is specified to be watched, its value can be referred to only while the current PC address is within the function that has declared the local variable.

(1) Use the [Watch] window to refer to variables.

Select [View -> Symbol -> Watch] as shown in Figure 4.22 to open the [Watch] window.

MAP Map									
Command Line	Ctrl+L	-			R 🗗 🗹 / 🏫 :		7		
🍇 TCL Toolkit	Ctrl+Shift+K	-		Ne	me Val	ie	Address	Type	Scope
Workspace	Alt+K	-							
🔁 Output	Alt+O								
🔲 Status Bar	Alt+A			1	Murra (un to)				
🚰 Disassembly	Ctrl+D				Watch1 (Watch2)	/ watch3 / watch4	./		
<u>C</u> PU		•		/					
Event		•							
		🖉 L <u>a</u> bels	Ctrl+Shift+A						
<u>S</u> ymbol		💹 <u>W</u> atch	Ctrl+W						
<u>Symbol</u> Code		88a watti	contri						

Figure 4.22 [Watch] Window



(2) Select g_IntBuf and then drag and drop it into the [Watch] window. Check that g_IntBuf is added to the [Watch] window as shown in Figure 4.23.



Figure 4.23 Watch Setting

(3) As shown in Figure 4.24, set an on-chip breakpoint at line 37 in sort.c, where "g_IntBuf = j;" is written. For details of the breakpoint setting procedure, refer to section 4.10, Break Functions.

Line	Source Ad	0.	S	Source	
25				//	<u> </u>
26					
27	FFFF85E7			sort(long *a)	
28				,¢	
29				long t;	
30				int i, j, k, gap;	
31					
32	FFFF85EC			gap = 5;	
	FFFF85F1			while $(gap > 0)$ (
34	FFFF85FB			<pre>for(k=0; k<gap; k++)="" pre="" {<=""></gap;></pre>	
	FFFF860C			for (i=k+gap; i<10; i=i+gap){	
	FFFF8620			for(j=i-gap; j>=k; j=j-gap)(
	FFFF863)	g_IntBuf = j;	
	FFFF8640	\sim	_	if (a[j]>a[j+gap])(
	FFFF865F			t = a[j];	
	FFFF866C			a[j] = a[j+gap];	
	FFFF8687			a[j+gap] = t;	
42				}	
43				else	
44				break;	
45				}	النے
<u> </u>			_	<u>`</u>	<u> </u>

Figure 4.24 Setting a Breakpoint

(4) After the above settings are completed, click on the [Reset Go] button to start program execution.



(5) At the first break, the program will stop before the specified line is executed and g_IntBuf will hold 0, which is the initial value.



Figure 4.25 g_IntBuf Value after the First Break

(6) Click on the [Go] button instead of the [Reset Go] button to start program execution again; execution will break at the same point. After the break, check the change of the g_IntBuf value through the [Watch] window. Repeat these steps and check that the g_IntBuf value is incremented from the initial value: 0 -> 1 -> 2 -> 3 -> ...

Watch			X
R Ŗ 📑 🎫	/ 🛍 🗙 🧬 🖻	E Rest	
Name	Value	Address	Type
🛛 g_IntBuf	н'0000001	{ 00001404 }	(int)
	Check the c	hanges of the value.	
4			Þ
✓ ► Watch1 / W	/atch2 À Watch3 À Watch	14 /	

Figure 4.26 g_IntBuf Value after Several Breaks



(7) The [Watch] window has four tabs and four combinations of variables can be monitored by switching the tabs.



Figure 4.27 Switching Tabs in [Watch] Window

(8) Variable values can be modified in two ways as shown in Figure 4.28. One way is through in-place editing by single-clicking on the variable value. Another is through the dialog box opened by double-clicking on the variable.

R R 🗗 🗹	/ 翰 🗙 🍠 🛛				
Name	Value	Edit by singl	e-clicking		
🖳 🖳 g_CharBu	f H'00	{ 00	001000 }	(char)	
	н'00000005	{ 00	0016D0 }	(int)	
Edit throug	the dialog	\rightarrow	Edit Value		
box opened double-click	lby		Expression:	gap	
		Watch4 /	Current Value:	H'00000005	
			<u>N</u> ew Value:	H'0000005	
			-		<u>L</u>

Figure 4.28 Modifying Values



(9) The changes of a value during program execution can be monitored also by enabling the automatic updating function for the [Watch] window.

To try the automatic updating function, clear all breakpoints that have been specified. After that, select a variable in the [Watch] window and then enable the automatic updating function by clicking on the [Auto Update] button as shown in Figure 4.29. For the variable for which the automatic updating function has been enabled, the R mark color changes from white to black.



Figure 4.29 Enabling Automatic Updating for the Target Watch Item

(10)After enabling the automatic updating function, execute the program.

The changes of the g_IntBuf variable can be monitored.

Vatch R R 🖬 🎫 🌌	1 悄× 3 2	r 😌	
Name	Value	Address	Type
- R g_IntBuf	н'0000008	{ 00001404 }	(int)
	Check th	ne changes of the val	ue.
∢ ∢ → Watch1 √	Watch2 👌 Watch3 👌 Wa	tch4 /	

Figure 4.30 [Watch] Window with Automatic Updating Function Enabled

(11)The changes of values can also be recorded.

Recording of the automatically updated values can be started only while program execution is stopped. Stop program execution and then click on the record starting icon shown in Figure 4.31; recording of the automatically updated values will start.

× r r / 🎫 🖊	🎕 🗙 🥩 🕜	R 😳
Name	Value	Start recording
R g_IntBuf	н'00000000	recording

Figure 4.31 Starting Recording of Automatically Updated Values





(12)When recording is started, the [Record Settings] dialog box shown in Figure 4.32 will appear. Select the [Watch] window sheet that you want to record and enter a file name with which the record is to be saved.

Record Settings
Recording <u>Sheet:</u> Watch1 Cancel
File <u>n</u> ame:
test_save Browse

Figure 4.32 [Record Settings] Dialog Box

(13)After starting recording, execute the program.

After checking that the value is automatically updated, click on the [Stop Recording] button with the desired timing; a text file with the file name specified in the [Record Settings] dialog box will be created in the specified folder.

Figure 4.33 shows an example of the created file.

🗒 test - WordP	Pad	
<u>Eile E</u> dit <u>V</u> iew 3	Insert Format <u>H</u> elp	
	3 🖪 🖊 X 🖻 🛍	<u>a</u> 🗠 💀
g_IntBuf		
Hex		
0000007		
0000002		
0000006		
0000007		
0000007		
0000001		
0000008		
0000000		
0000006		
0000004		
0000008		
0000008		

Figure 4.33 Result of Automatic Updated Value Recording

(14)To stop recording automatically updated values, click on the [Stop Recording] button.

×	R R 🗖 🏧 🥢	7 🛍 🗙 🗗 🎜 📌 🧬	^{I62D}
	Name	Value	Stop
	- R g_IntBuf	н'00000000	recording




4.11.2 Using the [Local] Window to Refer to or Modify Variables

Use the [Local] window to refer to local variables.

Unlike the [Watch] window, the [Local] window automatically displays the local variables used in the function that is pointed to by the PC value when program execution is stopped and allows the values to be modified.

The local variables are stored in a stack and cannot be referred to during program execution.

(1) Select [View -> Symbol -> Locals] as shown in Figure 4.35 to open the [Local] window.

Differences			et et et et (+) (),					
мяр Мар								
🔎 Command Line	Ctrl+L							
🍇 TCL Tool <u>k</u> it	Ctrl+Shift+K		S. Source					
	Alt+K		//#ifdefcl					
🔁 O <u>u</u> tput	Alt+O		//} //#endif	-				
🔲 Status Bar	Alt+A		//#endii	×	16 10 8 2			
👰 Disassembly	Ctrl+D		#pragma sect:	Ī	Name	Value	Type	
CPU		•	#pragma entry					
Event		, 3000	⇔ <mark>void PowerON_</mark>					
<u>S</u> ymbol		🕨 🖉 L <u>a</u> bels	Ctrl+Shift+A	7				
Code		• 🕅 <u>W</u> atch	Ctrl+W					
Performance		ka Locals	Ctrl+Shift+W	-	4			

Figure 4.35 Opening the [Local] Window

(2) Execute the program and stop it with the desired timing.

The [Local] window will automatically display the variables used in the function that is pointed to by the PC value when execution is stopped as shown in Figure 4.36.

Line	Source Ad	0	S., Source					
27	FFFF85E7		sort(long *a)					
28			(
29			long t;					
30			int i, j, k, ga); 				
31			_					
	FFFF85EC		gap = 5;					
	FFFF85F1		while(gap 0)					
	FFFF85FB FFFF860C		for (k=0, k-		4 - 4 A			
	FFFF8620			<mark>t+gap; i<10;</mark> (j=i-gap; j>=		. (
	FFFF8633		101	g IntBuf = f		1 (
	FFFF8640			g_incbui = _				
	FFFF865F			1 16 10	8 2			
	FFFF866C							
41	FFFF8687			Name	Valu	e	Туре	
42				a	0x00	0016dc { 0	(long*)	
43				•t	н'00	000030 { 0	(long)	
•				i		, ff894f { 0	-	
🧈 Tu	torial.c 🛷	reset	prg.c 🧈 sort.c	- <u>-</u>				
						000030 { 0		
				k	н'00	000000 { 0	(int)	
							(int)	

Figure 4.36 Referring to the [Local] Window



4.11.3 Using the Tooltip Watch Function to Refer to Variables

The tooltip watch function (watch by mouse-pointing) is available either during program execution or while execution is stopped.

Place the mouse cursor on the variable that you want to check, and the value of the variable will be displayed as shown in Figure 4.37.



Figure 4.37 Tooltip Watch



4.12 Referring to or Modifying Data through the [Memory] Window

The watch function allows reference to the variables that have been declared, but use the [Memory] window to refer to or modify data in a desired address area.

	Debug Setup	Tools Test W	
Differences		-	- 49Î
мер Мар			1 1 1 1 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7
Command Line	Ctrl+L		
🍇 TCL Toolkit	Ctrl+Shift+K		
Workspace	Alt+K	a Ad 0 S.	. Source
Dutput	Alt+O		long t;
Status Bar	Alt+A	_	int i, j
👼 Disassembly	Ctrl+D	BSEC	gap = 5;
<u>C</u> PU		R1 Registers	Ctrl+R
<u>E</u> vent		• <u>Memory</u>	Ctrl+M
<u>S</u> ymbol		▶ <u>1/0</u> ĪO	Ctrl+I
Code		🕨 🙀 Stat <u>u</u> s	Ctrl+U
Performance		EamMonito	r
<u>R</u> TOS		DebugCons	sole

(1) Select [View -> CPU -> Memory] as shown in Figure 4.38 to open the [Memory] window.

Figure 4.38 Opening the [Memory] Window

(2) The [Display Address] dialog box shown in Figure 4.39 will appear. Enter 1000H as the display start address, and the [Memory] window will open as shown in Figure 4.40.

Figure 4.39 [Display Address] Dialog Box

×			<u>6</u> <u>10</u>	± <u>10</u>	8 2	db	a s	ある	i iii	đe	£.	.d .16	.32	2				
	Address	+0	+1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+C	+D	+E	+F	ASCII
	00001000	ЗC	08	4C	72	19	19	FЗ	42	F2	12	05	в5	77	50	07	47	<.LrBwP.G
	00001010	в6	16	44	CD	BC	98	7C	41	в2	CF	ΟE	6 F	71	8A	67	5C	D Aoq.g\
Ш	00001020	31	26	86	1E	ЗC	54	32	EE	в4	93	50	61	D4	11	23	2A	1& <t2pa#*< td=""></t2pa#*<>
Ш	00001030	30	ЗD	в5	FC	в2	Oв	F7	5в	E9	25	04	8A	F1	D2	18	28	0=[.%(
Ш	00001040	1B	A2	6E	вЗ	99	14	74	F5	DA	6В	2C	4c	BB	24	6F	42	ntk,L.\$oB
	00001050	4c	\mathbf{BF}	5F	ЗC	9B	29	DO	C2	55	77	1B	39	A1	C2	4c	37	L<.)Uw.9L7
	00001060	33	40	2 E	84	C2	41	в9	9C	66	A3	3C	4C	71	D4	FF	57	30Af.⊲LqW
	00001070	17	C2	C8	D1	15	97	70	D5	44	AF	06	E5	4A	A2	00	04	p.DJ
	00001080	8A	0A	F7	в8	С1	вО	4B	вО	28	OF	ΟD	F6	89	92	87	E4	K.(

Figure 4.40 [Memory] Window



× III	••• :::	<u>16</u> <u>10</u>) ± <u>10</u>	82	db	a	あま	i ili	đe	.f.	t. b.	6.32	Ø					1-byte display
Addre	ss +0	+1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+C	+D	+E	+F	ASC	
00001	000 3C	08	4c	72	19	19	FЗ	42	F2	12	05	в5	77	50	07	47	<.L	
00001	D10 B6		44	CD	BC	98	7C	41	в2	CF	ΟE	6F	71	8A	67	5C	D	
00001			86	1E	3C	54	32	EE	в4	93	50	61	D4	11	23	2A	16.	
00001			в5	FC	в2	OB	F7	5B	E9	25	04	8A	F1	D2	18	28	0=.	
00001		A2	6E	B3	99	14	74	F5	DA	6B	2C	4C	BB	24	6F	42	n	
000(×	1 11		• <u>16</u>	<u>10</u> ±	10 8	2	dbc	a 7	ある		旋	b. 1.	.16	.32	2			2-byte display
000	Addre	33	+0	+2		+4	+6		+8	+A		+C	+E		ASCI	I		
0000	00001	000	083C	724	4C	1919	42	F3	12F2	в5	05	5077	47	07	<.Lr	E	8wP	
	00001	010	16B6	CD4	14	98BC	41	7C	CFB2	6F	ΟE	8A71	5C	67	D.		oq.	
	00001	020	2631	1E8	86	543C	EE	32	93B4	61	50	11D4	2A	23	16	<т2.	Pa;	
	00001		3D3O	FCI	35	Овв2	5B	F7	25E9	8A	.04	D2F1	28	18	0=	[_
	000 ×		" "		<u>16</u>	<u>10</u> ± <u>10</u>	8	2	dbc 👌	8 क	iii	in a	i J	d.	.16 .3	2	2	4-byte display
	000	Add	ress	+0			+4		+	8		+C		2	ASCII			
	000		0100	-	4C0			3191		5051;			7507				wP.0	
	000		0101	-	441			с98в		FOECI			78A7				··od·d,	
	<u> </u>		0102	-	862		EE32			1509:			311D				.Pa#*	
			0103	-	B53			70BB		A042			BD2F			-	*	
			0104		6EA		F574			C2C61			F24B				k,L.\$oH	
			0105		5FB		C2D			91B7 2221			CC2A		_		₩.9L7	
			0106		2E4 .c8c		9CB9	941C 0971		C3CA: 506A)			FD47 DA24				.⊲⊥qĭ	
			0107		F70.		BO41			506A. 60D01			JA24. 7928			-	J	
		000	0100	ро	E 70.	AOA	0041	DOC	тг	0000	r 20	P.40	1940	2	• • • • •	• • • • (

(3) The data display size can be changed. See Figure 4.41 for the access size change.

Figure 4.41 Changing Data Display Size in [Memory] Window

Notes: 1. In principle, data is accessed in the specified display size in the [Memory] window.

2. When the [Memory] window is used in the I/O register area, the access size allowed for the target register should always be selected as the display size. To refer to or modify the I/O register area, use the [I/O] window instead of the [Memory] window, because data is accessed in the correct access size automatically in the [I/O] window.

(4) Memory data can be modified in two ways.

	1 11 111 3	•	⁶ In	-pla	ice e	editi	ng b	y si	ngle	-clic	cking	. 16	5.32	Ø				
	Address	+0	+1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+C	+D	+E	+F	ASCII
	00001000	- 3 C	08	4c	72	19	19	FЗ	42	F2	12	05	в5	77	50	07	47	$<.{\tt Lr}\ldots{\tt B}\ldots{\tt wP}.{\tt G}$
	00001010	В6	16	44	CD	BC	98	7C	41	в2	CF	ΟE	6 F	71	8A	67	5C	D Aoq.g\
	00001020	31	26	86	1E	ЗC	54	32	ΕE	в4	93	50	61	D4	11	23	2A	1& <t2pa#*< td=""></t2pa#*<>
	00001030	30	ЗD	в5	FC	B2	OВ	F7	5B	E9	25	04	8A	F1	D2	18	28	0=[.%(
	00001040	1B	A2	6E	В3	99	14	74	F5	DA	6B	2C	4C	BB	24	6F	42	ntk,L.\$oB
	00001050	4C	BF	5F	ЗC	9B	29	10	C2	55	77	1B	39	Se	at			2
	00001060	33	40	2 E	84	C2	41	в9	98	66	A3	ЗC	4C		au			<u> </u>
	00001070	17	C2	C8	D1	15	97	70	D5	44	AF	06	E5					
	00001080	8A	OA	F7	в8	С1	вО	4в	вО	28	OF	QD	F6	1	Addre	SS:	10000	D1033 🔄 🗾
								-			alog click					-		
														Γ	Ve	-	ЭК	Cancel

Figure 4.42 Modifying Memory Data



(5) The [Memory] window shows the values acquired when a break occurred. Click on the [Refresh] button to check the latest values.

	••• []	<u>6</u> 10	± <u>10</u>	8 2	db	c 33	ある	in ilin	ðe	£.	.d .16	5.32		[R	efre	esh]	button
Address	+0	+1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+C	+D	+E	+F	ASCII
00001000	3C	08	4c	72	19	19	FЗ	42	F2	12	05	в5	77	50	07	47	<.LrBwP.G
00001010	B6	16	44	CD	BC	98	7C	41	в2	CF	OE	6F	71	8A	67	5C	D Aoq.g\
00001020	31	26	86	1E	3C	54	32	EE	в4	93	50	61	D4	11	23	2A	1& <t2pa#*< td=""></t2pa#*<>
00001030	30	ЗD	в5	FC	в2	Ов	F7	5в	E9	25	04	8A	F1	D2	18	28	0=(
00001040	1B	A2	6E	вЗ	99	14	74	F5	DA	6B	2C	4c	BB	24	6F	42	ntk,L.\$oB
00001050	4C	BF	5 F	3C	9в	29	DO	C2	55	77	1B	39	A1	C2	4c	37	L<.)Uw.9L7
00001060	33	40	2 E	84	C2	41	в9	9C	66	A3	ЗC	4C	71	D4	FF	57	30Af.⊲LqW
00001070	17	C2	С8	D1	15	97	70	D5	44	AF	06	E5	4A	A2	00	04	p.DJ
00001080	8A	0A	F7	в8	C1	вО	4B	вО	28	OF	OD	F6	89	92	87	E4	K. (

Figure 4.43 Updating the Display

(6) As with the [Watch] window, the changes of values during program execution can be checked.

	+0	<u>6 10</u> +1	+2	+3	+4	+5	+6	+7	+8	+9	+A	+B	+	<u>S</u> et	
Address 00001000	3C	08	4C	72	19	19	F3	42	F2	12	05	B5	7	Eill	
00001010	в6	16	44	CD	BC	98	7c	41	в2	CF	OE	6 F	7	Move	
00001020	31	26	86	1E	ЗC	54	32	EE	в4	93	50	61	D	-	
00001030	20	30	25	PC	P2	0.0	57	50	7 0	25	04	8A	F	<u>⊂</u> ompare	
00001040		•			the	-				3	2 C	4c	в	<u>T</u> est	
00001050	wi	ndo	w to	dis	play	the	me	nu.		1	1B	39	A	Save Memory contents	
00001060											30	4C	.7_	Eare Henry Contention	
00001070	17	C2	C8	D1	15	97	70	D5	44	AF	06	ES		<u>S</u> earch	
00001080	8A	OA	F7	в8	С1	вО	4в	вО	28	OF	OD	F6	8	_ Search Next	
													_	Joaren <u>H</u> oxe	
														<u>A</u> ddress	
														S <u>c</u> roll Area	
														Register	•
														-	
														Followed Stack Pointer	
														Set Start <u>U</u> p Symbol	
														<u>R</u> efresh	
														<u>R</u> efresh Lock Refresh	
													[-	

Figure 4.44 Enabling Automatic Refresh Function



4.13 Referring to or Modifying Register Values through the [Register] Window Use the [Register] window to refer to or modify the values of general registers and control registers. Let's try referring to the program counter (PC) value, for example.



(1) Select [View -> CPU -> Registers] as shown in Figure 4.45 to open the [Register] window.

Figure 4.45 Opening the [Register] Window

(2) After the [Register] window opens, click on the [Reset] button. As shown in Figure 4.46, the program counter is set to the value (FFFF 8000H) stored in the reset vector after a CPU reset.

			,
=		I	×
1	Name	Value	^
F	R8	0000000	
F	R9	0000000	
F	R10	0000000	
F	R11	0000000	
F	R12	0000000	
F	R13	0000000	
F	R14	00000004	
F	R15	0000000	
1 T	USP	0000000	
1	ISP	0000000	
F	PSW	000000000000000000000000000000000000000	
II E	PC	FFFF8000	
1	INTB	FFFF8140	
E	BPSW	0000000	
E	BPC	0000000	
E	FINTV	0000000	
F	FPSW	00000100	
A	ACC	000000000000000	
			~
<	<		
_			_

Figure 4.46 Referring to the [Register] Window

Note: The register values during execution cannot be referred to.



(3) Register values can be modified by double-clicking or single-clicking on the displayed register value.



Figure 4.47 Modifying Register Values

- 4.14 Referring to or Modifying I/O Register Values through the [I/O] Window Use the [I/O] window to refer to or modify the values of I/O registers.
- (1) Select [View -> CPU -> I/O] as shown in Figure 4.48 to open the [I/O] window.

Differences		Ī≣	⊒↓ Ef 11 ∃ ↓ {}	ł				
мар Мар								
Command Line	Ctrl+L			_				
🍇 TCL Tool <u>k</u> it	Ctrl+Shift+K	ə Ad C	D., S., Source		ŕ			
Workspace	Alt+K	35E7	sort (long *	In Calific		Address	Value	Access
🔊 O <u>u</u> tput	Alt+O		long t;	E SYSTEM				
🔲 Status Bar	Alt+A		int i,					
👼 Disassembly	Ctrl+D	35EC	gap = 5	E DMAC2				
CPU		R1 Regis	ters Ctrl+R	All Register	· 🖉 Selected Regi	ster /)
Event		• 🞵 Memo	ory Ctrl+M	7				
<u>S</u> ymbol		• <u>∎⁄</u> 0	Ctrl+I					
Code		🕨 🙀 Statu	is Ctrl+U					
<u>P</u> erformance		• <u>Ram</u> M	1onitor					
<u>R</u> TOS		Debug	-C	-				

Figure 4.48 Opening the [I/O] Window



(2) Let's try referring to the compare match timer control register (CMCR) in compare match timer 0 (CMT0).

As shown in Figure 4.49, select CMT0 and expand the module display to see the register values. This window shows the values acquired when a break occurred. To see the current value, right-click on the window to open the menu and select [Refresh] from the menu.



Figure 4.49 Referring to Register Values in the [I/O] Window

Figure 4.50 Refresh Menu



(3) I/O register values can be modified by double-clicking on the displayed I/O register value.

Name	Address	Value	Access 🔺			
🖻 🚔 DMAC1		_				
DMCSA	00082010	4b53ceea				
DMCDA	00082014	D648DC5 <mark>F</mark>				
📃 ДМСВС	00082018	13485c Ed	lit through	the dialog box o	ppened by doub	le-clicking
🗄 🚞 DMMOD	0008201c	Phone Cor				
DMRSA	00082210	Edit DMCSA	at 000820	10	? 🗙	
🔲 DMRDA	00082214					
	00007710	Current value	: 4853CEEA	v	ОК	
All Register Select	ed Register /	New value:	4B53CEEA	•	Cancel	

Figure 4.51 Modifying Register Values in the [I/O] Window

(4) To see only the desired registers, use the [Selected Register] tab.

Name		Address	Value		
	Choose the Se tab and right-cl		er	Refresh Loc <u>k</u> Refresh Load IO File Select IO Register Print	Choose "Select IO Register"
 ▲ ▶ ▲ ▲ 		ct IO Register		✓ Toolbar display	F 3
		lable Registers:		Customize toolbar Allow Docking Hide Check A Uncheck	AII
			[Sele	ct IO Register] dialog	

Figure 4.52 [Selected Register] Tab



4.15 Analyzing Performance

The E1 emulator can measure the elapsed cycles (processing time), the interrupt or exception processing cycles (processing time), or the number of accepted interrupts or exceptions in up to two user-specified sections.

(1) Open the [Performance Analysis] window.

Select [View -> Performance -> Performance Analysis] as shown in Figure 4.53 to open the [Performance Analysis] window.



Figure 4.53 Opening the [Performance Analysis] Window

- (2) As shown in Figure 4.54, select "[No.1] Not use" in the [Performance Analysis] window and double-click on it: the [Performance] dialog box will appear.
- (3) The measurement items can be selected from the pull-down menu in the [Performance] dialog box.

	Performance
No Condition Not use	Condition: Not use
	Image: Constraint of the performance only once. Image: Constraint on the performance on the performan





(4) Let's try measuring the execution time of the sort and change functions.

Specify the performance conditions as follows.

- The count start and end events can be specified by dragging and dropping the target function names.
- 1 Select "Execution cycle" for the [Condition] box in the [Performance] dialog box.
- 2 Select "sort" in the editor window and then drag and drop it into the [Count Start Event] box in the performance condition setting dialog box.
- 3 Click on the [Add] button for the [Count Stop Event] box in the dialog box to open the [Event] dialog box.
- 4 In the event setting dialog box, enter address FFFF87E4 as the measurement end event, which is the address of line 59 in Tutorial.c.
- 5 Click on the [OK] button in the the [Event] dialog box.
- 6 Choose "Display the cycle as time span" in the [Performance] dialog box, enter "25" in the frequency box, and choose "Measure the performance only once".

Choosing "Display the cycle as time span" causes the measured number of elapsed cycles to be converted into time. As the tutorial program is set up so that the CPU operates with a 25-MHz clock, enter 25 as the frequency.

Choosing "Measure the performance only once" means that only the first execution can be measured even if the specified conditions are satisfied repeatedly. When this option is not chosen, the cumulative time will be measured.

7 Check the specified conditions and click on the [Apply] button in the [Performance] dialog box to complete condition setting.



Figure 4.55 Specifying Performance Conditions



Note: This example assumes that the sort and change functions are called only from these addresses.

(5) Check that the settings are reflected in the [Performance Analysis] window as shown in Figure 4.56.



Figure 4.56 [Performance Analysis] Window after Conditions are Specified

(6) After checking that the window reflects the settings, click on the [Reset Go] button to start program execution.

When the specified measurement section has been passed, the [Performance Analysis] window will be updated as shown in Figure 4.57.

× +	*• ו ×3 🔷 • 5 🗗									
No	Condition	Time(h:m:s.ms.us.ns)		Count						
1	Execution cycle	00:00:00.000.098.280		2457						
2	Not use	::		-						
			Results							
			Count: 2457 (tim	nes)						
			Time: 98.28 (μs)							
<										

Figure 4.57 Performance Analysis Results

(7) To clear the measured results, wait until execution stops and then click on the [Clear All Data] button. When the measured results are not cleared, the results of the next measurement will be accumulated on the current results.



Figure 4.58 Clearing the Performance Analysis Results



4.16 Trace Function

The trace function acquires information such as PC values at branches or data access information during user program execution and stores the information in trace memory. By using the trace information, you can track the flow of application execution and examine the points where problems arise.

The E1 emulator provides the internal trace function, which uses the trace buffer in the MCU.

Execution-address and operand-access events can be used to acquire CPU-bus trace information on branches (origin and destination addresses) and data access. The acquired trace information is displayed in the [Trace] window as bus information, disassembled code, or source code.



(1) Select [View -> Code -> Trace] as shown in Figure 4.59 to open the [Trace] window.

Figure 4.59 Opening the [Trace] Window

(2) Open the [Trace conditions] dialog box to specify the conditions for acquiring trace information. Click the [Acquisition...] button in the [Trace] window as shown in Figure 4.60.



Figure 4.60 [Acquisition] Button



(3) Specify desired conditions in the [Trace conditions] dialog box.
 Table 4.3 shows the conditions that can be selected in the E1 emulator.
 In this example, specify conditions as shown in Figure 4.61 so that the trace function acquires the PC addresses for 256 branches immediately before the user program execution stops.

Trace Tab					
Trace Mode					
Fill until stop	The acquisition of trace data continues until the program stops or an event selected to stop tracing occurs.				
Fill until full	The acquisition of trace data stops when the trace buffer becomes full.				
Trace Output					
Do not output	The trace buffer in the MCU will be used.				
	The other options cannot be specified in the E1 emulator.				
Trace Type					
Branch	Branch information is acquired.				
Branch + Data	Branch and data-access information is acquired.				
Data	Data-access information is acquired.				
Trace Capacity	•				
Cannot be					
specified					

Table 4.3 Specifiable Trace Conditions (for E1 Emulator)

Note: For other trace conditions, refer to the user's manual.

Trace conditions Trace Condition and combination setting Trace Start Event in use : 0 Detail Event in use : 0 Detail Extract Event in use : 0 Detail Trace Trigger Total : 0 events
Trace Setting Trace Mode: Trace Output: Do not output (Internal buffer used) Trace Type: Branch Trace Capacity:
Event used PC: 0 OA: 0 Free PC: 8 OA: 4 Registered events Save Load Apply

Figure 4.61 Specifying Trace Conditions



(4) To stop program execution, specify an on-chip breakpoint at line 59, which processes "p_sam->s0=a[0];", in Tutorial.c as shown in Figure 4.62. For the breakpoint setting procedure, refer to section 4.10, Break Functions.



Figure 4.62 Setting a Breakpoint

- (5) Click on the [Reset Go] button to start program execution.
 When execution is temporarily stopped at the specified breakpoint, the [Trace] window will be updated. The [Trace] window shows the track of program execution.
- (6) Click on the display mode switching button to change from the bus display mode (Figure 4.63) to the source display mode (Figure 4.64).

Range: -00000255	, 00000000 File: Cyc	:le: -00000012 Add	ress:									
Cycle	Label	Address	Source	Destina	Data	Size	R/W	BUS Mas	Type	BCN	Branch	Ch
-00000012							-	CPU	BCND	0		-
-00000011							-	CPU	BCND	0		-
-00000010							-	CPU	BCND	1		-
-00000009							-	CPU	BCND	1		-
-00000008							-	CPU	BCND	1		-
-00000007							-	CPU	BCND	1		-
-00000006							-	CPU	BCND	1		-
-00000005							-	CPU	BCND	1		-
-00000004							-	CPU	BCND	1		-
-00000003							-	CPU	BCND	1		-
-00000002							-	CPU	BCND	1		-
-00000001							-	CPU	BCND	0		-
00000000		FFFF87E4		FFFF87E			-	CPU	DESTINA			-

Figure 4.63 [Trace] Window after a Break (Bus Display Mode)



1							
he source icon	OUTCE ICON IS D0000000 File: sort.c Cycle: -00000012 Address: FFFF8740						
ed.	drea	38	Now	Source			
000053	FFFF8'	70 D	_	change (long *a)			
000054	rrrro	100		{			
000055				long tmp[10];			
000056				int i;			
000057							
000058	FFFF8'	712	-	for(i=0; i<10; i++){			
000059	FFFF8'	71E	-	tmp[i] = a[i];			
000060				}			
000061	FFFF8'		>>	for(i=0; i<10; i++){			
000062	FFFF8'	74C	-	a[i] = tmp[9 - i];			
000063				}			
000064				}			
<							

Figure 4.64 Source Display Mode

(7) The [Trace] window display modes are shown in Figure 4.65. Figure 4.65 also shows a window display example where the bus display, disassembly display, and source display modes are all enabled.

Use these trace display mode switching buttons to select the information displayed in the [Trace] window.



Figure 4.65 [Trace] Window Display Modes



(8) In the source display mode, you can use the [Step Backward] button, which analyzes the trace information and tracks the program execution history in reverse as shown in Figure 4.66, and the [Step Forward] button, which tracks forward as shown in Figure 4.67.

In this tutorial example, the current location may be in the loop in the "change" function and in some cases, the indication of the current location will not move even when a step button is clicked. In this case, check the Cycle and Address in the top right of the window. If these values change when a step button is clicked, the current location is in the loop.



Figure 4.66 Step Backward

	Q Q Q
Range: -00000254, 00000000 File: sort.c Cycle: -00000023 A	ddress: FFFF8712
Line Address Now Source 000053 FFFF870D - Change(long *a)	<u>^</u>
000054 {	
000055 Iong tmp[10]; 000056 Int i;	
000057 000058 FFFF8712 for(i=0; i<10; i++){	Step forward to the
000059 FFFF871E tmp[i] = a[i];	breakpoint.
000061 F Current location (i=0; i<10; i++)	
000062 P++++++++++++++++++++++++++++++++++++	

Figure 4.67 Step Forward



4.17 Terminating Debugging

To terminate debugging, click on the \times mark in the top right of the window and a dialog box will open. Click the [Yes] button to save the window status; you can start debugging with the saved window status at the next time.



Figure 4.68 Terminating Debugging



4.18 Operating the User Board as a Stand-Alone Unit

When operating the user board as a stand-alone unit after completing debugging, download the program by using the flash-memory programmer mode as shown in the following figure (the steps described in the above sections are omitted).

Initial Settings Device Communication MCU group: FX510 Group Device: R5F56108 Wode Device: Debugging mode Select "Writing the on-chip flash memory mode". Device: Power supply Power target from the user system. and tum on power tor the user program after ending the debugger. Power supply Power target from the enulator. (MAX 200mA) Serial No: E1: SUMD00095 DK Cancel Do not show this dialog box again.	Image: constrained intervention of the constrained
E20RX	
Program was downloaded, but debug operations do not work in this r	mode. Please disconnect target or close HEW.
OK	
Terminate the High-performance Embeddisconnect the E1 emulator from the use	

Figure 4.69 Procedure for Operating the User Board as a Stand-Alone Unit



5. Supplementary Information

5.1 Emulator-Related Documents

The E1 emulator and High-performance Embedded Workshop also have a variety of useful features other than those introduced in this guide. For details on the specifications of these products, refer to the documents given below.

- (1) High-performance Embedded Workshop User's Manual This document describes the functions of the High-performance Embedded Workshop (building, file handling, common debugging functions, and so on).
- (2) RX Family E1/E20 Emulator User's Manual This document describes procedures and usage notes that are common to the E1 and E20 emulators. For debugging functions specific to the E1 or E20, refer to E1/E20 Emulator User's Manual: Additional Document for User's Manual.
- (3) E1/E20 Emulator User's Manual: Additional Document for User's Manual (Supplementary Information on Using the RX610 Group)
 This document contains notes on usage of the E1 or E20 emulator in combination with RX610-group MCUs. A separate document is provided for each group of MCUs.
- (4) RX Family C/C++ Compiler Package User's Manual This document contains descriptions of the C/C++ compiler, assembler, and linkage editor.

To refer to these documents, open the [Start] menu and select [All Programs -> Renesas -> High-performance Embedded Workshop -> Manual Navigator]. This invokes the Manual Navigator.



Figure 5.1 Invoking the Manual Navigator





Figure 5.2 Manual Navigator Screen

Also see the following pages for other useful information such as technical updates and restrictions.

http://www.renesas.com/e1 http://www.renesas.com/hew http://www.renesas.com/rx_c On-chip debugging emulator E1 High-performance Embedded Workshop RX family C/C++ compiler package



5.2 Evaluation Version of the RX Family C/C++ Compiler Package

5.2.1 Conditions on Usage of the Evaluation-Version C Compiler

A free evaluation version of the C compiler is available so that you can evaluate the performance of the compiler before purchasing the official product. Note that the following conditions apply.

- After the evaluation-version software has been installed, there are no functional restrictions within 60 days from the day of the first building.
- From the 61st day, the maximum linkage size is limited to 128 Kbytes. The following message appears in the output window once 60 days have elapsed.

Software license problem: Duration of Trial License of UNKNOWN is exhausted. Maximum link size limited to 128KB code+data.

5.2.2 Downloading the Evaluation-Version C Compiler

The evaluation-version C compiler can be downloaded from the following Web page.

http://www.renesas.com/tool_evaluation

To evaluate the compiler for RX MCUs, download the RX family C/C++ compiler package.

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Application Notes and Sample Code	customers to evaluate prod	duct functions and performance	
Downloads			
Documentation			
Discontinued Info	and performance before purchasing the produ official product versions. For more details, ple	aluation versions of software for you to download. This uct. Trial usage period, functions, performance and rela ase refer to "Evaluation Version Software Information" bug fixes, Technical Support or notification of new vers	ated services may differ from those of " below.
	Evaluation Version Software Informatio	n	
	C/C++ Compilers and Assemblers		

Figure 5.3 Downloading the Evaluation-Version Software



5.3 Information on the E20 Emulator

The E20 emulator has broader functionality than the E1.

The large-capacity trace memory in the E20 allows tracing of up to 2-M branches or cycles. The E20 also provides various powerful debugging functions including realtime RAM monitoring, which utilizes the acquired trace data to provide reference to the contents of memory in realtime.

Er	mulator Functions	E1 (R0E000010KCE00)	E20 (R0E000200KCT00)				
Events	Address execution	8 channels					
	Operand access	4 channels in total (on the CPU bus).					
		3 channels: Address/data masking is possible.					
		1 channel: Address or range/data masking is possible.					
		The unit (1, 2, or 4 bytes) and type (R, W, R/W) of access a specifiable.					
	Event combination	OR, AND (cumulative), or sequential.					
		An 8-bit pass counter is specifiab	le for a single event.				
Break	Software break	256 channels					
	On-chip break	Events are used.					
Trace	Capacity	256 branches or cycles	Minimum of 2-M branches or cycles				
	Data type for tracing	Branch or operand access (on the CPU bus)					
	Data trace conditions	Events are used.					
	Conditions to start or stop tracing	op Occurrence of a specified event or stopping the program					
	Display of trace data	Disassembly and C source modes are available.					
		Backward stepping is also possib	le in C source mode.				
RAM monitoring	Auto-updating of the display in the [Memory] window	Possible (using a cycle-stealing transfer mode; execution stops several cycles every time one word of data is read or written)					
	Realtime RAM monitoring	Not possible	Maximum of 4 Kbytes (still under development): Data and most recent access Access by the DMAC or DTC is not detectable.				
Performance measurement		Two 32-bit counters or one 64-bit counter (to count the number of cycles that have elapsed, number of cycles of processing for interrupts or other exceptions, number of interrupts and other exceptions accepted, number of valid instructions issued, or number of event-condition matches)					

Table 5.1Differences between the E1 and E20

For details on the E20 emulator, refer to the RX Family E1/E20 Emulator User's Manual.



6. Frequently Asked Questions

(1) Why does the warning message "Toolchain 'Renesas RX Standard Toolchain', version 'x.xx.x' is missing" shown in Figure 6.1 appear after I load a workspace?

This message indicates that a compatible compiler has not been installed or the compiler version that was used to create the workspace is not present on the host computer.



Figure 6.1 Warning Message

(2) Why does the warning message "The device ID code does not match the one for the selected device name." shown in Figure 6.2 appear when I start up the debugger?

This message indicates that the device selected in the [Initial Settings] dialog box does not match the actual device on the user system connected to the E1 emulator. Select the correct device name in the [Initial Settings] dialog box.

E20RX
The device ID code does not match the one for the selected device. Please check the device name.
()

Figure 6.2 Warning Message

(3) Why does the [Confirm Firmware] dialog box shown in Figure 6.3 appear when I start up the debugger? This message indicates that the firmware version corresponding to the emulator software and the firmware version in the E1 emulator are not the same. Click on the [Yes] button regardless of the version numbers shown in the dialog box.

Confirm Firmware	
It is necessary to download the emulator firmware. Do you wish to download it now?	
Current version:	
Expected version:	
Yes No	

Figure 6.3 [Confirm Firmware] Dialog Box



(4) What is the procedure when the downloaded program is not displayed correctly after I start up the debugger?

The endian is selectable for a RX600-family MCU. If the endian selected for the MCU does not match that selected when the program was created, the debugger cannot recognize the reset vector value of the downloaded program.

To check the endian selected when the program was created, select [Build]->[RX Standard Toolchain] in the High-performance Embedded Workshop to open the [RX Standard Toolchain] dialog box and see the [CPU] page (which shows compiler settings).

To check the endian selected for the MCU, see [Endian] on the [MCU] page of the [Configuration Properties] dialog box.

X Standard Toolchain Internal flash memory overwr External flash memory Assembly Link/Library Standard Library CPU Toolcl • • Configuration MCU System Debug_RX600_E1_E20_SYSTE -<u>C</u>PU Operating mode RX60 <u>D</u>etails. Sina • Endiar Mode: • The two must Little Endian: match. None • Input clock (EXTAL): 12.5000 MHz Base register External memory area: None • ROM Area Endian BUS Width Tutorial - High-performance Embedded Workshop - [Tutorial.c] File Edit View Project Build Debug Setup Tools Test Window 🗅 🥔 🗟 💋 8 👻 891 比 Build File Chrl+E7 16 10 8 2 1 7 F 👯 Build F7 🛗 Build All 6. > Tutorial Build Multiple.. 🔄 C source file Clean Current Project > Work <u>R</u>AM start address (0x400 3000 bytes used): 불 dbsct.c 불 intprg.c ≌ resetprg 🛗 Clean All Projects Update All Dependencie: : Ti Cancel ОK Compiler settings MCU settings It show this dialog box ag

If the selections do not match, modify either of them.

Figure 6.4 Checking That the Endians Match

(5) What is the procedure when I try to execute a debugging command but it is not accepted? Check whether you have selected the flash programmer mode in the [Initial Settings] dialog box. If the message shown below is displayed in the output window, the emulator is operating in the flash programmer mode. To execute a debugging command, select the debugging mode instead.



Figure 6.5 Display in the Output Window



(6) Since I installed the E1/E20 emulator software V.1.00 Release 00 on a host computer running Windows[®] XP, no other emulators are connectable to the host computer. How do I connect another emulator?

Follow the procedure below to check the operating state of USB driver.

Connect the emulator to the host computer and supply power to the emulator.

Start up the Windows' Device Manager to see the list of devices.

Open the properties for the Renesas' USB driver and click on the [General] tab.

If the "The drivers for this device need to be reinstalled." message is displayed in the [Device status] section, take the following steps to reinstall the USB driver.

- 1 Right-click on [Renesas E-Series USB Driver] under the [Renesas Emulator] category in the Device Manager and select [Delete] from the menu.
- 2 Turn off power to the emulator.*
- 3 Turn on power to the emulator.*
- 4 [Found New Hardware Wizard] automatically starts. Select [Install the software automatically (Recommended)] and click on the [Next] button.
- 5 [Found New Hardware Wizard] shows the "Please select the best match for your hardware from the list below." message.
- 6 When two or more inf files are displayed, select one and click on the [Next] button.
- 7 Check that [Found New Hardware Wizard] shows the "The wizard has finished installing the software for: Renesas E-Series USB Driver" message. Click on the [Finish] button.
- Note: Turning power to the emulator on and off

Most on-chip emulators can only be supplied with power while they are connected to the host computer via a USB interface cable. To turn power to such emulators on or off, connect or disconnect the USB interface cable.

This note applies to the emulators given below. That is, the only exception is the E8a.

- E10A-USB
- E1
- E20
- E8
- E7
- E10T-USB
- E100
- E30A
- E200F
- (7) Other Questions

The most common questions and answers about Renesas products, including the E1, are given as "FAQs" on the Renesas Web site. Enter the E1 emulator page and click on "FAQs".



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Renesas Electronics' Homepage http://www.renesas.com/

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Revision History

		Description		
Rev.	Date	Page	Summary	
1.00	Sep. 07, 2010	_	First edition issued	

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Renease Electronics America Inc. 2880 Sport Boulevard Santa Clara, CA 96050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130 Renease Electronics Canada Limited 1011 Nichoton Road, Newmarket, Ontano L3Y 903, Canada Tel: +1-408-898-5441, Fax: +1-905-598-3220 Renease Electronics Europe Limited Duke Meadow: Milboard Road, Bourne End, Buokinghamshire, SL8 5FH, U.K Tel: +44-1628-585-100, Fax: +44-1628-586-900 Renease Electronics Europe GmbH Anadiastrasse 10, 40472 Dusseldorf, Germany 11: +49-211-65030, Fax: +49-211-5503-1327 Renease Electronics (China) Co., Ltd. 7th Floor, Duantum Plaza, No 27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +49-21-550, No 27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +49-21-550, No 27 ZhiChunLu Haidian District, Beijing 100083, P.R.China Tel: +49-10-8236-1155, Fax: +86-10-8235-7755 / -7893 Renease Electronics (Shang) Co., Ltd. Unit 204, 205, A2IA Center, No 1233 Luiazui Ping Rd. Pudong District, Shanghai 200120, China Tel: +4862-24577-1518, Fax: +86-21-6507-7858 / -7893 Renease Electronics Hong Kong Limited Unit 1961-1613, 16FF, Towor 2, Grand Contury Plaze, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +4562-2456-9316, Fax: -4562-2086-0239044 Renease Electronics Taiwan Co., Ltd. 7F, No. 368 Ushing North Road Taipei, Taiwan Tel: +4862-24175-9600, Fax: +486-24175-9670 Renease Electronics Rong 2, Grand Contury Plaze, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +65-6219-0200, Fax: +486-24175-9670 Renease Electronics Kong 2, Grand Contury Plaze, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +65-6219-0200, Fax: +486-24175-9670 Renease Electronics Kong 2, Chel 11: Abourfront Avenue, #06-10, koppel Bay Tower, Singapore 096832 Tel: +65-6219-0200, Fax: +486-24175-9670 Renease Electronics Korea Co., Ltd. 11: Somitk Luwid or Bidlig, 720-2 Yeokisam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: +65-6219-5990, Fax: +85-2458-5110

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