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M16C/64 Group

Buzzer output

1. Abstract

The timer mode is used to make the buzzer ring.

Use the following peripheral function:

- The pulse-outputting function in timer mode of timer A.

2. Introduction

This application note is applied to the M16C/64 group microcomputers.

This program can be operated under the condition of M16C family products with the same SFR (Special Function Register) as M16C/64 Group products. Because some functions may be modified of the M16C family products, see the user's manual. When using the functions shown in this application note, evaluate them carefully for an operation.

3. Specification

(1) Sound a 2-kHz buzz beep by use of timer A0.

(2) Effect pull-up in the relevant port by use of a pull-up resistor. When the buzzer is off, set the port high-impedance, and stabilize the potential resulting from pulling up.

(3) Connect a 16-MHz oscillator to XIN.

(4) Using POFS0 bit in TAPOFS register, select the output polarity of the TA0OUT pin.

4. Operation

(1) The MCU begins performing a count on timer A0. Timer A0 has disabled interrupts.

(2) The MCU begins pulse output by setting the pulse output function select bit to “Pulse output effected”. P70 changes into TA0OUT pin and outputs 2-kHz pulses.

(3) The MCU stops outputting pulses by setting the pulse output function select bit to “Pulse output not effected”. P70 goes to an input pin, and the output from the pin becomes high-impedance.

Figure 1 shows the operation timing of buzzer output.

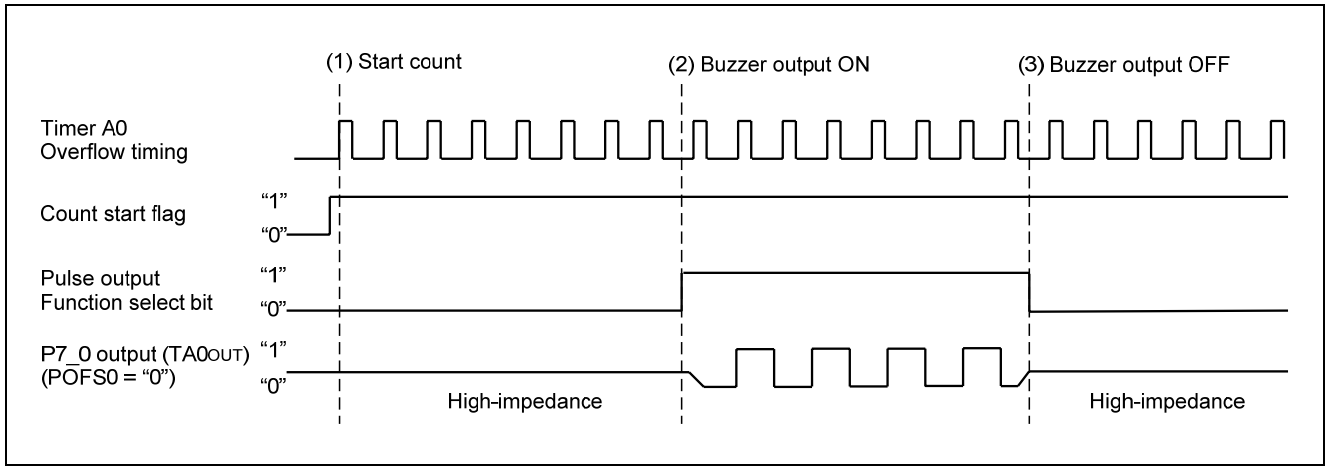


Figure 1. Operation timing of buzzer output

5. Set-up procedure

Table 1 shows Timer A count source, Figure 2 shows block diagram of Timer A count source in timer mode.

Table 1. Count Source Selection of Timer A

TACSj register (Note 1)				TAiMR register		Count source	Count source period
TCS3/ TCS7	TCS2/ TCS6	TCS1/ TCS5	TCS0/ TCS4	TCK1	TCK0		f(PLL):24MHz f(XCIN):32.768kHz
0	-	-	-	0	0	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
0	-	-	-	0	1	f8TIMAB	333.3ns
0	-	-	-	1	0	f32TIMAB	1333.3ns
0	-	-	-	1	1	fc32	976.56µs
1	0	0	0	-	-	f1TIMAB/f2TIMAB (Note 2)	41.7ns or 83.3ns
1	0	0	1	-	-	f8TIMAB	333.3ns
1	0	1	0	-	-	f32TIMAB	1333.3ns
1	0	1	1	-	-	f64TIMAB	2666.7ns
1	1	0	1	-	-	foco-s	About 8µs
1	1	1	0	-	-	fc32	976.56µs

Note 1: TCS3~TCS0 bits of TACS0 register correspond to Timer A0 count source selection, TCS7~TCS4 bits of TACS0 register correspond to Timer A1 count source selection, TCS3~TCS0 bits of TACS1 register correspond to Timer A2 count source selection, TCS7~TCS4 bits of TACS1 register correspond to Timer A3 count source selection, and TCS3~TCS0 bits of TACS2 register correspond to Timer A4 count source selection.
Note 2: When the PCLK0 bit in the PCLKR register is "1", the selected clock source is f1TIMAB. When the PCLK0 bit is "0", the selected clock source is f2TIMAB.

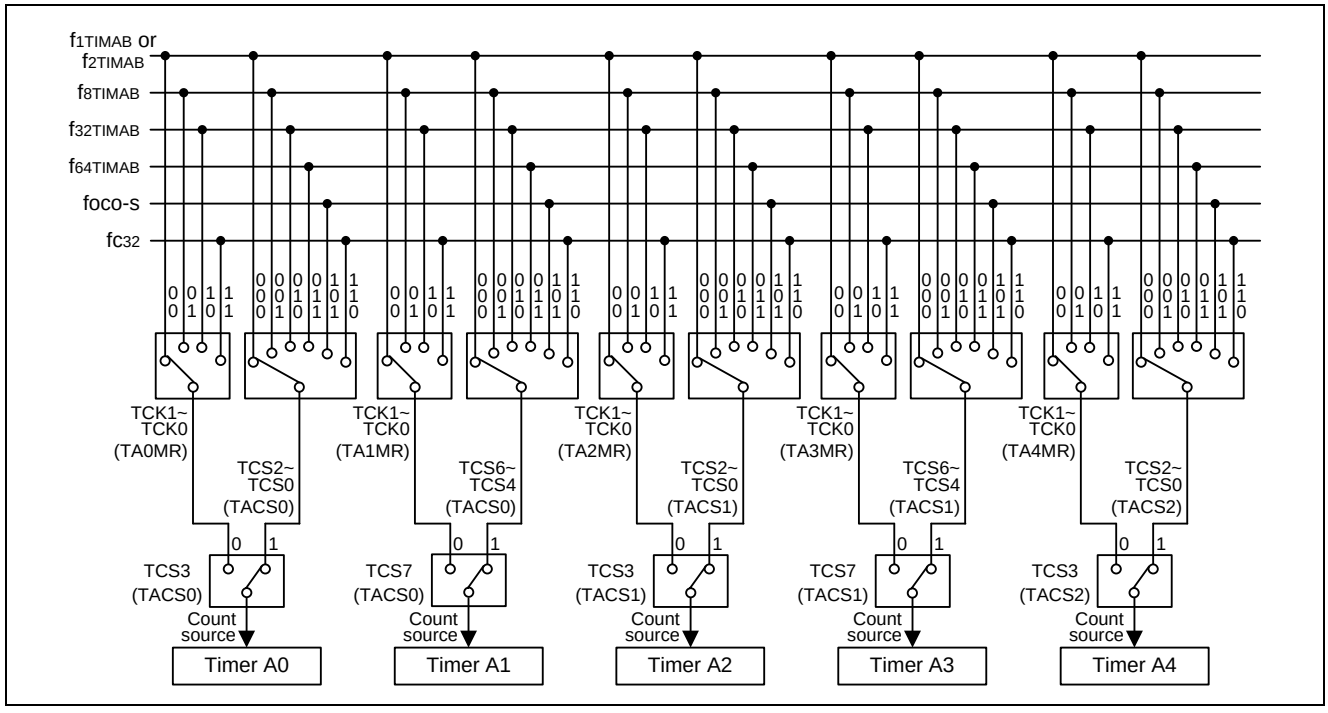
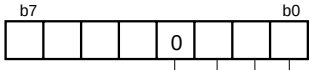


Figure 2. Count source of Timer A

Selecting timer count source



Timer A count source select register 0 [Address 01D0h] TACS0

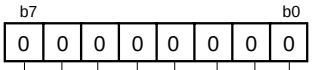
TA0 count source select bit (Note1)

b2 b1 b0
0 0 0 : f_{1TIMAB} or f_{2TIMAB} (Note2)
0 0 1 : f_{8TIMAB}
0 1 0 : $f_{32TIMAB}$
0 1 1 : $f_{64TIMSB}$
1 0 0 : Do not set
1 0 1 : foco-s
1 1 0 : fc_{32}
1 1 1 : Do not set

TA0 count source option specified bit (Note1)
0 : TCK0, TCK1 enabled, TCS0 to TCS2 disabled

Note 1: About the count source period, please refer to Table 2.
Note 2: When the PCLK0 bit in the PCLKR register is “1”, the selected clock source is f_{1TIMAB}. When the PCLK0 bit is “0”, the selected clock source is f_{2TIMAB}.

Initialization of timer A0



Timer A0 mode register [Address 0336h]
TA0MR

Selection of timer mode

Pulse output function select bit
0 : Pulse is not output ($TA0_{OUT}$ pin is a normal port pin)

Gate function select bit

b4 b3
0 0 :
0 1 : } Gate function not available (TAi_{IN} pin is a normal port pin)

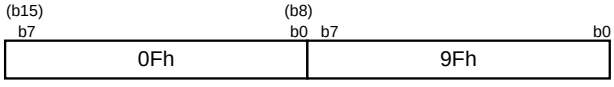
0 (Must always be “0” in timer mode)

Count source select bit (Note1)

b7 b6
0 0 : f_{1TIMAB} or f_{2TIMAB} (Note2)
0 1 : f_{8TIMAB}
1 0 : $f_{32TIMAB}$
1 1 : fc_{32}

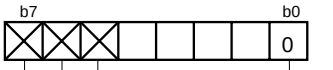
Note 1: Valid when the TCS3 bit or TCS7 bit in registers TACS0 to TACS2 is set to 0 (TCK0, TCK1 enabled). About the count source period, please refer to Table 2.
Note 2: When the PCLK0 bit in the PCLKR register is “1”, the selected clock source is f_{1TIMAB}. When the PCLK0 bit is “0”, the selected clock source is f_{2TIMAB}.

Setting counter value



Timer A0 register [Address 0327h, 0326h]
TA0

Selecting waveform output function

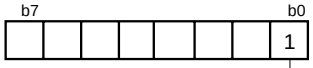


Timer A waveform output function select register [Address 01D5h]
TAPOFS

TA0OUT output polar control bit
0 : Output waveform "H" active

No register bits. If necessary, set to 0. Read as undefined value

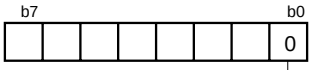
Setting count start flag



Count start flag [Address 0320h]
TABSR

Timer A0 count start flag
1 : Start counting

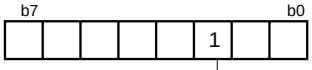
Initialization of port P7 direction register



Port P7 direction register [Address 03EFh]
PD7

Port P70 direction register
0 : Input mode

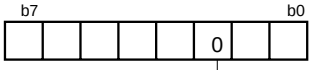
Buzzer ON



Timer A0 mode register [Address 0336h]
TA0MR

Pulse output function select bit
1 : Pulse is output (Port P7_0 is TA0OUT output pin)

Buzzer OFF



Timer A0 mode register [Address 0336h]
TA0MR

Pulse output function select bit
0 : Pulse is not output

6. Reference

Hardware manual

M16C/64 Group Hardware Manual

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