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## PCB Footprint Landing Pattern Recommendations

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This document describes the PCB footprint landing pattern recommendations for Renesas Electronics device packages.

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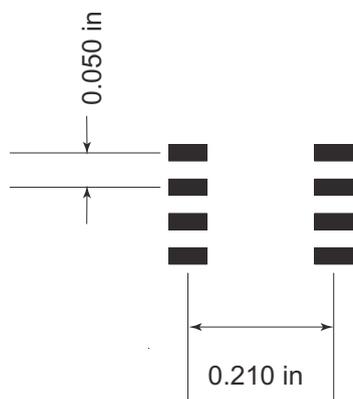
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# 1. 8-Lead, 150-mil Narrow Body, Plastic Gull Wing Small Outline (JEDEC SOIC)

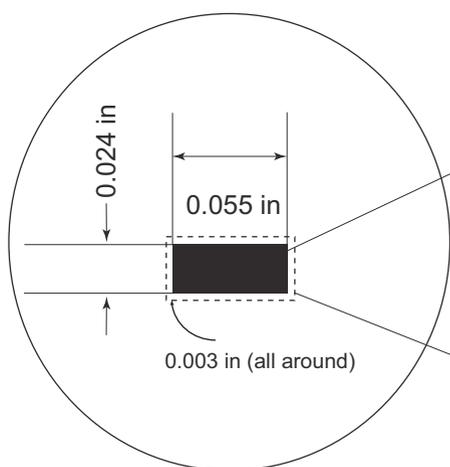
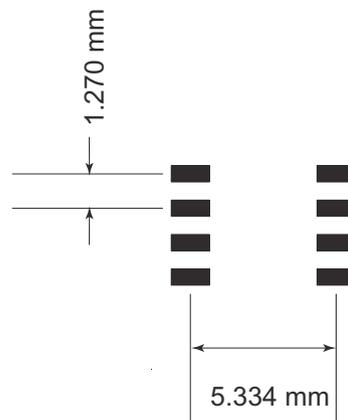
JEDEC SOIC, SOIC 8  
8-lead 0.150" narrow body

## LANDING PATTERN

(Dimensions in inches)

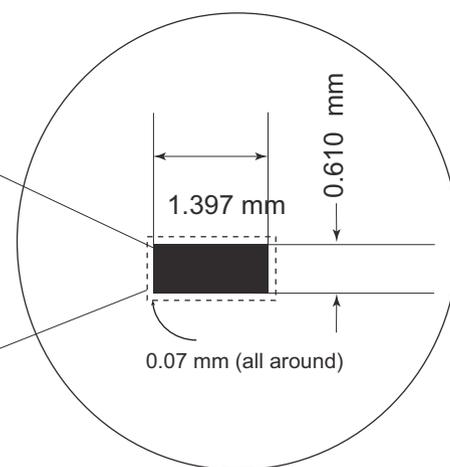


(Dimensions in millimeters)



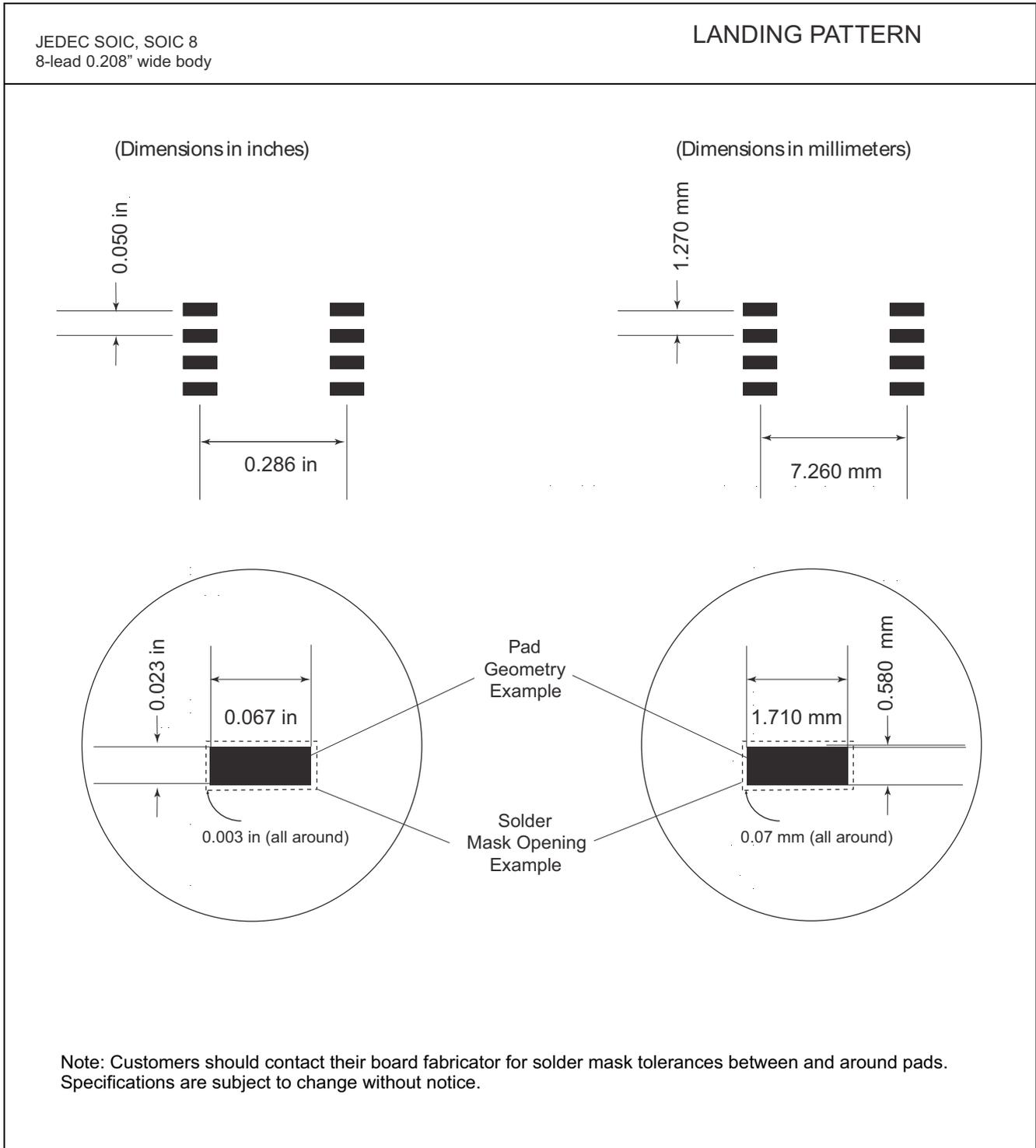
Pad  
Geometry  
Example

Solder  
Mask Opening  
Example

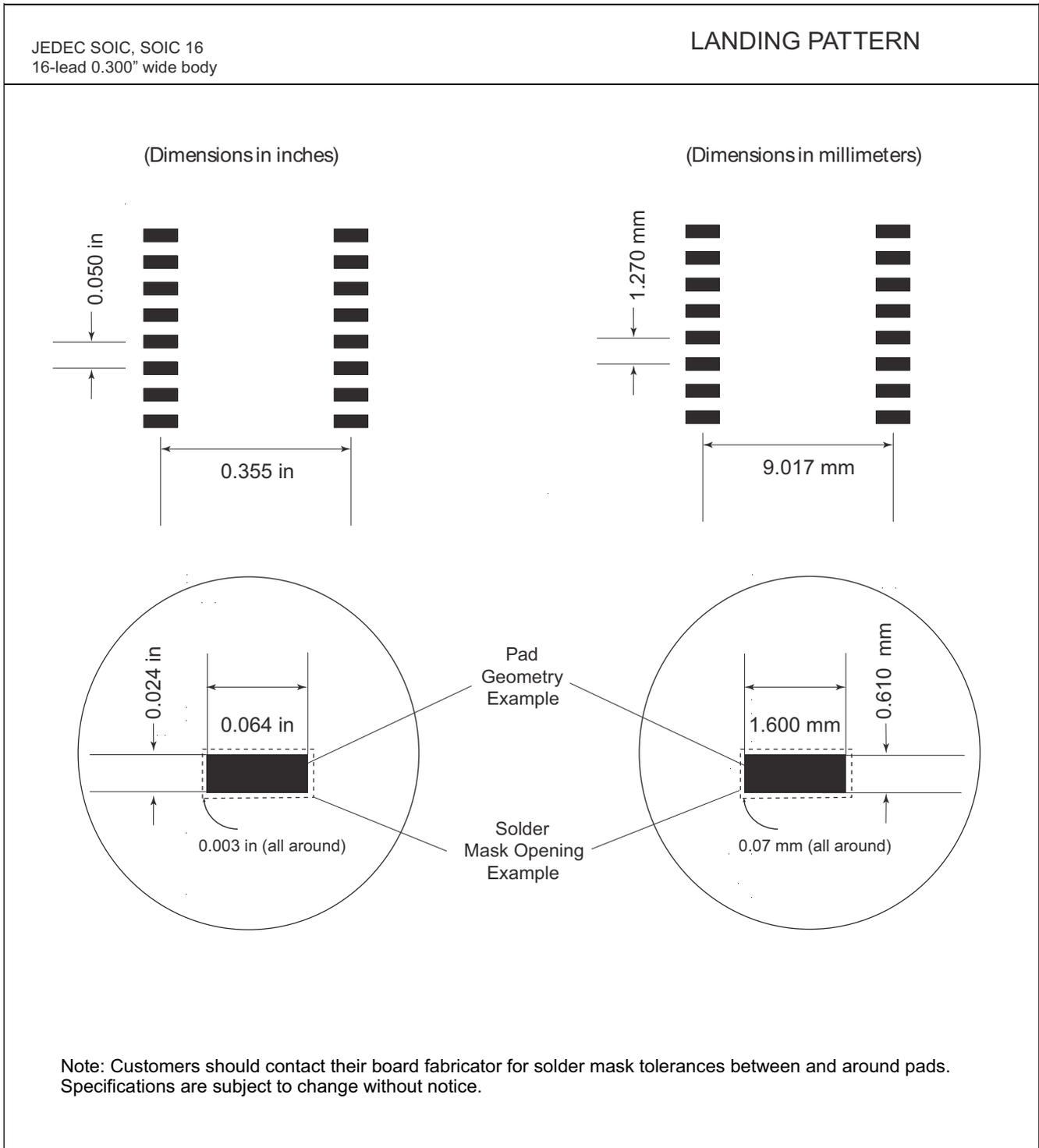


Note: Customers should contact their board fabricator for solder mask tolerances between and around pads. Specifications are subject to change without notice.

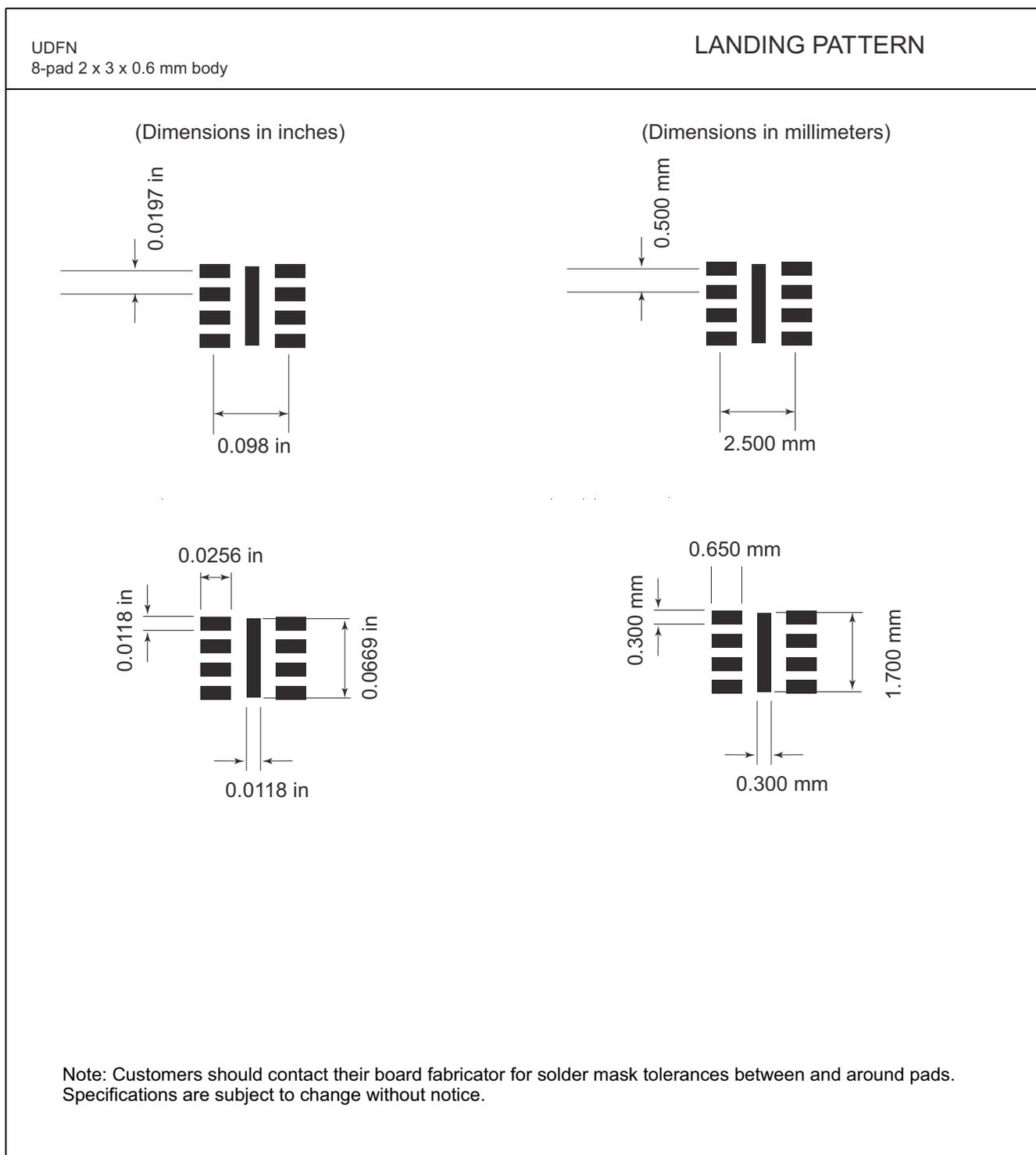
## 2. 8-Lead, 208-mil Wide Body, Plastic Gull Wing Small Outline (JEDEC SOIC)



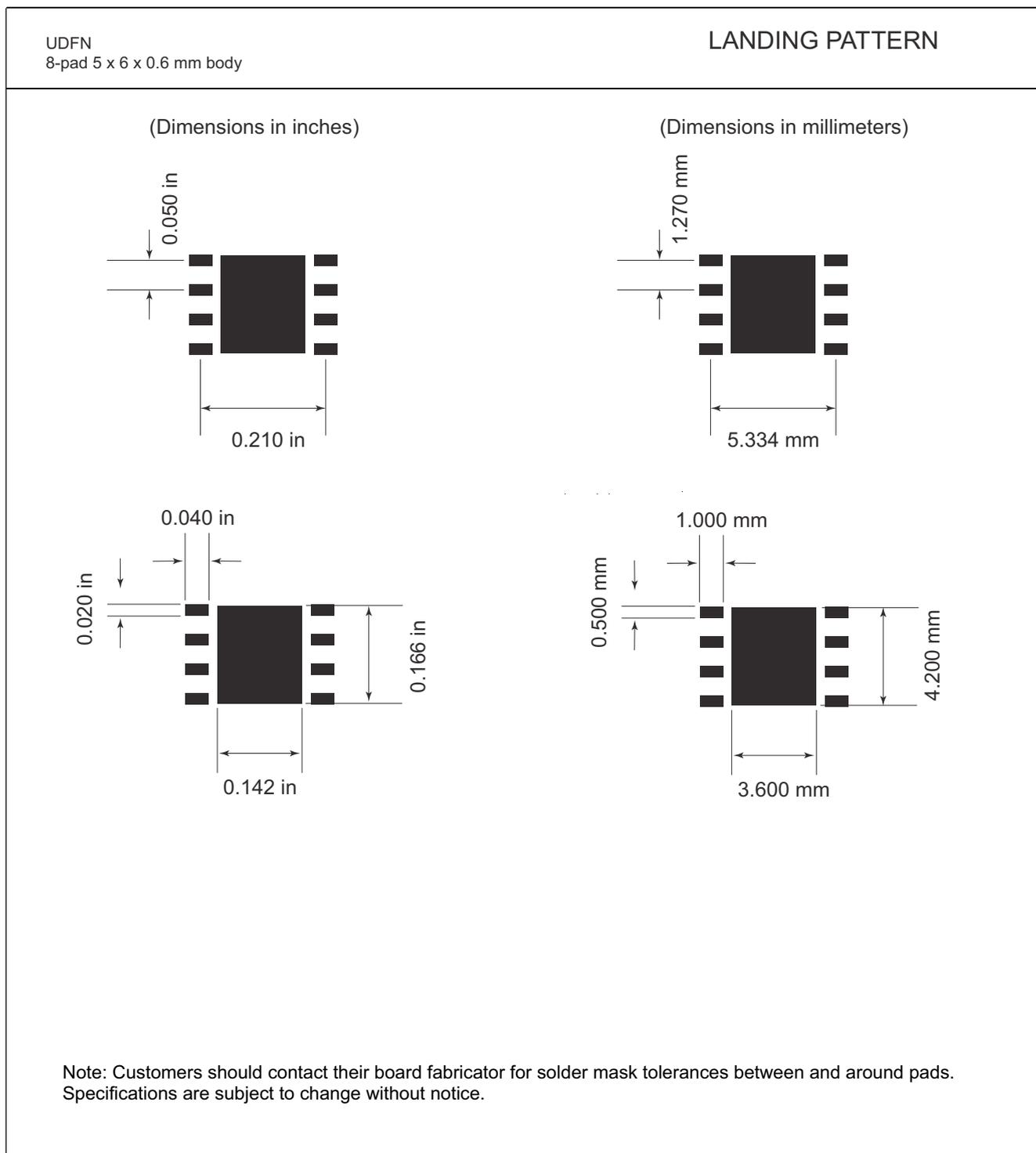
### 3. 16-Lead, 0.300-mil Wide Body



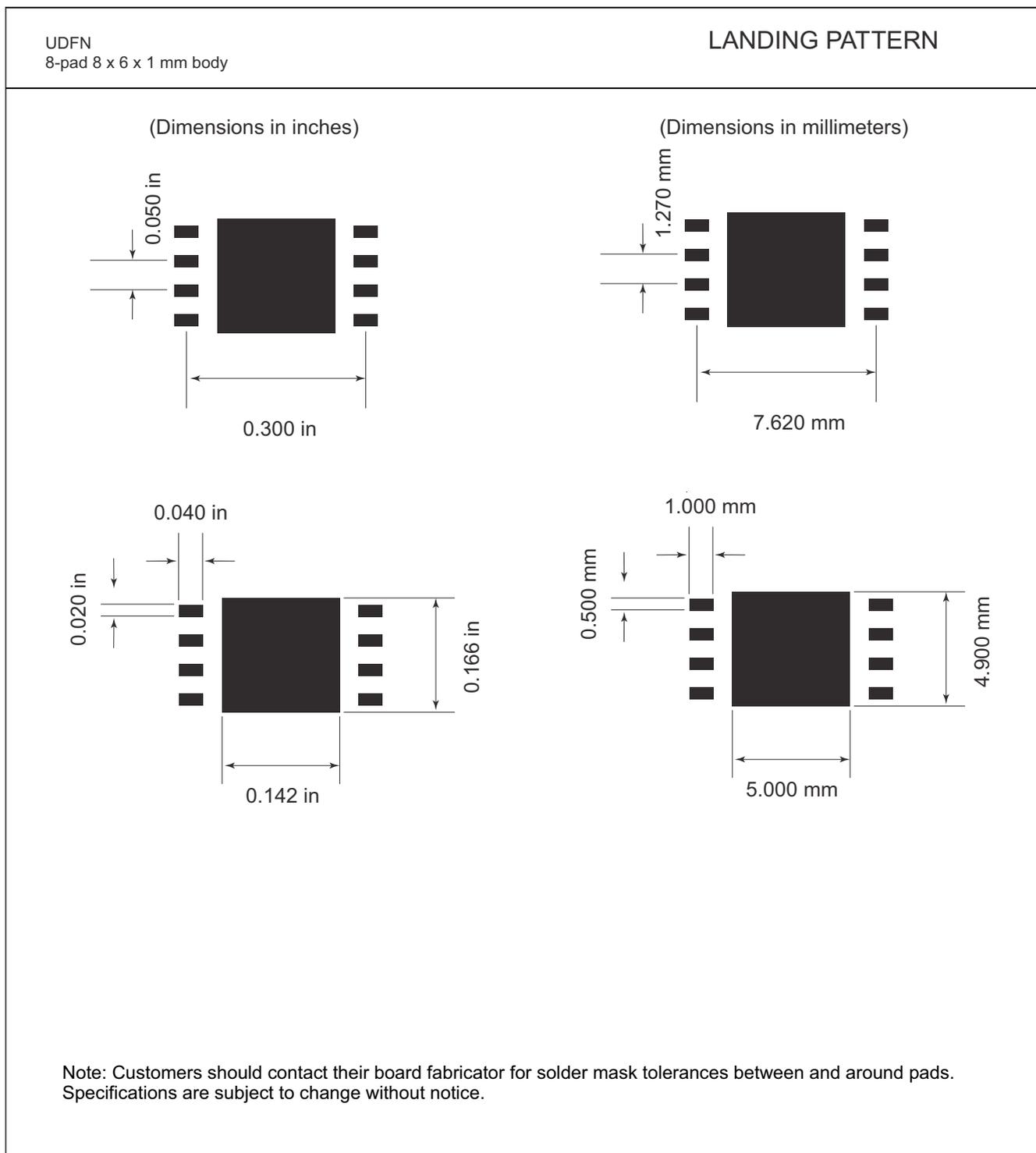
## 4. 8-Pad 2 x 3 x 0.6 mm UDFN



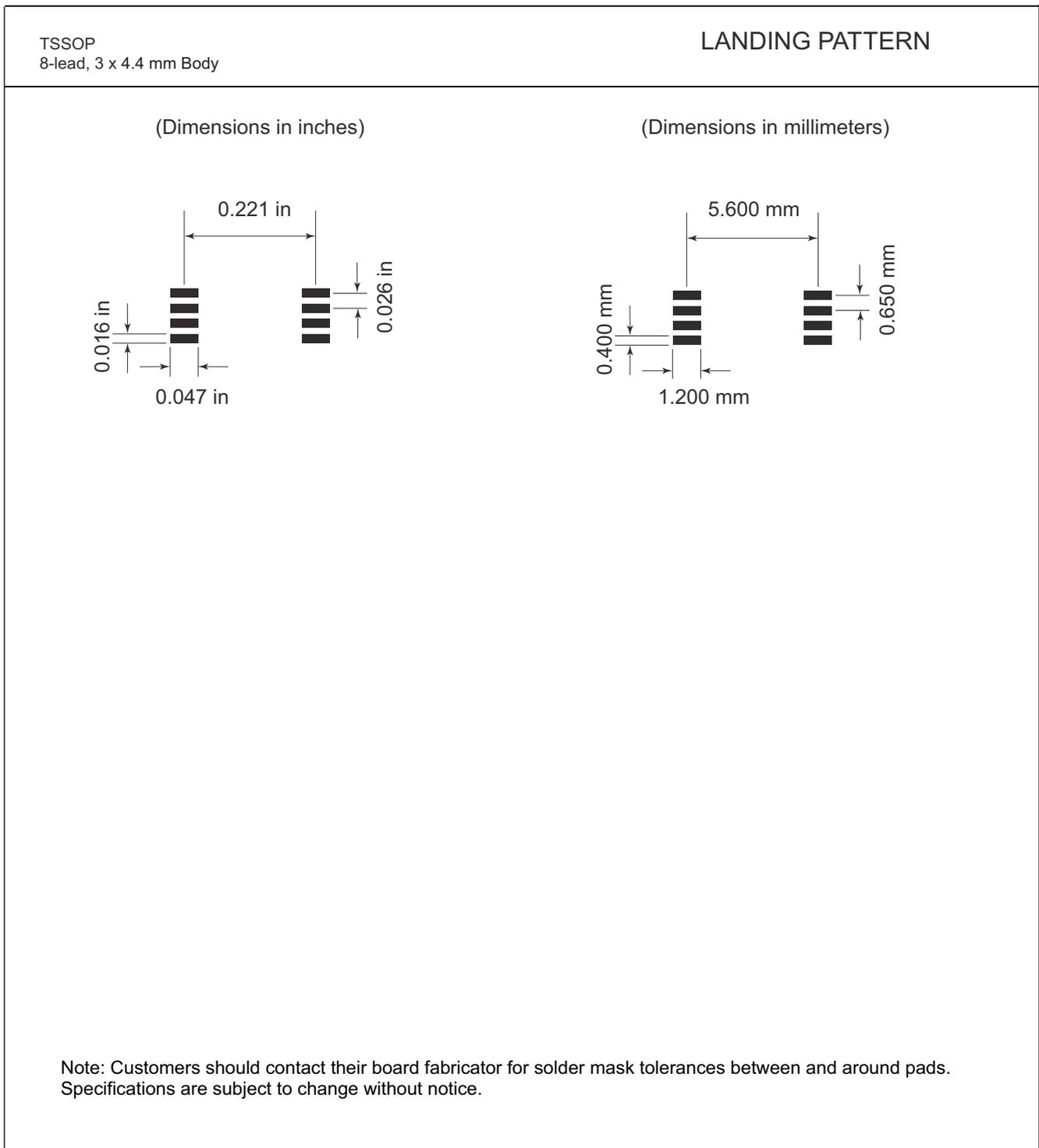
## 5. 8-Pad 5 x 6 x 0.6 mm UDFN



## 6. 8-Pad 8 x 6 x 1 mm UDFN



## 7. 8-Lead 3 x 4.4 mm TSSOP

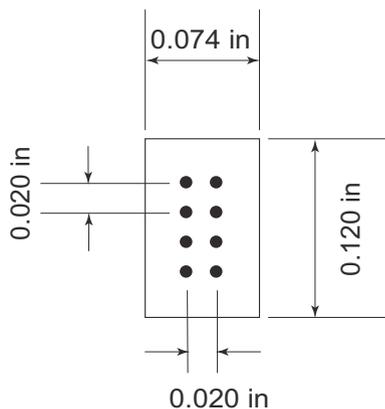


## 8. 8-Ball (4 x 2 Array), 0.50 mm Pitch 3.038 x 1.887 x 0.409 mm WLCSP

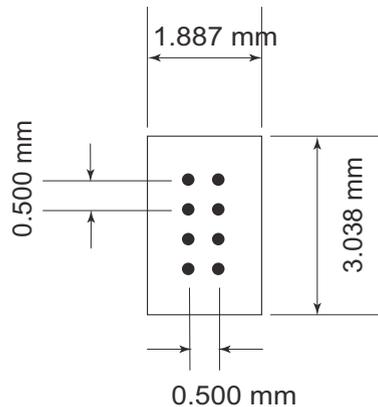
WLCSP  
8-ball (4 x 2 Array), 0.50 mm Pitch, 3.038 x 1.887 x 0.409 mm body

### LANDING PATTERN

(Dimensions in inches)



(Dimensions in millimeters)



Note: Customers should contact their board fabricator for solder mask tolerances between and around pads. Specifications are subject to change without notice.

## 9. Revision History

Revision	Date	Description
A0	June, 2023	Initial release.

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