

Auto Sync Separator Circuit for EL1883 in HDTV application

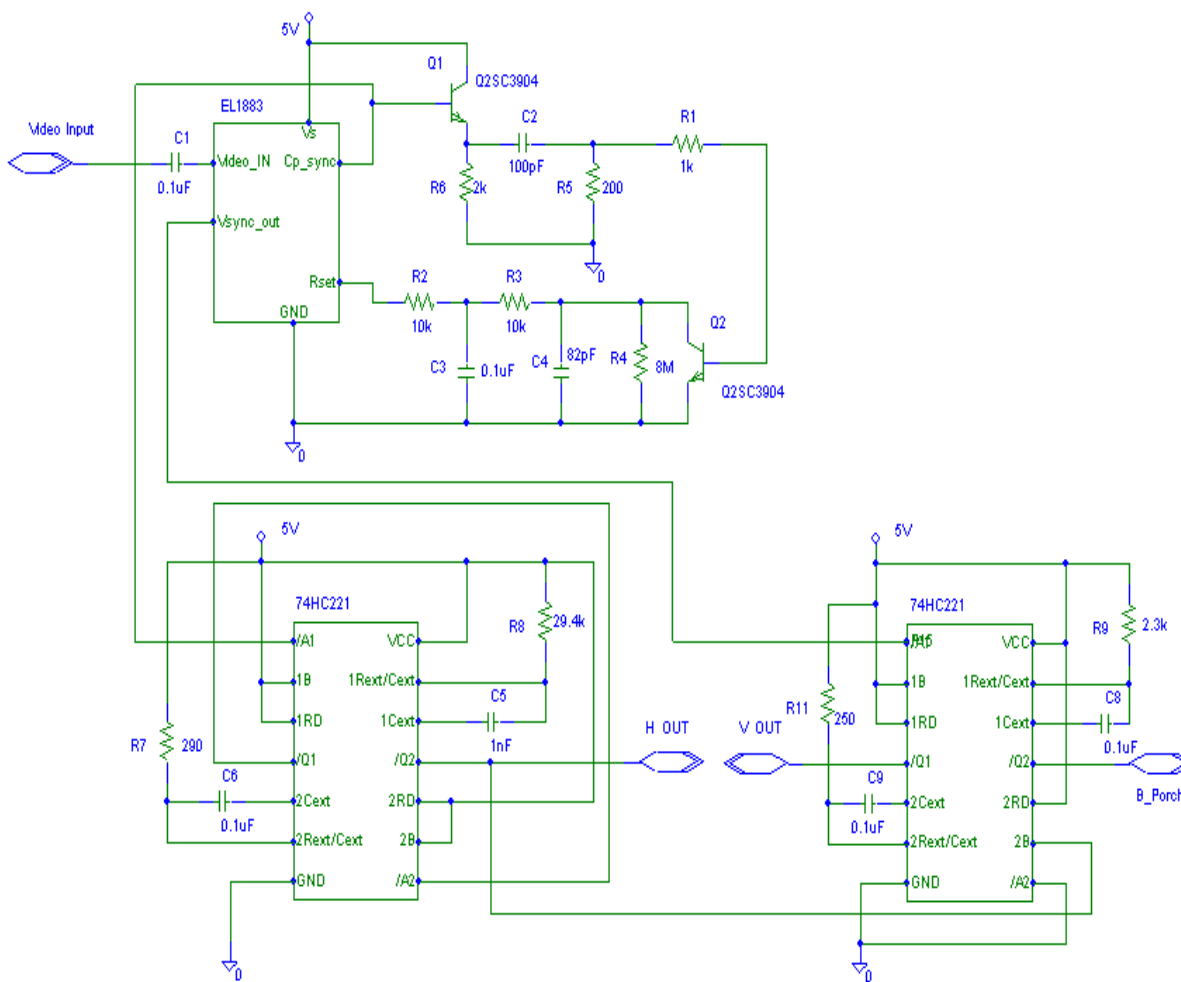
**Introduction**

Originally, the EL1883 is designed to use in the NTSC, PAL and SECAM (Bi-level sync) applications. But with some external circuitries, it can also be used to separate most of the HDTV (Tri-level sync) signals automatically.

**Circuit Description**

Normally, the EL1883 uses a resistor  $R_{SET}$  to control the internal timing. It is a current source and the current is set by the value of  $R_{SET}$ . The resistor  $R_{SET}$  needs to be changed

for different line rate. With the following frequency to current conversion circuit, the EL1883 can detect most of the HDTV video signals automatically without changing  $R_{SET}$ . The composite sync is passed through an emitter follower Q1. The differentiator C2 and R5 generates a positive going voltage spike at the rise edge of the composite sync pulse. The voltage spike turns on Q2 and discharge C4. R1 is used to reduce the miller capacitor coupling effect on C4. R2, R3, C3 and C4 form a charge tank.



As the line rate increases, the discharge rate also increases. The average current sources out from R<sub>SET</sub> pin increases. R4 is a start up resistor.

Using the 74HC221, a dual non-retriggerable monostable multivibrator, can generate a true horizontal line, back porch pulse and vertical line.

### Horizontal Sync and Back porch generator

The composite sync is input to /A1 pin of the 74HC221. R8 and C5 set the pulse width at about 21μs to eliminate the half line pulse for the composite sync. The output of /Q1 is connected to /A2. R7 and C6 set the pulse width of the output horizontal sync to about 4μs. The Back porch pulse can be used for DC restored application and generated from the Horizontal Sync

### Performance Curves

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

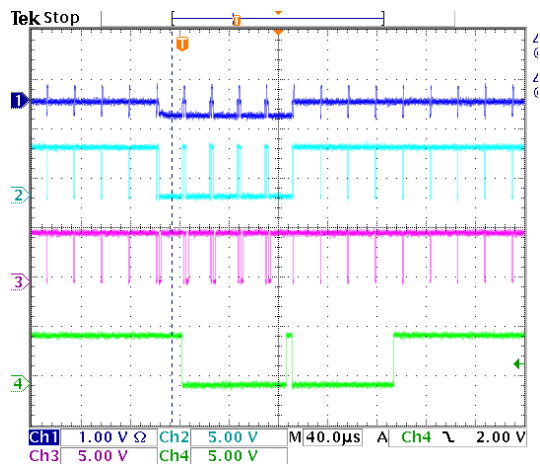


FIGURE 1. 720P50---OUTPUT FROM EL1883

output. R11 and C9 set the pulse width to be about 5μs for the Back Porch output.

### Vertical Sync generator

The Vertical Sync from EL1883 is connected to /A1 of the 74HC221. R9 and C8 set the output pulse width to about 180μs to eliminate the double pulse from the EL1883's Vertical Sync. The vertical pulse should have only the leading negative going edge used for timing as the trailing edge is a timer pulse and not hard synced to the video.

With the above circuit, it works for the following HDTV signals: 720P50~60, 1035I29~30, 1080I23~30 and 1080P23~30. It doesn't work for 1080P50~60.

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

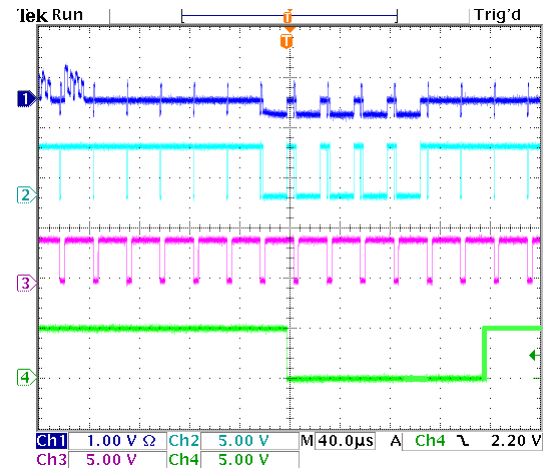


FIGURE 2. 720P50---OUTPUT AFTER THE 74HC221

Performance Curves (Continued)

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

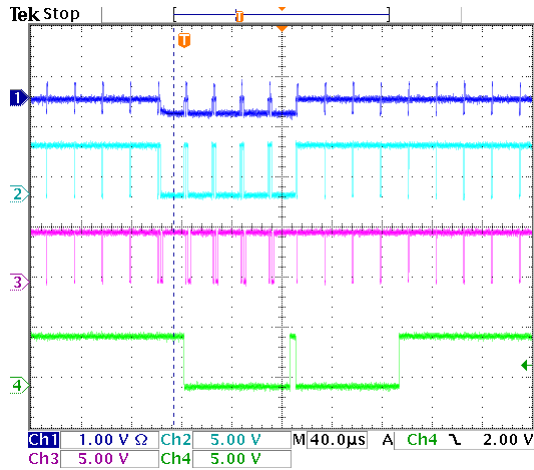


FIGURE 3. 720P59 AND 720P60---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

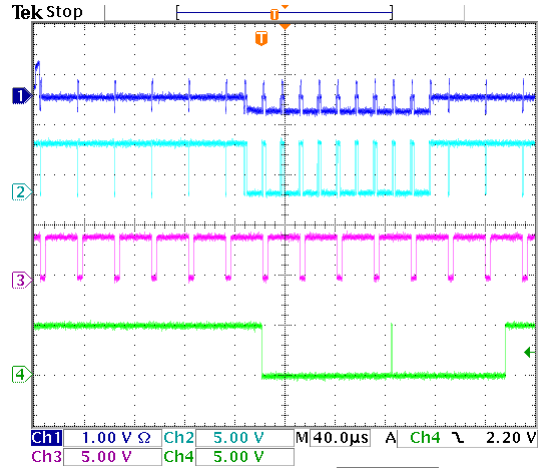


FIGURE 4. 1035I29---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

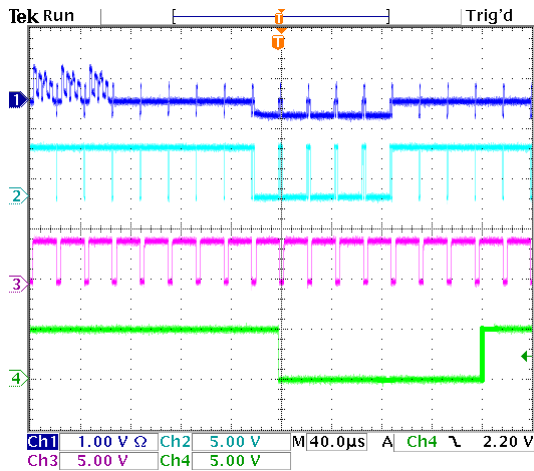


FIGURE 5. 720P59 AND 720P60---OUTPUT AFTER THE  
74HC221

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

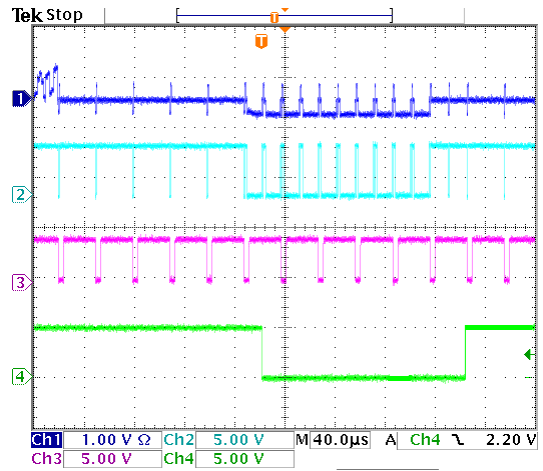


FIGURE 6. 1035I29---OUTPUT AFTER THE 74HC221

Performance Curves (Continued)

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

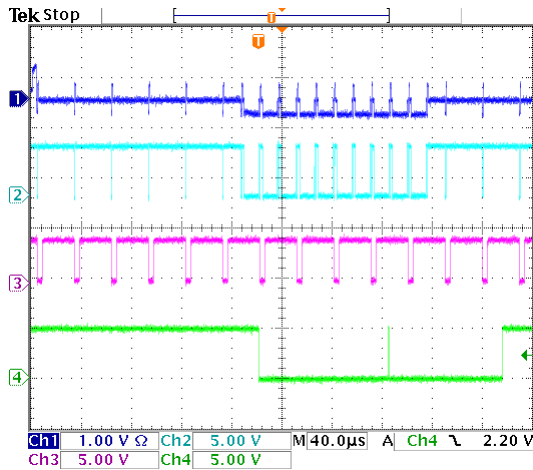


FIGURE 7. 1035I30---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

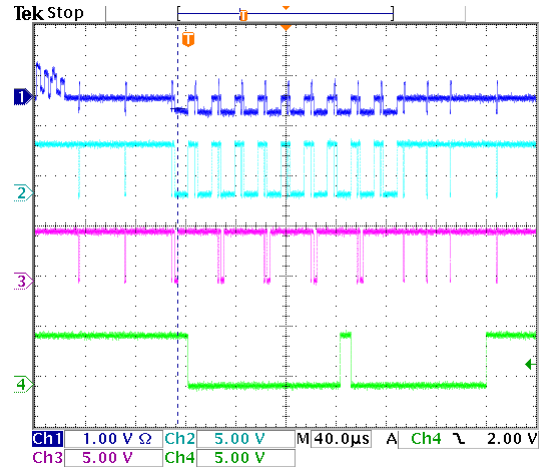


FIGURE 8. 1080I23 AND 1080I24---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

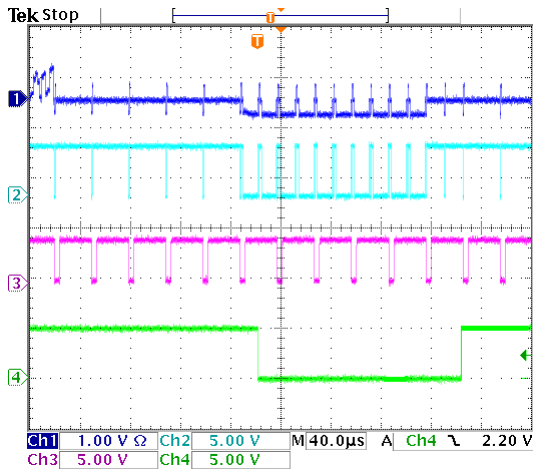


FIGURE 9. 1035I30---OUTPUT AFTER THE 74HC221

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

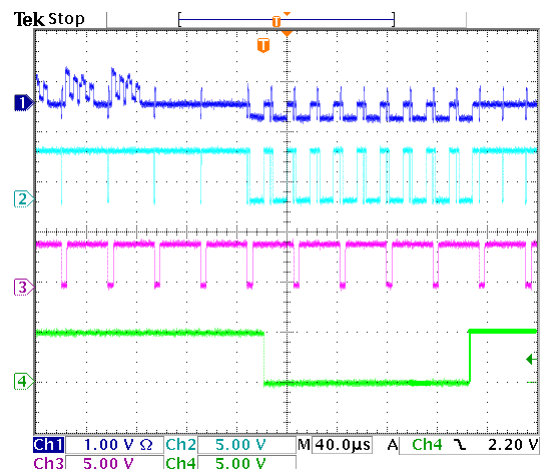


FIGURE 10. 1080I23 AND 1080I24---OUTPUT AFTER THE 74HC221

Performance Curves (Continued)

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

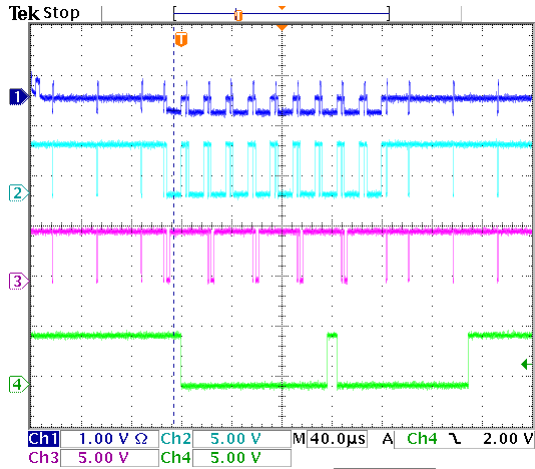


FIGURE 11. 1080I25 H28, V50---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

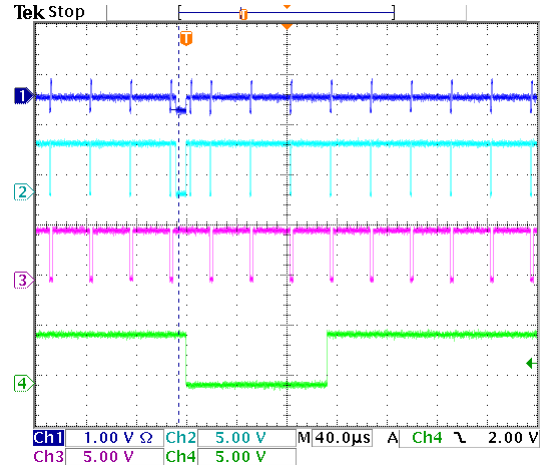


FIGURE 12. 1080I25 H31, V50---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

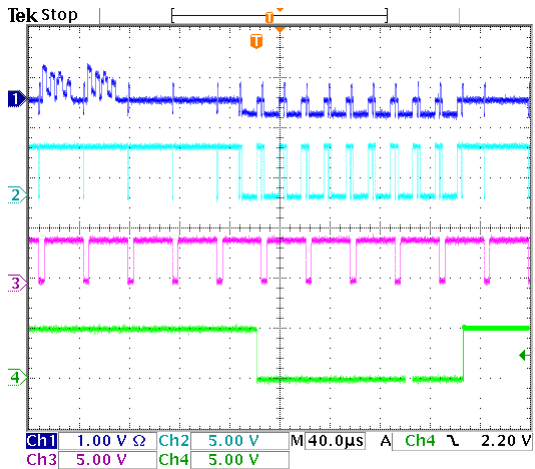


FIGURE 13. 1080I25 H28, V50---OUTPUT FROM 74HC221

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

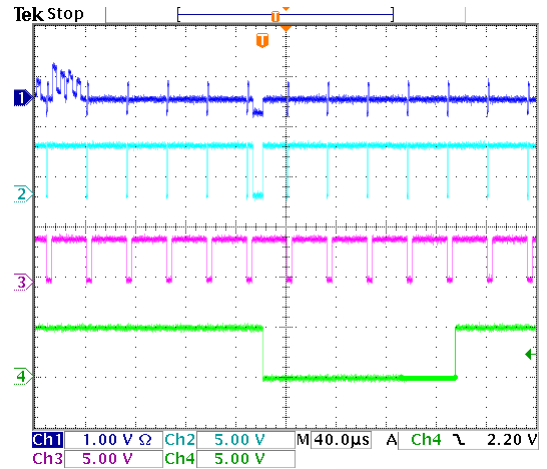


FIGURE 14. 1080I25 H31, V50---OUTPUT FROM 74HC221

Performance Curves (Continued)

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

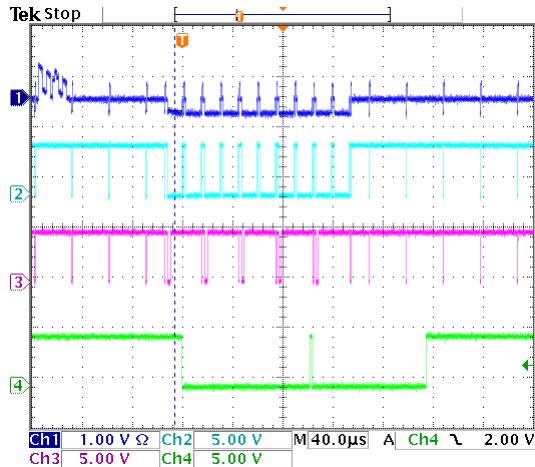


FIGURE 15. 1080I29 AND 1080I30---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

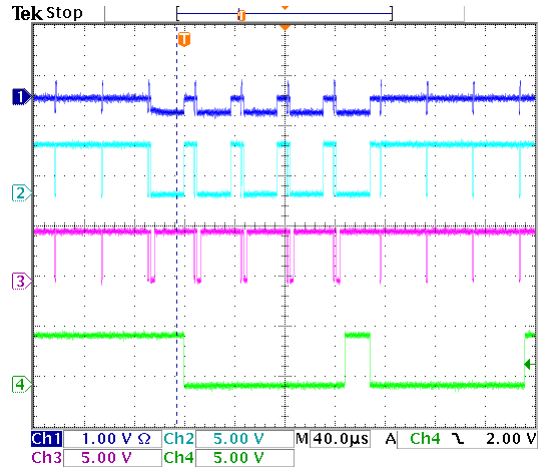


FIGURE 16. 1080P23 AND 1080P24---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

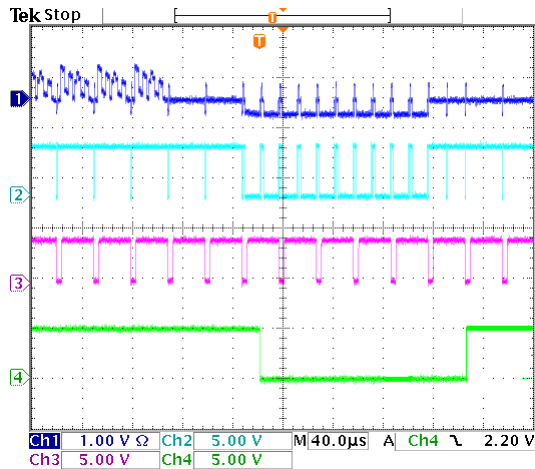


FIGURE 17. 1080I29 AND 1080I30---OUTPUT FROM 74HC221

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

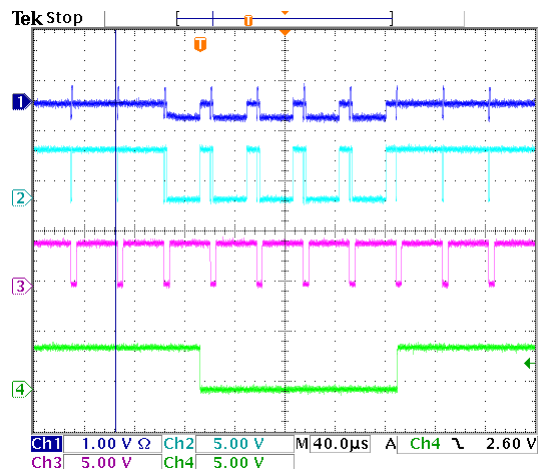


FIGURE 18. 1080P23 AND 1080P24---OUTPUT FROM 74HC221

Performance Curves (Continued)

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

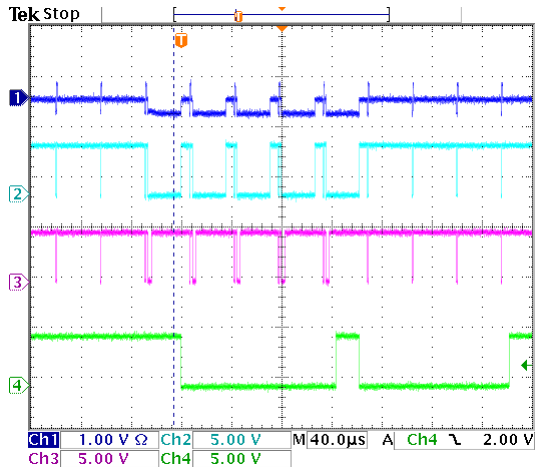


FIGURE 19. 1080P25---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

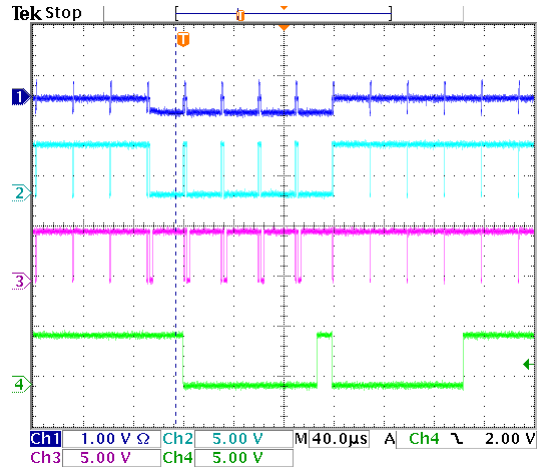


FIGURE 20. 1080P29 AND 1080P30---OUTPUT FROM EL1883

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

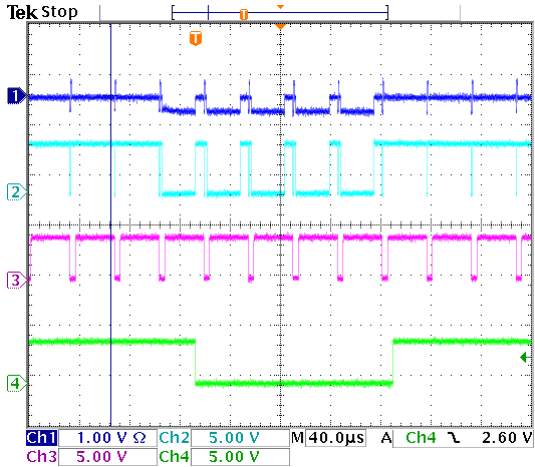


FIGURE 21. 1080P25 ---OUTPUT FROM 74HC221

CH1---Composite Video input  
CH2---Composite Video output  
CH3---Horizontal Sync output  
CH4---Vertical Sync output

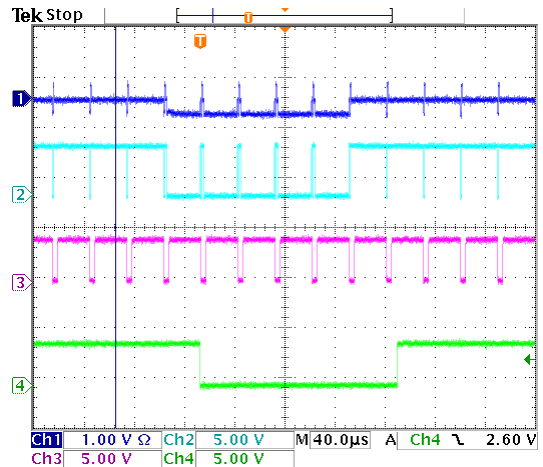


FIGURE 22. 1080P29 AND 1080P30---OUTPUT FROM 74HC221

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Tel: +1-408-432-8888, Fax: +1-408-434-5351

**Renesas Electronics Canada Limited**  
9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3  
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**Renesas Electronics Europe Limited**  
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K  
Tel: +44-1628-651-700, Fax: +44-1628-651-804

**Renesas Electronics Europe GmbH**  
Arcadiastrasse 10, 40472 Düsseldorf, Germany  
Tel: +49-211-6503-0, Fax: +49-211-6503-1327

**Renesas Electronics (China) Co., Ltd.**  
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Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

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Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China  
Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

**Renesas Electronics Hong Kong Limited**  
Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong  
Tel: +852-2265-6688, Fax: +852-2886-9022

**Renesas Electronics Taiwan Co., Ltd.**  
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Tel: +886-2-8175-9600, Fax: +886-2-8175-9670

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Tel: +91-80-67208700, Fax: +91-80-67208777

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