Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.



Qzrom Programming Confirmation Form SINGLE-CHIP 8-BIT MICROCOMPUTER M37544G2A-XXXSP/GP RENESAS TECHNOLOGY

KO	ivi Humbei	
Receipt	Date:	
	Section head signature	Supervisor signature

DOM number

Note: Please fill in all items marked *.

	<u></u>	Company name					ФФ	Supervisor
*	Sustomer	Telephone number		()		Issuance signature	
	Date issued	Date:						
*1.	The su	y the name of ubmitted flopp	f the product beir y disk must be 3 he mask files mu	.5-inch 2HD typ		mat if this order is	perfor	med by a floppy disk.
	Micro	computer nar	me:	M37544G2	A-XXXSP	M375440	G2A-X	XXGP

Note: Write data to only ROM data area (addresses E080 $_{16}$ to FFD3 $_{16}$, FFD5 $_{16}$ to FFFD $_{16}$). ROM option data area: Addresses 10 $_{16}$

Notes (RENESAS ---> Customer)

File code

Mask file name

Note 1 : ROM data confirmation request

QzROM programming will be processed based on the mask file generated by the mask file generating utility. Only in the case when ROM data programmed in the actual mass produced product differs from that of above mentioned mask file, Renesas takes the responsibility. There is no Engineering Sample, thus please confirm the ROM data at the receipt of the Initial product delivery.

Should you find any problem, please return immediately. Two weeks without technical error feedback towards Renesas will automatically be regarded as acceptance of products.

Note 2: ROM option ("Mask option" written in the mask file converter MM)

Either of the following data should be set to the ROM option data address (10₁₆) of the mask file you have ordered. When you don't protect the ROM data, a third party can read out it.

When the ROM data is protected

00₁₆

Address 10₁₆

When the ROM data is not protected

FF₁₆ Address 10₁₆

(hexadecimal notation)

.MSK (equal or less than eight characters)

If you set except the above data or nothing at the ROM option data address (10 $_{16}$), We can't generate the ROM data. Then we request to submit the data again.

When Renesas ships QzROM write products, we write the data in the ROM option address (10 16) to the actual ROM code protect address (FFD416).

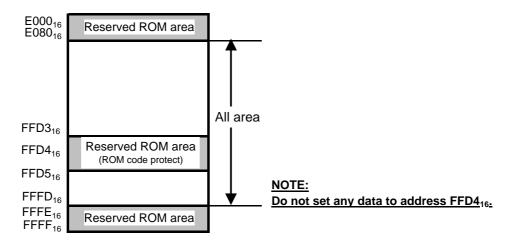
Therefore, set FF_{16} to address $FFD4_{16}$ in the ROM data regardless of the presence or absence of a protect. When data other than FF_{16} is set, we may ask that the ROM data be submitted again.

Note 3: Mark specification

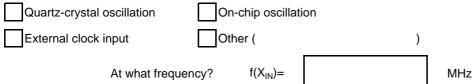
You can appoint the mark by the mark specification form. Without submitting the mark specification form, your mark will be a standard mark. Please fill out the 32P4B MARK SPECIFICATION FORM for the M37544G2A-XXXSP, the 32P6B/U MARK SPECIFICATION FORM for the M37544G2A-XXXGP, and attach it when you submit the QzROM PROGRAMMING CONFIRMATION FORM. We can't deal with special font marking (customer's trademark etc.) in QzROM microcomputer.

Qzrom Programming Confirmation form SINGLE-CHIP 8-BIT MICROCOMPUTER M37544G2A-XXXSP/GP RENESAS TECHNOLOGY

ROM-Protection-Area



*2.	Usage conditions For our reference of new products, plordered.	lease reply to the following questions about the sage of the products you
	(1) Which operation source main clos	ck do you use?
	Ceramic resonator	RC oscillation



(2) What is the voltage of power supply (V_{DD}) you use?

Тур.=		V Min.=		V	Max.=		٧
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(3) What is the ambient temperature you use?

Typ.=	င	Min.=	C	Max.=	¢	С
Typ.=	C	IVIII1.=	C	wax.=		۲

(4) Which clock division ratio mode do you use?

Double-speed mode $(f(\phi)=f(X_{IN}))$	High-speed mode $(f(\phi)=f(X_{IN})/2)$
Middle-speed mode ($f(\phi)=f(X_{IN})/8$)	Applied from on-chip oscillator

Qzrom Programming Confirmation Form Single-Chip 8-bit Microcomputer M37544G2A-XXXSP/GP RENESAS TECHNOLOGY

(5) Please reply to the	following questions about timer function.	
(i) Which timer do yo	ou use?	
Timer1	TimerX TimerA	
(ii) Which count sour	rce of timer so you use?	
- Timer1	$f(X_{IN})/16$ $f(X_{IN})/2$ On-chip oscillator output	
- TimerA	$f(X_{IN})/16$ $f(X_{IN})/2$ On-chip oscillator output	
- TimerX		
(iii) Which operating	mode do you use?	
- TimerA	Timer mode Period measurement mode	,
	Event counter mode Pulse width HL continuously measurement mode	
- TimerX	Timer mode Pulse output mode	
	Event counter mode Pulse width measurement mode	
(6) Do you use the Ser	rial I/O?	
Use	Not use	
(Clock sy	nchronous Serial I/O mode Asynchronous Serial I/O(UART) mode)	
(7) Do you use the A/D) converter?	
Use	Not use	
(8) Do you use the Wa	tchdog timer?	
Use	Not use	
(9) Do you use the osc	cillation stop detection circuit?	
Use	Not use	
Thank you cooperation	on	

*3. Comments