RENESAS Tool News

Renesas Peripheral Driver Library for RX63N and RX631 Group Revised to V.1.10

We have revised Renesas Peripheral Driver Library for RX63N and RX631 Group from V.1.01 to V.1.10.
For the overview of the product, go to:
https://www.renesas.com/driver/rpdl
The above URL is one of our global sites.

1. Descriptions of Revision

1.1 Conformed to user's manual Rev.1.60 for RX63N and RX631 Group of MCUs

The library functions have been updated conforming to
the specifications of the latest user's manual Rev.1.60.

1.2 Corresponding MCU packages added

The 48-pin and 64-pin packages of RX631 Group MCUs have been supported.

1.3 Options and functions added

(1) Addition of Sub-clock Oscillator Driving Ability Select Option
The Sub-clock Oscillator Driving Ability Select Option (Sub-clock
oscillator drive ability) has been added to the second arguments of
the clock setting function (R_CGC_Set function).

(2) Addition of Sub-clock Oscillator Stabilization Waiting Time Select
Option
The Sub-clock Oscillator Stabilization Waiting Time Select Option
(Sub-clock oscillator waiting time) has been added to the eleventh
arguments of the clock setting function (R_CGC_Set function).

(3) Addition of Realtime Clock (RTC) Count Source Select Option
The RTC Count Source Select Option (RTC Count source selection) has
been added to the second arguments of the clock setting function
(R_CGC_Set function).

(4) Addition of a new function to the IO_PORT API
   The R_IO_PORT_Switch Function which controls the pin order when
   using the 48-pin or 64-pin package of RX631 Group MCUs has been
   added.

(5) Addition of 8 Hz to the frequency of the RTC's periodic
   interrupt generation
   The "PDL_RTC_PERIODIC_8_HZ" option has been added to the Periodic
   interrupt selection in the eleventh arguments of the RTC control
   function (R_RTC_Control function).

(6) Addition of Continuous Receive Mode Option in communications using
   Serial Communication Interface (SCI) asynchronous
   The Continuous Receive Option (Continuous receive Mode) has been
   added to the second arguments of the SCI receive function
   (R_SCI_Receive function).

(7) Addition of 10-bit Address Format Option to the I2C Bus Interface
   (RIIC) master receive
   function
   The transmitting option (Slave address size override) for 10-bit
   address format has been added to the second arguments of
   the RIIC master receive function (R_IIC_MasterReceive function).

1.4 Problems fixed
The following three problems have been fixed.
(1) Problem with Setting the Real-Time Clock (RTC) of RX63N Group MCUs
   For details of the problem, see RENESAS TOOL NEWS Document
   No. 120916/tn3 at:
      https://www.renesas.com/search/keyword-search.html#genre=document&q=120916tn3

(2) Problem with Setting Programmable Pulse Generators (PPGs)
   Notes about the setting procedures have been added to the
   explanation of the R_PPG_Create function in the
   Renesas Peripheral Driver Library User's Manual.
   For details of the problem, see RENESAS TOOL NEWS Document
   No. 120916/tn2 at:
      https://www.renesas.com/search/keyword-search.html#genre=document&q=120916tn2

(3) With Setting Privileged Instruction Exception and Access Exception
   Interrupts
   For details of the problem, see RENESAS TOOL NEWS Document
   No. 130401/tn5 at:
      https://www.renesas.com/search/keyword-search.html#genre=document&q=130401tn5

Note that the problems in (1), (2) and (3) above have not been fixed in
Peripheral Driver Generator yet.

2. Obtaining Library

Download the sample program of Renesas Peripheral Driver Library for
RX63N and RX631 Group from the following Web page:
https://www.renesas.com/mw/rpdl_app_notes
  Document Title: RX63N, RX631 Group Renesas Peripheral Driver Library

This sample program will be published in this Web page on June 20, 2013.
The above URL is one of our global sites.