Tool News

Peripheral Driver Generator Revised to V.2.03

We have revised Peripheral Driver Generator from V.2.02 to V.2.03.
This utility program aids you to create on-chip peripheral I/O drivers.
Peripheral Driver Generator V.2 is specific to the RX family of MCUs.
For an overview of Peripheral Driver Generator, see:
https://www.renesas.com/pdg
The above URL is one of our global sites.

1. Description of Revision

The revised product supports the RX210 group of MCUs. The following peripheral I/O modules of these MCUs are supported:

- Voltage detection circuit (LVDAa)
- Clock generator circuit
- Clock frequency accuracy measurement circuit (CAC)
- Low power consumption
- Register write protection function
- Interrupt control unit (ICUb) and exception handlers
- Buses
- DMA controller (DMACA)
- Data transfer controller (DTCa)
- Event link controller (ELC)
- I/O ports
- Multi-function pin controller (MPC)
- Multi-function timer pulse unit 2 (MTU2a)
- Port output enable 2 (POE2a)
- 8-bit timer (TMR)
- Compare match timer (CMT)
- Real-time clock (RTCb)
- Watchdog timer (WDTA)
- Independent watchdog timer (IWDTa)

- Serial communication interface (SCIc and SCId)
- I2C bus interface (RIIC)
- Serial peripheral interface (RSPI)
- CRC calculator (CRC)
- 12-bit A/D converter (S12ADb)
- D/A converter (DA)
- Comparator A (CMPA)
- Comparator B (CMPB)
- Data calculation circuit (DOC)

2. Host System Requirements

Computer: IBM PC/AT or compatible
OS: Windows7, Windows XP, or Windows Vista

3. How to Update Your Product

Update yours in either of the following ways:
(1) Use Auto Update Utility. This service will be available on and
    after March 14.
(2) Download the installer of the revised product from:
       https://www.renesas.com/pdg_download

    Then execute it. The installer will be published on the
    Web   site on March 12.
     The above URL is one of our global sites.