RENESAS Tool News

The C/C++ Compiler Package for the SuperH RISC engine MCU Family Revised to V.9.02 Release 00

The C/C++ compiler package for the SuperH RISC engine MCU family has been revised from V.9.01 Release 01 to V.9.02 Release 00.

1. Descriptions of Revision

1.1 High-performance Embedded Workshop Updated

The High-performance Embedded Workshop included in the package has been updated to V.4.04.01. For details of the update, see RENESAS TOOL NEWS Document No. 071216/tn5, published on December 16, 2007, at

http://tool-support.renesas.com/eng/toolnews/071216/tn5.htm

(Content of revision from V.4.03.00 to V.4.04.00)

and RENESAS TOOL NEWS Document No. 080118/tn1, published on January 18, 2008, at

http://tool-support.renesas.com/eng/toolnews/080118/tn1.htm

(Content of revision from V.4.04.00 to V.4.04.01)

1.2 Simulator Debugger Updated

The simulator debugger included in the package has been updated to V.9.07.00. For details of the update, see RENESAS TOOL NEWS Document No. 070901/tn6, published on September 1, 2007, at

http://tool-support.renesas.com/eng/toolnews/070901/tn6.htm

(The Simulator Debuggers for the SuperH RISC engine Family and the H8SX, H8S, and H8 Families of MCUs Revised)

1.3 Functionality Introduced to the Compiler

(1) The following options introduced:

(a) stuff_gbr: Places the variables declared using #pragma gbr_base or #pragma gbr_base1 into their proper sections according to the size.

(b) align4: Aligns the branch destination addresses in 4 bytes.

(c) cpp_noinline: Suppress the inline expansion of the functions

with inline specifiers and the member functions in C++ source.
(2) Preprocessing directive "pragma align4" introduced, which aligns the branch destination addresses in 4 bytes.

1.4 Functionality Introduced to the Optimizing Linkage Editor
(1) The following options introduced:
  (a) RTs_file: Generates information files for the specified emulators.
  (b) CRc: Calculates the CRC (Cyclic Redundancy Check) of the specified ranges and embed the result into the specified address of memory.

1.5 Problems Fixed
The following problems have been fixed:
(1) With updating the C/C++ compiler packages for the SuperH family, and the H8SX, H8S, and H8 families of MCUs
   For details see RENESAS TOOL NEWS Document No. 070901/tn1 at
     http://tool-support.renesas.com/eng/toolnews/070901/tn1.htm
(2) With using the Automatic Literal Pool Generation function
(3) With optimizing the program that contains instructions referencing literal pools using the linkage editor
   For details of (2) and (3) above, see RENESAS TOOL NEWS Document No. 080216/tn1 at
     http://tool-support.renesas.com/eng/toolnews/080216/tn1.htm
   (available on and after February 20, 2008).
(4) With creating projects for the SH4AL-DSP CPU-cored MCUs
   For details see RENESAS TOOL NEWS Document No. 070601/tn1 at
     http://tool-support.renesas.com/eng/toolnews/070601/tn1.htm
(5) With changing the version of your toolchain to another
   For details see RENESAS TOOL NEWS Document No. 070416/tn1 at
     http://tool-support.renesas.com/eng/toolnews/070416/tn1.htm

2. How to Update Yours and Purchase the Revised Product
2.1 Updating
Free-of-charge online update is available. Use AutoUpdate Utility; otherwise, download the update program of the product from the Web site at
   http://www.renesas.com/shc_download
and execute it. This site will be opened from February 20 on.

2.2 First Ordering
If you place an order for the product, please supply the following items of information to your local Renesas Technology sales office or

distributor.

Product Type:	C/C++ compiler package for the SuperH RISC engine family
Version No.:	V.9.02
Release No.:	Release 00
Host OS:	Windows XP or Windows 2000

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