

Notes on Using the E8 Emulator Software V.2.08 Release 00

Please take note of the following problems in using the emulator software V.2.08 Release 00 for the E8 on-chip debugging emulator:

- On debugging the user's programs using watch-dog timers
 - On inputting reset signals from the user's system
-

1. Problem on Debugging the User's Programs Using Watch-Dog Timers

1.1 Description

If the target MCU is reset after the user's program that validates the count source protection mode of a watch-dog timer is loaded in the target MCU, "Communication Timeout Error" may arise.

1.2 Conditions

This problem occurs if the following conditions are all satisfied:

- (1) The R8C/Tiny E8 emulator debugger V.2.04.00 (included in the emulator software) is used.

How to check for the version of your R8C/Tiny E8 emulator debugger

In the High-performance Embedded Workshop, open the Tool menu and select the Administration command. The Administration dialog box appears, and you see the Registered Components list box.

Then read the version number of "R8C E8 SYSTEM" in the Debugger Components folder, which is displayed in the list box.

- (2) Any member of the R8C/Tiny MCU series listed in the following table is targeted for debugging:
 - R5F21142, R5F21143, and R5F21144 (R8C/14 group)

- R5F21152, R5F21153, and R5F21154 (R8C/15 group)
- R5F21162, R5F21163, and R5F21164 (R8C/16 group)
- R5F21172, R5F21173, and R5F21174 (R8C/17 group)
- R5F21181, R5F21182, R5F21183, and R5F21184 (R8C/18 group)
- R5F21191, R5F21192, R5F21193, and R5F21194 (R8C/19 group)
- R5F211A1, R5F211A2, R5F211A3, and R5F211A4 (R8C/1A group)
- R5F211B1, R5F211B2, R5F211B3, and R5F211B4 (R8C/1B group)
- R5F21206 and R5F21207 (R8C/20 group)
- R5F21216 and R5F21217 (R8C/21 group)
- R5F21226 and R5F21227 (R8C/22 group)
- R5F21236 and R5F21237 (R8C/23 group)
- R5F21244 and R5F21246 (R8C/24 group)
- R5F21254 and R5F21256 (R8C/25 group)

- (3) The option function select register (address FFFFh) is set in the user's program as follows:
- The watchdog timer start select bit (bit 0) is set to 0.
 - The count source protection mode after reset select bit (bit 7) is set to 0.

1.3 Workaround

Set the count source protection mode after reset select bit (bit 7, at address FFFFh) to 1 to invalidate the count source protection mode.

1.4 Schedule of Fixing the Problem

We plan to fix this problem in the next release of the product.

2. Problem on Inputting Reset Signals from The User's System

2.1 Description

If the execution or stopping of the program and the inputting of reset signals from the User's system are performed at the same time, "Communication Timeout Error" may arise.

2.2 Conditions

This problem occurs if the conditions (1) and (2) in Section 1.2 above are both satisfied.

2.3 Workaround

Do not input reset signals from the user's system unless the program is running free with no break events being set or if any of the following buttons is pressed:

- Go To Cursor

- Reset Go
- Step In
- Step Over
- Step Out
- Halt Program

2.4 **The Schedule of Fixing the Problem**

We plan to fix this problem in the next release of the product.

[Disclaimer]

The past news contents have been based on information at the time of publication. Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

© 2010-2016 Renesas Electronics Corporation. All rights reserved.