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# A Note on Using the Real-Time OSes M3T-MR308K/4 and -MR308S/4

Please take note of the following problem in using the real-time OSes M3T-MR308K/4 and -MR308S/4, which are used for the M32C/80, and M16C/80 series of MCUs:

On issuing the iunl\_cpu service call

#### 1. Versions Concerned

M3T-MR308K/4 and -MR308S/4 V.4.00 Release 00 and V.4.00 Release 01

### 2. Description

When the iunl\_cpu service call is issued, the IPL value of the FLG register may become indefinite. As a result, an interrupt that should be masked will be generated.

#### 3. Condition

This problem occurs if the iloc\_cpu service call is not issued, and the iunl\_cpu service call is issued with the context being kept in the CPU-unlocked state.

#### 4. Workaround

Do not issue the iunl\_cpu service call with the CPU being unlocked; that is, issue it after judging that the CPU is locked using the sns\_loc service call as shown in Example below.

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## 5. Schedule of Fixing the Problem

We plan to fix this problem in the next release of the products.

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