

Note on Using Real-Time OS for RX Family RI600/4 V1.01 Release 00

When using the real-time OS for the RX family of MCUs RI600/4 V1.01 Release 00, take note of the following problem:

- With creating projects on High-performance Embedded Workshop

1. Description

When you create a project for any MCU of the RX600 series on High-performance Embedded Workshop, the resetprg.c file is generated. In this file, two incorrect codes are found, which are shown below.

```
-----  
.....  
void PowerON_Reset_PC(void)  
{  
#ifdef __ROZ  
#define _ROUND 1  
#else  
#define _ROUND 0  
#endif  
#ifdef _DOFF // Incorrect; must be #ifdef __DOFF  
#define _DENOM 100 // Incorrect; must be #define _DENOM 0x100  
#else  
#define _DENOM 0  
#endif  
    set_fpsw(FPSW_init | _ROUND | _DENOM);  
-----
```

2. Workaround

In the generated resetprg.c file, rewrite `#ifdef _DOFF` to `#ifdef __DOFF`,
and `#define _DENOM 100` to `#define _DENOM 0x100`

[Disclaimer]

The past news contents have been based on information at the time of publication. Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

© 2010-2016 Renesas Electronics Corporation. All rights reserved.