

Note on Using the Peripheral Driver Generator

When using the Peripheral Driver Generator, take note of the problem on the following point:

- Setting up the MTU2a in complementary PWM mode or reset-synchronized PWM mode (RX630 group)
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1. Product and Version Concerned

Peripheral Driver Generator V2.09 and later versions

2. MCUs Involved

RX family: RX630 group

3. Description

A build error occurs due to a shortfall in the number of parameters for `R_MTU2_Set()` in the function `bool R_PG_Timer_Set_MTU_U0_C3_C4(void)` when code for the multifunction timer pulse unit (MTU2a) is generated with complementary PWM or reset-synchronized PWM mode selected.

4. Conditions

The error occurs if the complementary PWM mode or reset-synchronized PWM mode of MTU2a is used.

5. Workaround

To avoid the build error, modify two points in the source code generated by the Peripheral Driver Generator as shown below.

That is, modify the parameters for the calls of `R_MTU2_Set()` from within the function `bool R_PG_Timer_Set_MTU_U0_C3_C4(void)`.

Refer to the user's manual for details of the function.

RX630 Group Renesas Peripheral Driver Library User's Manual

<https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut0817>

4.2.14. Multi-Function Timer Pulse Unit

Before the modification:

```
-----  
bool R_PG_Timer_Set_MTU_U0_C3_C4(void)  
{  
    ...  
    res = R_MTU2_Set( 3 ); <- Modify the parameters of this function  
    if( !res ){  
        return res;  
    }  
    res = R_MTU2_Set(    <- Modify the parameters of this function  
        4,  
        PDL_NO_DATA  
    );  
    ...  
-----
```

After the modification:

The marker "<-" below indicates the modifications (added parts).
xx means the port number.

```
-----  
bool R_PG_Timer_Set_MTU_U0_C3_C4(void)  
{  
    ...  
    res = R_MTU2_Set(  
        3,  
        PDL_MTU2_PIN_3B_Pxx | PDL_MTU2_PIN_3D_Pxx, <-  
        PDL_NO_DATA                                <-  
    );  
    if( !res ){  
        return res;  
    }  
    res = R_MTU2_Set(  
        4,  
        PDL_MTU2_PIN_4A_Pxx | PDL_MTU2_PIN_4B_Pxx <-  
        | PDL_MTU2_PIN_4C_Pxx | PDL_MTU2_PIN_4D_Pxxs, <-  
        PDL_MTU2_PIN_CLKx_Pxx                        <-  
    );  
    ...  
-----
```

6. Schedule for Fixing the Problem

This problem will be fixed in a later version of the product.

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