

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A051A/E	Rev.	1.00
Title	RZ/T1 Group User's Manual: Hardware Restriction and notification about the Ethernet MAC		Information Category	Technical Notification		
Applicable Product	RZ/T1 Group	Lot No.	Reference Document	RZ/T1 Group User's Manual: Hardware Rev1.40 R01UH0483EJ0140 Rev.1.40		
		All lots				

There is a restriction about the Ethernet MAC. Please use the Ethernet MAC with the contents as shown below.

And a notification about the Ethernet MAC has been found in User's Manual. Please use the Ethernet MAC with the setting as shown below.

## 1. Restriction

### 1) Contents

SF bit of TX MODE Register (GMAC\_TXMODE) should be 1.

Setting of SF bit = 0 will be prohibited.

In case SF bit of TX MODE Register (GMAC\_TXMODE) is 0, there is a possibility of an unintended TX FIFO interrupt due to SF bit setting of the TX MODE Register (GMAC\_TXMODE).

### 2) Correction of User's Manual

#### 28.2.2.6 TX MODE Register (GMAC\_TXMODE)

Bit	Symbol	Bit Name	Description	R/W
b15, b14	<del>FSTTH[1:0]</del> -	<del>Frame Start Threshold</del> Reserved	<del>Starts transmission when the number of words written to TX FIFO is equal to or more than the value indicated by this bit.</del> b15 b14 <del>0 0 : 4 words</del> <del>0 1 : 8 words</del> <del>1 0 : 16 words</del> <del>1 1 : 32 words</del> These bits are read as 0. The write value should be 0.	R/W
b29	SF	Store & Forward	1: Starts transmission after the data up to the end of the frame is written to TX FIFO. Use this setting to use TCP/IP Accelerator. 0: <del>Starts transmission when the number of words written to TX FIFO reaches the value set by the FSTTH[1:0] bits.</del> Setting prohibited.	R/W

2. Notification

1) Correction of User's Manual

Add the Usage Notes as the following.

These products which are described [Applicable products] do not support transmission/reception of the frame with over 1,518 bytes (Jumbo Frame).