

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A154A/E	Rev.	1.00
Title	RX64M, RX71M Group Notes on QSPI when using serial flash memory.		Information Category	Technical Notification		
Applicable Product	RX71M Group, RX64M Group	Lot No.	Reference Document	RX71M Group User's Manual: Hardware (R01UH0493EJ0100) RX64M Group User's Manual: Hardware (R01UH0377EJ100)		
		All				

This document describes notes on QSPI when using serial flash memory in section 45.4 Usage Notes. It is additional information for RX64M Group and a modification for RX71M Group.

■RX64M Group

【additional information】

45.4 Usage Notes

45.4.3 When using serial flash memory

When using the serial flash memory with SPI mode in Dual-SPI or Quad-SPI, set the SPCMDn.CPOL bit and SPCMDn.CPHA bit (n = 0 to 3) to 1 before using SPI mode 3. SPI mode 0 should not be used. Set the SPCMDn.SPNDEN bit, SPCMDn.SLNDEN bit, SPCMDn.SCKDEN bit (n = 0 to 3) to 1 and provide a delay period for each of them.

■RX71M Group

45.4 Usage Notes

【Before】

45.4.3 When Using SPI Mode 3 in Single-/Dual-/Quad-SPI Operation

When using the serial flash memory in single-/dual-/quad-SPI operation, set the SPCMDn.CPOL and CPHA bits (n = 0 to 3) to 1 before using SPI mode 3.

【After】

45.4.3 When using serial flash memory

When using the serial flash memory with SPI mode in Dual-SPI or Quad-SPI, set the SPCMDn.CPOL bit and SPCMDn.CPHA bit (n = 0 to 3) to 1 before using SPI mode 3. SPI mode 0 should not be used. Set the SPCMDn.SPNDEN bit, SPCMDn.SLNDEN bit, SPCMDn.SCKDEN bit (n = 0 to 3) to 1 and provide a delay period for each of them.

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