

RENESAS TECHNICAL UPDATE

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| | | | | | | |
|--------------------|---|-----------------|----------------------|--|------|------|
| Product Category | MPU/MCU | | Document No. | TN-SY*-A012A/E | Rev. | 1.00 |
| Title | Restriction of serial programming mode and ID code protection in S124 | | Information Category | Technical Notification | | |
| Applicable Product | Renesas Synergy™ S1 Series S124 | Lot No. | Reference Document | S124 User's Manual: Microcontrollers, Rev.1.20 | | |
| | | Refer to Page 2 | | | | |

There are restrictions about S124 products.

1. Restriction about serial programming mode

1.1. Restriction

Serial programming mode cannot be used.

1.2. Workaround

On-chip debug mode can be used for programming and erasing method.

1.3. Future plan

Please refer to "3. Remarks" about the revised products.

2. Restriction about ID code protection ("ALeRASE" code)

2.1. Restriction

"ALeRASE" ID code which is sent from the on-chip debugger can't be used.

ALeRASE: This command can erase all flash area including ID code for forgetting ID code set by user.

2.2. Workaround

Please remember the ID code, when you set ID code protection enable.

2.3. Future plan

Please refer to "3. Remarks" about the revised products.

3. Remarks

3.1. Marking Specification

Marking Specification of each Package is as follows.

(YY: Year Code, WW: Week Code, X: unknown)

[64-pin QFN]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| R | 7 | F | S | 1 | 2 | 4 | 7 | 7 | 3 |
| A | 0 | 1 | C | N | B | | | | |
| Y | Y | W | W | X | X | X | X | X | |

[64-pin QFP]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|--|
| | | R | 7 | F | S | 1 | 2 | 4 | |
| 7 | 7 | 3 | A | 0 | 1 | C | F | M | |
| | | Y | W | W | X | X | X | X | |

[48-pin QFN]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| R | 7 | F | S | 1 | 2 | 4 | 7 | 7 | 3 |
| A | 0 | 1 | C | N | E | | | | |
| Y | Y | W | W | X | X | X | X | X | |

[48-pin QFP]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|--|
| Y | W | W | X | X | X | X | X | X | |
| S | 1 | 2 | 4 | 7 | 7 | 3 | | | |
| | | | A | 0 | 1 | C | | | |

[40-pin QFN]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| R | 7 | F | S | 1 | 2 | 4 | 7 | 7 | 3 |
| A | 0 | 1 | C | N | F | | | | |
| Y | Y | W | W | X | X | X | X | X | |

[36-pin LGA]

| | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|
| R | 7 | F | S | 1 | 2 | 4 | 7 | 7 | 2 |
| A | 0 | 1 | C | L | M | | | | |
| Y | Y | W | W | X | X | X | X | X | |

3.2. Restriction and Revised products information

Restriction products can be distinguished with revised products from a part of the sample Lot No. (Year Code Week Code)

Following table indicates the information of Year Code and Week Code.

| Package | Product Name | Year Code Week Code (A part of Lot No.) | |
|------------|----------------------|---|----------------------------|
| | | Restriction device (YYWW, YWW) | Revised device (YYWW, YWW) |
| 64-pin QFN | R7FS124773A01CNB#AC0 | 1610 | from 1622 |
| 64-pin QFP | R7FS124773A01CFM#YB0 | 551 | from 614 |
| | R7FS124773A01CFM#AA0 | 608 | |
| 48-pin QFN | R7FS124773A01CNE#AC0 | 1614 | from 1630 |
| 48-pin QFP | R7FS124773A01CFL#AA0 | 608, 615 | from 620 |
| 40-pin QFN | R7FS124773A01CNF#AC0 | No shipment | from 1630 |
| 36-pin LGA | R7FS124772A01CLM#YB0 | 1552 | from 1613 |
| | R7FS124772A01CLM#AC0 | 1608 | |