

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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RENESAS TECHNICAL UPDATE

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|--------------------|---|----------------------|------------------------|------|------|
| Product Category | MPU&MCU | Document No. | TN-16C-A145A/E | Rev. | 1.00 |
| Title | R8C/1A, 1B Groups Precautions on SSI Signal Pin Select Function of Clock Synchronous Serial I/O with Chip Select | Information Category | Technical Notification | | |
| Applicable Product | R8C/1A, 1B Groups | Lot No. | Reference Document | | |
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As for the clock synchronous serial I/O with chip select (SSU) included in the above applicable products, there is a precaution on using it.

1. Precautions

If operating the clock synchronous serial I/O with chip select (SSU) as a slave device in standard mode of four-wire bus communication mode, use it with CMOS output when using P1_6 as the SSI pin. Do not use the SSI01 pin with the NMOS open drain output.

2. Settings

Set the SOOS bit in the SSMR2 register to "0" (CMOS output) in the following settings:

- SSUMS bit in SSMR2 register = 1 (four-wire bus communication mode)
- BIDE bit in SSMR2 register = 0 (standard mode)
- MSS bit in SSCRH register = 0 (operate as slave device)
- SSISEL bit in PMR register = 1 (use P1_6 pin for SSI01 pin)

Do not use the SSI01 pin with the NMOS open drain output in the above settings.