

Microcontroller Technical Information

<p>QB-V850ESX3H In-Circuit Emulator for V850E/SJ3-H and V850E/SK3-H</p> <p>Usage Restrictions</p>	Document No.	ZBG-CD-09-0056	1/2
	Date issued	October 21, 2009	
	Issued by	Development Tool Solution Group Multipurpose Microcomputer Systems Division Microcomputer Operations Unit NEC Electronics Corporation	
<p>Related documents</p> <p>QB-V850ESX3H Preliminary User's Manual: ZUD-CD-08-0190-1</p>	Notification classification	√	Usage restriction
			Upgrade
			Document modification
			Other notification

1. Affected product

Product	Outline	Control Code ^{Note}
QB-V850ESX3H	In-circuit emulator for V850E/SJ3-H and V850E/SK3-H	A, B

Note The control code is the second digit from the left in the 10-digit serial number. To see if the product has been upgraded, click the ID850QB **Help** menu, select **About**, and then check the control code.

X in **IECUBE **** X F/W: V*. **** is the control code.

2. New restriction

Restriction No. 2 has been added. See the attachment for details.

3. Workaround

See the attachment for details.

4. Modification schedule

Products in which No. 2 is corrected are scheduled for release as follows.

Accepting orders of corrected products (control code C): From October 30, 2009

Accepting orders for exchanging already shipped products: Available from October 30, 2009

* Note that this schedule is subject to change without notice. For the detailed release schedule of modified products, contact an NEC Electronics sales representative.

5. List of restrictions

See the attachment.

6. Document revision history

Document Number	Issued on	Description
ZBG-CD-09-0016	March 9, 2009	1st edition. Addition of restriction No. 1
ZBG-CD-09-0056	October 21, 2009	Addition of restriction No. 2

Operating Precautions for QB-V850ESX3H

This document describes the items below. See the user's manual for cautions on using this emulator.

- Restrictions not applicable to the target device but applicable to this emulator
- Restrictions applicable to both the target device and this emulator, but for which correction is planned only for this emulator

Also see the following documents for the restrictions related to the target device.

- User's manual of target device
- Restrictions notification document for target device

1. Product Version

The product versions of emulators are indicated by a control code. The control code is the second digit from the left in the 10-digit serial number. To see if the product has been upgraded, click the ID850QB **Help** menu, select **About**, and then check the control code.

In Figure 2, **X** in **IECUBE **** X F/W: V*.**** is the control code.

Figure 1. Checking the Control Code (Label on IECUBE)

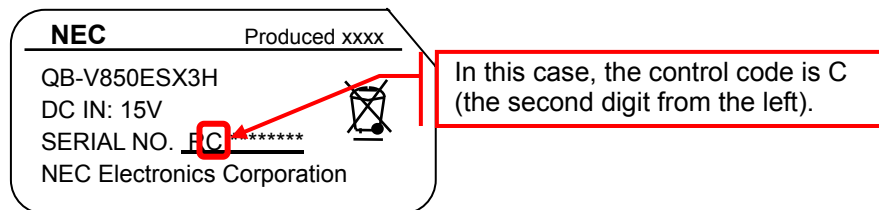
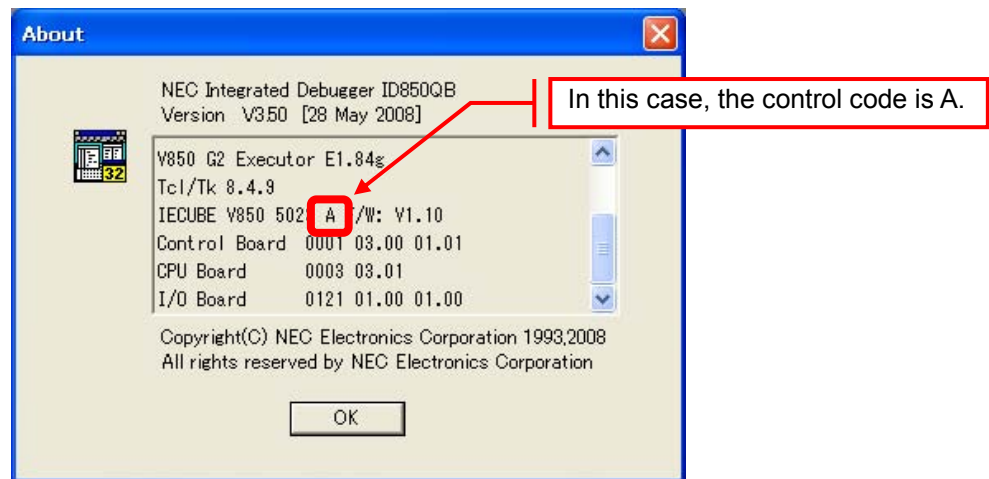
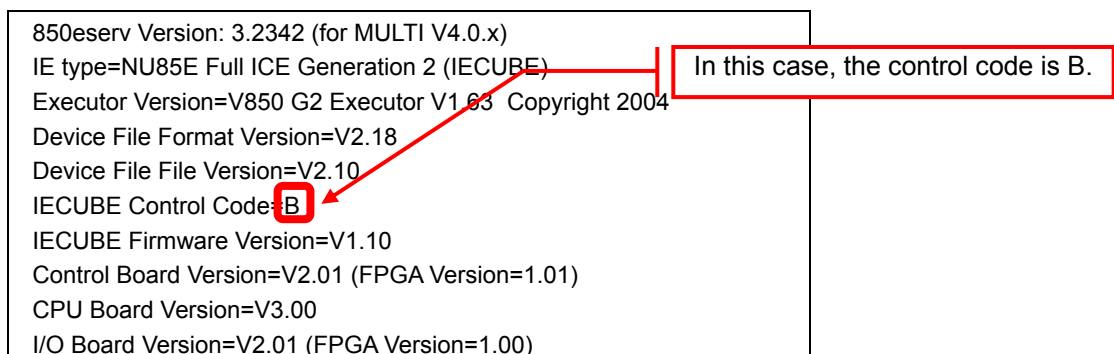


Figure 2. Checking the Control Code (ID850QB)



When using the Green Hills Software™ debugger MULTI®, execute the *version* command of 850eserv. **X** in **IECUBE Control Code=X** is the control code.

Figure 3. Checking the Control Code (MULTI)



2. Product History

No.	Restrictions and Changed/Added Specifications	Control Code		
		A	B	C
1	Restriction on accessing addresses 0x100000 or higher in internal ROM or internal expansion RAM	×	○	○
2	Restriction on using memory map 2 for controlling the external bus	×	×	○

×: Applicable, ○: Corrected

3. Details of Restrictions and Changed Specifications

No. 1 Restriction on accessing addresses 0x100000 or higher in internal ROM or internal expansion RAM

Description:

To access the spaces shown in (1) and (2) below via an external bus interface in multiplexed bus mode when the CPU operating frequency is 32 MHz or higher, an additional data wait cycle is required.

(1) Addresses 0x100000 to 0x17FFFF in internal ROM spaces

(2) Internal expansion RAM spaces

Workaround:

There is no workaround.

To access the spaces shown in (1) and (2), add one data wait cycle as shown below.

(1) Addresses 0x100000 to 0x17FFFF in internal ROM spaces

Set DWC0 to xxx1H (x is any value specified in the program.)

(2) Internal expansion RAM spaces

Set DWC1 to 1777H.

Correction:

This issue has been corrected in products with control code B.

No. 2 Restriction on using memory map 2 for controlling the external bus

Description:

If memory map 2 is selected for controlling the external bus by using the CSC0 register, the CS3 area cannot be accessed correctly.

Workaround:

There is no workaround.

Correction:

This issue will be corrected in products with control code C.