

Microcontroller Technical Information

QB-179F124 In-Circuit Emulator for μ PD179F1xx Usage Restrictions		Document No.	ZBG-CD-10-0037	1/1
		Date issued	October 4, 2010	
		Issued by	MCU Tool Product Marketing Department MCU Software Division MCU Business Unit Renesas Electronics Corporation	
Related documents	QB-179F124 In-Circuit Emulator for μ PD179F1xx - Upgrade: ZBG-CD-10-0038	Notification classification	<input checked="" type="checkbox"/>	Usage restriction
			<input type="checkbox"/>	Upgrade
			<input type="checkbox"/>	Document modification
			<input type="checkbox"/>	Other notification

1. Affected product

Product	Outline	Control Code ^{Note}
QB-179F124	In-circuit emulator for μ PD179F1xx	A, B

Note The control code is the second digit from the left in the 10-digit serial number. To see if the product has been upgraded, click the ID78K0-QB **Help** menu, select **About**, and then check the control code. X in **IECUBE **** X F/W: V*. **** is the control code.

2. New restriction

A new restriction (No. 3) has been added. See the attachment for details.

3. Workarounds

See the attachment for details.

4. Modification schedule

Products in which restriction no. 3 is corrected are scheduled for release as follows:

Newly shipped products: Ordered starting on October 12, 2010 (control code: C)

Upgrade for already shipped products: Available starting on October 12, 2010

* Note that this schedule is subject to change without notice. For the detailed release schedule of modified products, contact a Renesas Electronics sales representative.

5. List of restrictions

See the attachment.

6. Document revision history

Document Number	Issued on	Description
ZBG-CD-07-0031	May 21, 2007	1st edition
ZBG-CD-10-0037	October 4, 2010	Addition of restriction (No. 3)

Operating Precautions for QB-179F124

This document describes the restrictions applicable to this emulator, and for which correction is planned.

See the following documents for the restrictions related to the target device:

- User's manual of target device
- Restrictions notification document for target device

Also see the user's manual for cautions on using this emulator.

1. Product Version

The product version of the Renesas Electronics in-circuit emulator IECUBE is indicated by a control code. The control code is the second digit from the left in the 10-digit serial number. To see if the product has been upgraded, click the ID78K0-QB **Help** menu, select **About**, and then check the control code.

In Figure 2, **X** in **IECUBE **** X F/W: V*. **** is the control code.

Figure 1. Checking the Control Code (Label on QB-179F124)

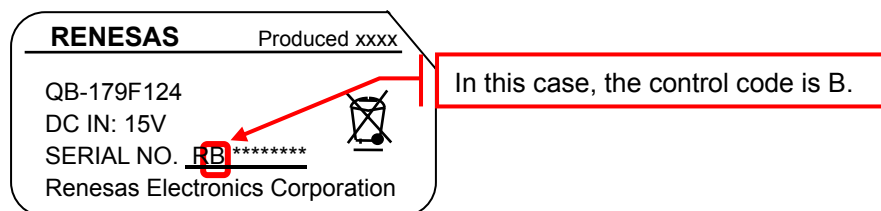
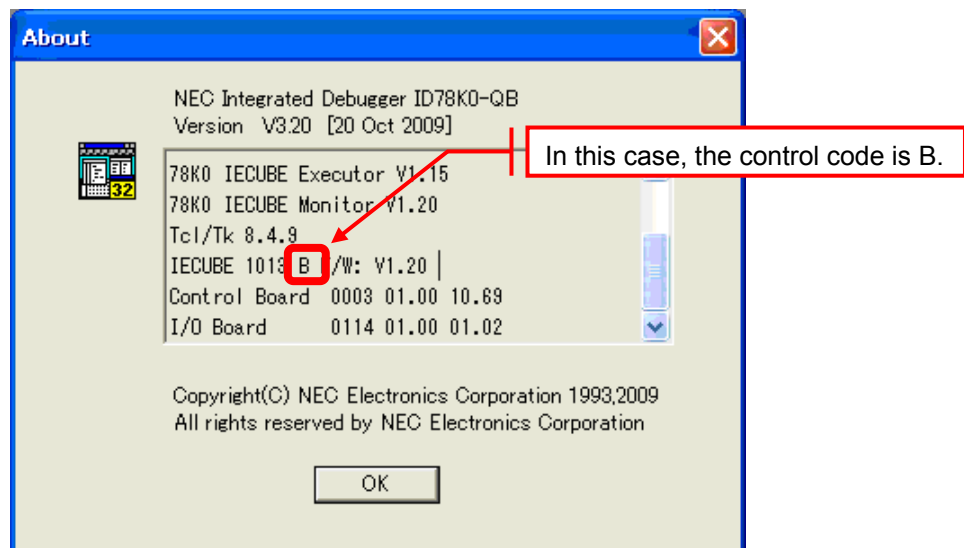


Figure 2. Checking the Control Code (ID78K0-QB)



2. Restrictions

2.1 List of restrictions

No.	Restrictions	Control Code		
		A	B	C
1	Bug whereby R4MTRM2 register cannot be read/written	×	○	○
2	Bug whereby TMHMD1 x2 multiply setting (2fprs) is fixed to 8 MHz	×	○	○
3	Bug whereby RAM data retention detector	×	×	○

×: Restriction applies, ○: Restriction does not apply

2.2 Restriction details

No. 1 Bug whereby R4MTRM2 register cannot be read/written

Description:

The operation after the high-speed internal oscillator trimming register (R4MTRM2) is read or written is as follows.

When write is performed, manipulation for the register is traced in the Trace window but the actual register values remain 00H.

When read is performed, 0x00 is always read.

Workaround:

There is no workaround.

Correction:

This issue has been corrected in products with control code B and later.

After correction, the written values will then be reflected, but note the following caution.

Caution:

IECUBE uses an internal high-speed oscillator clock, which does not require trimming. IECUBE therefore does not perform emulation of the R4MTRM2 register, so accesses to the R4MTRM2 register do not affect the actual operation.

No. 2 Bug whereby TMHMD1 x2 multiply setting (2fprs) is fixed to 8 MHz

Description:

When a count clock is multiplied by 2 (2fprs) using 8-bit timer H mode register 1 (TMHMD1), the count clock is fixed to 8 MHz. (Other settings do not cause this case.)

If the peripheral hardware clock (fprs) is 2 MHz and the TMHMD1 register is set to 2fprs, for example, the count clock should become 4 MHz but becomes 8 MHz.

Workaround:

There is no workaround.

Correction:

This issue has been corrected in products with control code B and later.

No. 3 Bug whereby RAM date retention detector

Description:

RAM date retention detector does not work.

Workaround:

There is no workaround.

Correction:

This issue will be corrected in products with control code C and later.