Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-H8*-A412A/E	Rev.	1.00
Title	Points for Caution when Using the Erase-Suspend Function		Information Category	Technical Notification		
Applicable Product	H8S/20103, H8S/20203, H8S/20223 Groups	Lot No.	Reference Document	H8S/20103, H8S/20203, H8S/20223 Group Hardware Manual Rev.1.00 (REJ09B0465-0100)		

In CPU reprogramming mode, if the erase-suspend function is used during erasure of the user ROM area, operations to erase the user ROM area will not be completed in some cases.

1. Conditions for the problem

In CPU reprogramming mode, repeated use of the erase-suspend function at a certain interval within the period of erasure when the user ROM area is being erased in EW0 mode or EW1 mode

(1) EW0 mode

Repeated generation of interrupts with a certain interval

Repeated setting of the FMSPREQ bit in FLMCR2 to 1 with a certain interval

(2) EW1 mode

Repeated generation of interrupts with a certain interval

2. To avoid the problem

Do not use repeatedly the erase-suspend function at the certain interval. If the erase-suspend function is to be repeatedly used within the period of erasure, employ software control to ensure that the interval between requests satisfies the conditions given in formula 2-1 below to avoid the same interval.

$$T2 > T1 + TD \text{ or } T2 < T1 - TD \dots (2-1)$$

TD = 1.0 µsec (certain period that does not depend on the operating frequency of the product)

T1: Interval between previous and current requests for suspension of erasure

T2: Interval between current and next requests for suspension of erasure