

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A045A/E	Rev.	1.00
Title	Point for Caution Regarding Access to the LCST and GTST Registers of the General PWM Timer (GPT)		Information Category	Technical Notification		
Applicable Product	RX62T Group, RX62G Group, RX63T Group	Lot No.	Reference Document	RX62T Group User's Manual: Hardware		
		All lots		RX62G Group User's Manual: Hardware RX63T Group User's Manual: Hardware		

Thank you for your valued patronage and best wishes for your continued success in business.

This update is notification of a point for caution regarding operations when clearing status flags in the LCST and GTST registers of the general PWM timer (GPT).

## 1.1 Point for Caution

When clearing status flags in the LCST and GTST registers of the general PWM timer (GPT), be sure to only write 0 to the bits that you wish to clear and to write 1 to the other bits. Take care on this point, since if you write 0 to bits other than those you intend to clear, status flags other than those intended will be cleared.

## 1.2 Action from Here

We will reflect this point in the hardware manuals for the RX62T, RX62G, and RX63T the next time they are revised.