

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

Send any inquiries to <http://www.renesas.com/inquiry>.

# RENESAS TECHNICAL UPDATE

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Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A706A/E	Rev.	1.00
Title	Notice about the flag bit of Sampling Rate Converter (SRC)		Information Category	Technical Notification		
Applicable Product	SH7263 Group	Lot No.	Reference Document	SH7263 Group Hardware Manual Rev.2.00 (REJ09B0290-0200)		

We would like to inform you of the notice about Sampling Rate Converter (SRC) included in the above-mentioned applicable product.

## [Notice]

About OVF bit in SRC status register (SRCSTAT), just when the flag is set to 1, if the flag is read, the read data will be 0, but the internal state will be the same as reading 1.

In that case, if the flag is written 0, the flag will be cleared as 0, because the internal state is the same as reading 1.

## [Workaround]

In the case of using OVF bit, to protect unintended bit clear to 0, please write it as following.

- 1) In the case of intended bit clear, please write 0 after reading 1 to the flag.
- 2) In the other cases, please write 1 to the flag.

If the flag is not used, it is no problem to write 0 to flag (in the case of intended bit clear, write 0 after reading 1 to the flag).