To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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date: 2003/11/06

RENESAS TECHNICAL UPDATE

Classification of Production	MPU&MCU			No	TN-SH7-489A/E	Rev	1
THEME	A notice on the 16-bit Timer Pulse Unit (TPU) –an errata on the hardware manual		Classification of Information	1. Spec change (2.) Supplement of Documents 3. Limitation of Use 4. Change of Mask 5. Change of Production Line			
PRODUCT NAME	HD6417705	Lot No. ALL	Reference Documents	SH7705 Group Hardware Manual Rev. 2.00 REJ09B0082-0200O		term of validity Eternity	

When falling edge of count clock is selected by the clock edge bits, CKEG1,0=01 in the timer control registers (TCR), the input clock PØ/1 cannot be selected. If it is selected, the timer does not work properly.

The section 14.3.1 Timer Control Registers (TCR), bit4,3 Clock Edge,

Errata:

00: Count at rising edge
01: Count at falling edge
1x: Count at both edges *
[Legend] X: Don't care
Note: *Internal clock-edge selection is valid when the input clock PØ/4 or slower.
If the input clock is PØ/1, this operation is not performed.

Correction:

00: Count at rising edge
01: Count at falling edge *
1x: Count at both edges *
[Legend] X: Don't care
Note: *Internal clock-edge selection is valid when the input clock PØ/4 or slower.
If the input clock is PØ/1, this operation is not performed.