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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A027A/E	Rev.	1.00
Title	Notes on Transmit End Flag (TEND Flag) in Serial Communications Interface (SCIc, SCId) for the RX630 Groups		Information Category	Technical Notification		
Applicable Product	RX630 Group	Lot No.	Reference Document	RX630 Group User's Manual: Hardware Rev.1.20 (R01UH0040EJ0120)		
		All				

1. Notes

   When writing transmission data to the TDR register while the SCR.TE bit is 1 (serial transmission is enabled), the SSR.TEND flag becomes 0 (a character is being transmitted). When transmission is completed, the TEND flag becomes 1 (character transfer has been completed). However, under the conditions below, transmission may be completed before reading the SSR register, so the TEND flag value may not be 0 when a read access is performed to confirm that it has become 0.

   • The CPU operating speed is extremely slow compared to the bit rate.

   • Interrupt handling or DMA/DTC transfer is inserted between writing transmission data to the TDR register and reading the SSR register.

2. Measure

   Under the conditions described above, it is not necessary to read the TEND flag and confirm that it has become 0 after writing transmission data to the TDR register.

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