

RENESAS TECHNICAL UPDATE

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|--------------------|---|---------|----------------------|------------------------|------|------|
| Product Category | MPU/MCU | | Document No. | TN-16C-A242A/E | Rev. | 1.00 |
| Title | Note on Using E ² PROM Emulation Data Flash (E ² dataFlash) | | Information Category | Technical Notification | | |
| Applicable Product | R32C/120 Group, R32C/121 Group R32C/151 Group, R32C/152 Group R32C/153 Group, R32C/156 Group R32C/157 Group R32C/160 Group, R32C/161 Group E ² dataFlash embedded product of above. | Lot No. | Reference Document | - | | |
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1. Note

When change the OM bit in the E2FM register from 0 (sleep mode) to 1 (normal operating mode), the operation mode monitor flag (the OMM bit in the E2FS1 register) may not become 1 (operating mode) depending on the used environment (power supply variation, noise, temperature change).

After this phenomenon the E²dataFlash cannot be used.

2. Workaround

When this phenomenon has occurred, reset the MCU⁽¹⁾.

Note: 1. Hardware reset (including power on reset), software reset and Watchdog timer reset.

Even if the RST bit (reset bit) in the E2FM register is set to 1, the E²dataFlash is not reset. The RST bit is used to stop ongoing program/erase operations, so it does not reset the E²dataFlash module.