To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

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Renesas Electronics Corporation

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RENESAS

RENESAS TECHNICAL UPDATE

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Renesas Technology Corp.

Product Category	MPU&MCU			Document No.	TN-SH7-A683A/E	Rev.	1.00
Title	Note on Use of SH7780 Module Standby Mode			Information Category	Technical Notification		
Applicable Product	SH7780 Group	Lot No.		Reference Document	SH7780 Hardware Manual Rev.1.00 Dec.13.2005 (REJ09B0158-0100)		
		All lots					

The SH7780 has an additional note on use of module standby mode.

Hardware manual of the SH7780 is corrected as follows.

Under line part is added.

Transition to Module Standby Mode (reference document page 649 of 1286, section 17.5.1)

Setting a corresponding bit in the standby control register (MSTPCR) to 1 will stop the clock supply.

Modules in module standby state keep the state immediately before the transition to the module standby state. The registers keep the contents before halted, and the external pins keep the functions before halted. At waking up from the module standby state, operation is restarted from the condition immediately before the registers and external pins have halted.

Note that, the registers of the HSPI, MMCIF, HAC and SSI modules are initialized by the manual reset generation even if these modules are in module standby mode. Therefore, in this case, the values of their registers are not retained when modules return from its module standby mode.

Note: Make sure to set the MSTP bit to 1 while the modules have completed the operation and are in an idle state, with no interrupt sources from the external pins or other modules.

In relation to above content, add the following note to the register state of module standby mode for the corresponding modules listed below.

Under line part is added and cancellation part is removed.

Note: When the manual reset is generated, the registers are initialized and become the same value of the power-on reset or manual reset.

[Corresponding modules]

- HSPI (page 851 of 1286, section 23, table 23.3, Register States of HSPI in Each Processing Mode)
- MMCIF (page 869 and 870 of 1286, section 24, table 24.3, Register States of ~~HSPI~~ MMCIF in Each Processing Mode)
- HAC (page 957 of 1286, section 25, table 25.3, Register States of HAC in Each Processing Mode)
- SSI (page 985 of 1286, section 26, table 26.3, Register States of SSI in Each Processing Mode)

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