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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-H8*-A421B/E	Rev.	2.00
Title	Note on Usage of H8S/20103, H8S/20203, H8S/20223, H8S/20115,H8S/20215, H8S/20235 Group Products		Information Category	Technical Notification		
Applicable Product	H8S/20103, H8S/20203, H8S/20223, H8S/20115,H8S/20215, H8S/20235 Group	Lot No. All lots	Reference Document	H8S/20103, H8S/20203, H8S/20223, H8S/20115, H8S/20215, H8S/20235 Group Hardware Manual (REJ09B0465-0200 Rev.2.00)		

Thank you for your consistent patronage of Renesas semiconductor products.

We would like to inform you of an amendment to the description for the lock-bit reading command.

7.6.1 Software Commands

(7) Lock-Bit Reading Command

[Before amendment]

A lock-bit reading command is to cause a transition to a mode in which the lock bit in flash memory can be read.

When H'71 is written in the first command cycle and H'D0 is written to any address in the block in the second command cycle, lock-bit reading of the specified block is started.

After transition to lock-bit read mode, reading the specified block address BA returns the lock-bit value in the bit 14 value to be read. Do not execute a lock-bit reading command in the ROM.

[After amendment]

This command is used to read the value of the lock bit in flash memory.

Writing H'71 in the first command cycle and reading from the specifying block address (BA) in the second command cycle returns the value of the lock bit. If a word instruction is used for reading, the value of the lock bit will be reflected in bits 6 and 14 of the read-out word. If a byte instruction is used, the value of the lock bit will be reflected in bit 6. Execute the lock-bit reading command in EW0 mode.