RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A035A/E	Rev.	1.00
Title	Note on Transmit Enable Bit (TE bit) in Serial Communications Interface (SCI) of RX210 Group		Information Category	Technical Notification		
Applicable Product	RX210 Group	Lot No. All	Reference Document	RX210 Group User's Manual: Hardwar Rev.1.10 (R01UH0037EJ0110)		lardware

1. Note

When setting the SCR.TE bit to 0 (serial transmission is disabled) while the pin function is "TXDn" (n = 0, 1, 5, 6, 8, 9, 12), output of the pin becomes high impedance.

2. Measure

Prevent the TXDn line from becoming high impedance by any of the following ways:

(1) Connect a pull-up resistor to the TXDn line.

(2) Change the pin function to "general-purpose I/O port, output" before setting the SCR.TE bit to 0.

Set the SCR.TE bit to 1 before changing the pin function to "TXDn".

Note that a TXI interrupt request is generated when both bits SCR.TE and SCR.TIE become 1.

