

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RZ*-A0074A/E	Rev.	1.00
Title	Note on the size of OctaRAM memory of the Octa Memory Controller in RZ/A2M Group Products		Information Category	Technical Notification		
Applicable Product	RZ/A2M Group	Lot No.	Reference Document	RZ/A2M Group User's Manual: Hardware Rev.4.00 (R01UH0746EJ0400)		
		All				

This document is a cautionary note regarding the size of OctaRAM memory of the Octa Memory Controller in products of the RZ/A2M Groups.

1. Description of the DV1SZ[29:0] bits

[Before]

Bit	Bit Name	Initial Value	R/W	Description
29 to 0	DV1SZ[29:0]	All 0	R/W	Device 1 size setting Set a 30-bit value for the size of memory connected as device 1. Examples: H'010000000: 16-Mbyte RAM H'00800000: 8-Mbyte RAM

[After]

Bit	Bit Name	Initial Value	R/W	Description
29 to 0	DV1SZ[29:0]	All 0	R/W	Device 1 size setting Set a 30-bit value for the size of memory connected as device 1. Examples: H'010000000: 16-Mbyte RAM H'00800000: 8-Mbyte RAM Note. DV1SZ[29:0] can be set less than or equal to H'00800000.

2. 22.5.2 Address Map

[Before]

22.5.2 Address Map

In the memory-map mode, OctaFlash is allocated to the OctaFlash space (H'50000000 to H'5FFFFFFF), and OctaRAM is allocated to the OctaRAM space (H'60000000 to H'6FFFFFFF).

One OctaFlash device and one OctaRAM device can be connected to this LSI, and up to 256 Mbytes can be accessed in each device.

	Internal Address	Maximum Accessible Area
OctaFlash	H'50000000 to H'5FFFFFFF	Up to 256 Mbytes
OctaRAM	H'60000000 to H'6FFFFFFF	Up to 256 Mbytes

[After]

22.5.2 Address Map

In the memory-map mode, OctaFlash is allocated to the OctaFlash space (H'50000000 to H'5FFFFFFF), and OctaRAM is allocated to the OctaRAM space (H'60000000 to H'6FFFFFFF).

One OctaFlash device and one OctaRAM device can be connected to this LSI, ~~and up to 256 Mbytes can be accessed in each device.~~

	Internal Address	Maximum Accessible Area
OctaFlash	H'50000000 to H'5FFFFFFF	Up to 256 Mbytes
OctaRAM	H'60000000 to H'607FFFFF	Up to 8 Mbytes