

# RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan  
Renesas Electronics Corporation

Product Category	MPU/MCU		Document No.	TN-RX*-A037A/E	Rev.	1.00
Title	Note on Setting Counter Clock for Independent Watchdog Timer (IWDT) of RX210 Group		Information Category	Technical Notification		
Applicable Product	RX210 Group	Lot No.	Reference Document	RX210 Group User's Manual: Hardware Rev.1.10 (R01UH0037EJ0110)		
		All				

## 1. Note

When the frequency of the IWDT counter clock is close to the frequency of the peripheral module clock (PCLK), registers associated with the IWDT may not be accessed properly.

## 2. Measure

Set the frequency of the IWDT counter clock to a quarter of the PCLK frequency or lower.

Specifically, set the IWDTCR.CKS[3:0] bits to satisfy the following condition:

$$\text{IWDTCLK} / n \leq \text{PCLK} / 4 \quad (n = 1, 16, 32, 64, 128, 256)$$