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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A152A/E	Rev.	1.00
Title	Note on mail box register j (MBj) (j=0~31) of CAN module		Information Category	Technical Notification		
Applicable Product	RX71M Group、RX64M Group、 RX630 Group (With CAN)、	Lot No.		See below		
	RX63N Group (With CAN), RX631 Group (With CAN), RX63T Group (With CAN),	All	Reference Document			

This document describes a note on register access for mail box register j (MBj)(j=0~31) in CAN module.

[Note]

When accessing the mail box register j (MBj)(j=0~31), only 4 of 16 addresses are available for 32-bit access and only even-number addresses are available for 16-bit access. If this guidance is not followed, the expected read value cannot be obtained. Figure1 shows an example of the various read accesses to the mail box register of CAN0 with combination of 32/16/8-bit access and all kinds of address.

Whether the expected value can be obtained or not is indicated by \bigcirc and \times in Figure 1.

[Countermeasure]

When accessing the mail box register j (MBj)(j=0~31) with 32-bit access, please access to the addresses ending by 0h, 4h, 8h, Ch. Please access to even-number address when using 16-bit access.



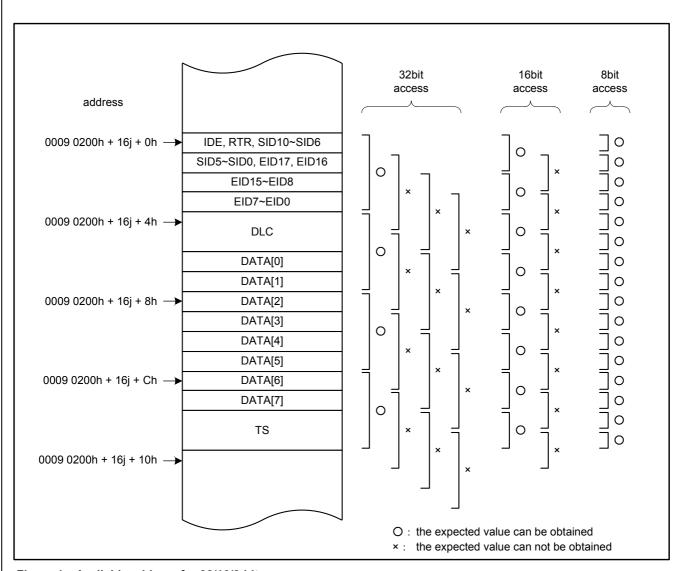


Figure.1 Available address for 32/16/8-bit access.

[Reference Document]

Series	Group	Title	Rev.	Document No.
RX700	RX71M	RX71M Group User's Manual: Hardware	1.00	R01UH0493EJ0100
RX600	RX64M	RX64M Group User's Manual: Hardware	1.00	R01UH0377EJ0100
	RX630	RX630 Group User's Manual: Hardware	1.60	R01UH0040EJ0160
	RX63N、 RX631	RX63N Group、RX631 Group User's Manual: Hardware	1.80	R01UH0041EJ0180
	RX63T	RX63T Group User's Manual: Hardware	2.20	R01UH0238EJ0220
	RX62T、 RX62G	RX62T Group、RX62G Group User's Manual: Hardware	2.00	R01UH0034EJ0200
	RX62N、 RX621	RX62N Group、RX621Group User's Manual: Hardware	1.40	R01UH0033EJ0140

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