RENESAS TECHNICAL UPDATE

1753, Shimonumabe, Nakahara-ku, Kawasaki-shi, Kanagawa 211-8668 Japan
Renesas Electronics Corporation

Product Category	MPU/MCU			Document No.	TN-V85-A018A/E	Rev.	1.00
Title	The matter about the level of output pin in System Reset.			Information Category	Technical Notification		
Applicable Product	Refer to a body.	Lot No.		Reference Document	User Manual of each product		
		All lots					

The following contents are added to the User Manual.

1. Additional contents

In the User Manual, it is mentioned that pin status is Hi-Z during and after reset. However the output pin has a possibility to become unstable at the transition moment (less than 10ns) of external and internal reset. After that, pin status will be Hi-Z.

Note. The phenomenon of unstable output period and influence of the pin level after the unstable situation, it depend on customer board and measurement conditions.

2. Target Product

Target Product Series		
V850E/IA1 Series	V850E/IA2 Series	V850E/IA3 Series
V850E/IA4 Series	V850E/IG3 Series	V850E/IF3 Series
V850E/IG4 Series	V850E/IH4 Series	V850E/Ix4-H Series
V850E/MS1 Series	V850E/MS2 Series	V850ES/Hx2 Series
V850ES/Hx3 Series	V850ES/IE2 Series	V850ES/IK1 Series
V850ES/Kx1 Series	V850ES/Kx1+ Series	V850ES/Kx2 Series
V850E/SV2 Series	V850E/MA1 Series	V850E/MA2 Series
V850E/MA3 Series	V850E/ME2 Series	V850E2/ME3 Series
V850E2/ML4 Series	V850E2/MN4 Series	V850ES/Jx2 Series
V850ES/Jx3 Series	V850ES/Jx3-L Series	V850ES/Jx3-H Series
V850ES/Jx3-E Series	V850ES/Jx3-U Series	V850ES/SA2 Series
V850ES/SA3 Series	V850ES/ST2 Series	V850ES/ST3 Series

RENESAS