

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	27 December 2001	No.	TN-EML-078A/E
THEME	Limitation on Using the SH7727 E10A Emulator		
CLASSIFICATION	<input type="checkbox"/> Spec change <input checked="" type="checkbox"/> Limitation on Use <input type="checkbox"/> Supplement of Documents		
PRODUCTNAME	SH7727 E10A Emulator Type number: HS7727KCM01H, HS7727KCM02H, HS7727KCI01H, HS7727KCM02H	Lot No. etc.	SH7727 E10A
REFERENCE DOCUMENTS		Effective Date	Permanent
		From	

In the SH7727, pins for port G, AUD, Hitachi-UDI, and PCC are multiplexed. The function of port G is determined according to the status of the /ASEMD0 (ASE mode) pin when the rising edge of RESETP is generated. The table below shows the multiplexed functions.

When the /ASEMD0 pin is low, the port function and function 1 can be used.
 When the /ASEMD0 pin is high, the port function and function 2 can be used.

Therefore, note that function 2 cannot be used when using the E10A emulator.

Table: Multiplexed Functions

Port	Port Function	Function 1	Function 2
G	PTG[5] input/output (port)	/ASEBRKAK (Hitachi-UDI)	-
G	PTG[3] input/output (port)	AUDATA[3] (AUD)	PCC0BVD2 (PCC)
G	PTG[2] input/output (port)	AUDATA[2] (AUD)	PCC0BVD1 (PCC)
G	PTG[1] input/output (port)	AUDATA[1] (AUD)	/PCC0CD2 (PCC)
G	PTG[0] input/output (port)	AUDATA[0] (AUD)	/PCC0CD1 (PCC)