

# RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-SH7-A818A/E	Rev.	1.00
Title	The error correction and addition about LCD controller (LCDC)		Information Category	Technical Notification		
Applicable Product	SH7760 Group	Lot No.	Reference Document	SH7760 Group Hardware Manual Rev.2.00 Feb. 12, 2010 (REJ09B0554-0200)		
		All lots				

We would like to inform you of the following error correction and addition to the description of the LCD controller (LCDC) in the applicable products.

## 1. 30.4.6 Power-Supply Control Sequence Processing, Paragraph 2

[Error]

Figures 30.4 to 30.7 are summary timing charts for power-supply control sequences and table 30.5 is a summary of available power-supply control sequence periods.

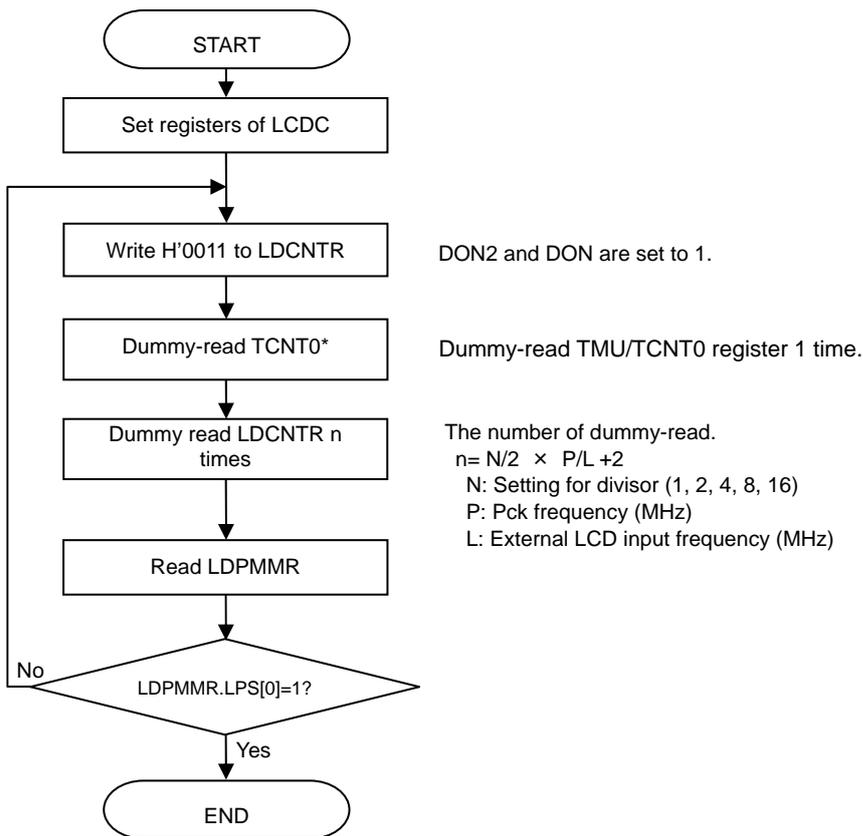
[Correction]

Figure 30.4 gives the flowcharts for power-supply control sequences, figures 30.5 to 30.8 are timing charts that show outlines of power-supply control sequences, and table 30.5 is a summary of available power-supply control sequence periods .

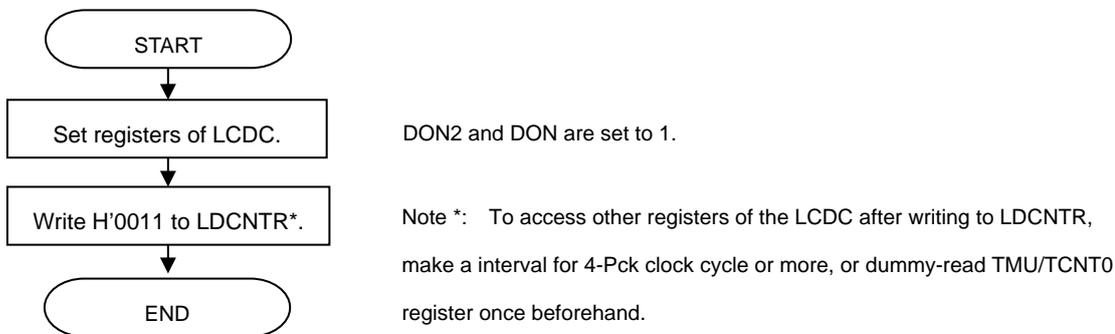
## 2. Added Figure 30.4

Figure 30.4 to 30.22 are re-numbered to Figure 30.5 to 30.23 respectively, then add new Figure 30.4.

(1) Power-on sequence (when LCD\_CLK is selected as the input clock)



(2) Power-on sequence (when the peripheral clock or bus clock is selected as the input clock)



(3) Power-off sequence

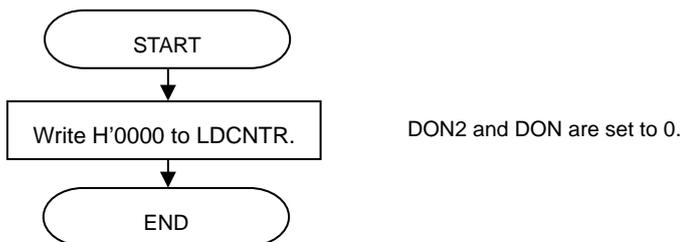


Figure 30.4 Flowchart for Power-Supply Control Sequences