

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-78K-A003A/E	Rev.	1.00
Title	Document error correction 78K0R/KC3-L, 78K0R/KE3-L (On-Chip USB Controller) User's Manual: Hardware Rev.2.01		Information Category	Technical Notification		
Applicable Product	78K0R/KC3-L, 78K0R/KE3-L (On-Chip USB Controller) UPD78F1022, UPD78F1023, UPD78F1024, UPD78F1025, UPD78F1026	Lot No.	Reference Document	78K0R/KC3-L, 78K0R/KE3-L (On-Chip USB Controller) User's Manual: Hardware Rev.2.01 (R01UH0019EJ0201)		
		all lots				

This is to inform you that the User's manual has clerical errors following. These errors in the User's manual will be corrected in the next revision.

Correction information

1. CHAPTER 16, Figure 16-2 (P.781)

(Wrong description)

Address: FFFD1H After reset: 00H R/W

Symbol	7	6	<5>	<4>	<3>	<2>	<1>	<0>
IF2H	RSUMIF	USBIF	0	SREIF2 ^{Note}	0	0	0	PIF11 ^{Note}

(Corrected description)

Address: FFFD1H After reset: 00H R/W

Symbol	7	<6>	<5>	<4>	3	2	1	<0>
IF2H	RSUMIF 0	USBIF RSUMIF	0 USBIF	SREIF2 ^{Note}	0	0	0	PIF11 ^{Note}

2. CHAPTER 16, Figure 16-3 (P.783)

(Wrong description)

Address: FFFE4H After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
MK0L	PMK5	PMK4	0	PMK2	PMK1	PMK0	LVIMK	WDTIMK

Address: FFFE7H After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
MK1H	TMMK04	0	PMK6	STMK2 ^{Note} CSIMK20 IICMK20	KRMK	RTCIMK	RTCMK	ADMK

Address: FFFD4H After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
MK2L	PMK10 ^{Note}	PMK9 ^{Note}	PMK8 ^{Note}	0	SRMK2	TMMK07	TMMK06	TMMK05

Address: FFFD5H After reset: FFH R/W

Symbol	7	6	<5>	<4>	<3>	<2>	<1>	<0>
MK2H	0	RSUMMK	USBMK	SREMK2 ^{Note}	0	0	0	PMK11 ^{Note}

(Corrected description)

Address: FFFE4H After reset: FFH R/W

Symbol	<7>	<6>	5	<4>	<3>	<2>	<1>	<0>
MK0L	PMK5	PMK4	0 1	PMK2	PMK1	PMK0	LVIMK	WDTIMK

Address: FFFE7H After reset: FFH R/W

Symbol	<7>	6	<5>	<4>	<3>	<2>	<1>	<0>
MK1H	TMMK04	0 1	PMK6	STMK2 ^{Note} CSIMK20 IICMK20	KRMK	RTCIMK	RTCMK	ADMK

Address: FFFD4H After reset: FFH R/W

Symbol	<7>	<6>	<5>	4	<3>	<2>	<1>	<0>
MK2L	PMK10 ^{Note}	PMK9 ^{Note}	PMK8 ^{Note}	0 1	SRMK2	TMMK07	TMMK06	TMMK05

Address: FFFD5H After reset: FFH R/W

Symbol	7	<6>	<5>	<4>	3	2	1	<0>
MK2H	0 1	RSUMMK	USBMK	SREMK2 ^{Note}	0 1	0 1	0 1	PMK11 ^{Note}

3. CHAPTER 16, Figure 16-4 (P.784)

(Wrong description)

Address: FFFE8H After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
PR00L	PPR05	PPR04	0	PPR02	PPR01	PPR00	LVIPR0	WDTIPR0

Address: FFFECH After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
PR10L	PPR15	PPR14	0	PPR12	PPR11	PPR10	LVIPR1	WDTIPR1

(Corrected description)

Address: FFFE8H After reset: FFH R/W

Symbol	<7>	<6>	5	<4>	<3>	<2>	<1>	<0>
PR00L	PPR05	PPR04	0 1	PPR02	PPR01	PPR00	LVIPR0	WDTIPR0

Address: FFFECH After reset: FFH R/W

Symbol	<7>	<6>	5	<4>	<3>	<2>	<1>	<0>
PR10L	PPR15	PPR14	0 1	PPR12	PPR11	PPR10	LVIPR1	WDTIPR1

3. CHAPTER 16, Figure 16-4 (P.785)

(Wrong description)

Address: FFFEBH After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
PR01H	TMPR004	0	PPR06	STPR02 ^{Note} CSIPR020 IICPR020	KRPR0	RTCIPR0	RTCPR0	ADPR0

Address: FFFD8H After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
PR02L	PPR010 ^{Note}	PPR09 ^{Note}	PPR08 ^{Note}	0	PPR06	TMPR007	TMPR006	TMPR005

Address: FFFDCH After reset: FFH R/W

Symbol	<7>	<6>	<5>	<4>	<3>	<2>	<1>	<0>
PR12L	PPR110 ^{Note}	PPR19 ^{Note}	PPR18 ^{Note}	PPR17	PPR16	TMPR107	TMPR106	TMPR105

Address: FFFD9H After reset: FFH R/W

Symbol	7	6	<5>	<4>	<3>	<2>	<1>	<0>
PR02H	0	RSUMPR0	USBPR0	SREPR02 ^{Note}	0	0	0	PPR011 ^{Note}

Address: FFFDDH After reset: FFH R/W

Symbol	7	6	<5>	<4>	<3>	<2>	<1>	<0>
PR12H	0	RSUMPR1	USBPR1	SREPR12 ^{Note}	0	0	0	PPR111 ^{Note}

(Corrected description)

Address: FFFEBH After reset: FFH R/W

Symbol	<7>	6	<5>	<4>	<3>	<2>	<1>	<0>
PR01H	TMPR004	0 1	PPR06	STPR02 ^{Note} CSIPR020 IICPR020	KRPR0	RTCIPR0	RTCPR0	ADPR0

Address: FFFD8H After reset: FFH R/W

Symbol	<7>	<6>	<5>	4	<3>	<2>	<1>	<0>
PR02L	PPR010 ^{Note}	PPR09 ^{Note}	PPR08 ^{Note}	0 1	PPR06	TMPR007	TMPR006	TMPR005

Address: FFFDCH After reset: FFH R/W

Symbol	<7>	<6>	<5>	4	<3>	<2>	<1>	<0>
PR12L	PPR110 ^{Note}	PPR19 ^{Note}	PPR18 ^{Note}	PPR17 1	PPR16	TMPR107	TMPR106	TMPR105

Address: FFFD9H After reset: FFH R/W

Symbol	7	<6>	<5>	<4>	3	2	1	<0>
PR02H	0 1	RSUMPR0	USBPR0	SREPR02 Note	0 1	0 1	0 1	PPR011 ^{Note}

Address: FFFDDH After reset: FFH R/W

Symbol	7	<6>	<5>	<4>	3	2	1	<0>
PR12H	0 1	RSUMPR1	USBPR1	SREPR12 Note	0 1	0 1	0 1	PPR111 ^{Note}