Microcomputer Technical Information

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		Document No.	ZBG	-CD-04-0045	1/2
DF703107		Date issued	July 26, 2004		
Device File for V850 Series V850E/MA1		Issued by	Development Tool Group		
			Multipurpose Microcomputer Systems Division		
Upgrade			3rd Systems Operations Unit		
			NEC Electronics Corporation		
Related	Device File DF703107 (V1.21) User's	Document		Usage restriction	
documents	Manual: ZUD-CD-04-0025	classification	\checkmark	Upgrade	
	DF703107 Device File for V850E/MA1			Document modification	
	Usage Restrictions: ZBG-CD-04-0028			Other notification	

1. Product types and version

Product	Outline	Old Version	New Version
μSxx13DF703107	Device file DF703107 for V850 Series V850E/MA1	V1.20	V1.21
Product	Outline	Target Version	
μSxx17SP850 V850 Series software package SP850		V2.00 or later	

Note xx indicates AB or BB.

2. Details of upgrade

The major revisions in DF703107 V1.21 are shown below. For details, refer to "Device File DF703107 (V1.21) User's Manual (ZUD-CD-04-0025)" supplied with the product.

(1) The following peripheral I/O register bit access attributes have been modified.

ADM1 (0x0fffff201)	1-/8-bit, R/W ^{Note}	\rightarrow	8-bit, R/W ^{Note}
DTOC (0x0fffff8a0)	8-bit, R/W ^{Note}	\rightarrow	1-/8-bit, R/W ^{Note}

- (2) Reading the following bit registers of the peripheral I/O registers is now prohibited.
 E00, STG0, INT0, MLE0, TC0, E11, STG1, INT1, MLE1, TC1,
 E22, STG2, INT2, MLE2, TC2, E33, STG3, INT3, MLE3, TC3
- (3) The following peripheral I/O register bit access attributes have been modified.
 - $\mathsf{C0} \qquad \mathsf{E}^{\mathsf{Note}} \to \mathsf{R}^{\mathsf{Note}}$
 - $\mathsf{TC1} \quad \mathsf{E}^{\mathsf{Note}} \to \mathsf{R}^{\mathsf{Note}}$
 - $\mathsf{TC2} \quad \mathsf{E}^{\mathsf{Note}} \to \mathsf{R}^{\mathsf{Note}}$
 - $\mathsf{TC3} \qquad \mathsf{E}^{\mathsf{Note}} \to \mathsf{R}^{\mathsf{Note}}$

Note E: Read/Write, R: Read only, W: Write only

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(4) The bit reserved words of the following peripheral I/O registers have been added.

TCO0 (bit 0 of DTOC), TCO1 (bit 1 of DTOC), TCO2 (bit 2 of DTOC), TCO3 (bit 3 of DTOC)

(5) The following peripheral I/O register bit access attributes have been modified.

IMR3L	$RRRRRRRE^{Note} \rightarrow$	EEEEEEEE ^{Note}
IMR3H	$RRRRRRE^{Note} \rightarrow$	EEEEEEE

- $\mathsf{BCP} \qquad \mathsf{0000EEEE}^{\mathsf{Note}} \rightarrow \mathsf{E000E000}^{\mathsf{Note}}$
 - **Note** The attributes are read from left to right in the order bit 7, bit 6, bit 5, ... bit 0. E: Read/Write, R: Read only, W: Write only, 0: Fixed to 0

3. How to upgrade

(1) µSxx13DF703107

• Free upgrade (without supply medium)

Download the software through ODS (online delivery service). When using this method, the supply medium will not be delivered but the upgrade is free of charge.

Note Access the following URL.

http://www.necel.com/micro/ods/eng/ (µSBB13DF703107; English version) http://www.necel.com/micro/ods/jpn/ (µSAB13DF703107; Japanese version)

(2) SP850

• Free upgrade (without supply medium)

Download the upgraded software (DF703107) through ODS (online delivery service). When using this method, the supply medium will not be delivered but the upgrade is free of charge.

Note Access the following URL.

http://www.necel.com/micro/ods/eng/ (μSBB17SP850; Windows English version) http://www.necel.com/micro/ods/jpn/ (μSAB17SP850; Windows Japanese version)

- 4. Upgrade start date
 - Free upgrade (Windows version, without supply medium)

The upgraded product can be downloaded through ODS from July 20, 2004.

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