

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A056A/E	Rev.	1.00
Title	Corrections to Cases where Registers SCKCR, SCKCR3, BCKCR, and OPCCR Should Not Be Modified in the RX210 Group		Information Category	Technical Notification		
Applicable Product	RX210 Group	Lot No.	Reference Document	RX210 Group User's Manual: Hardware Rev.1.30 (R01UH0037EJ0130)		
		All				

This document describes corrections to cases where registers SCKCR, SCKCR3, BCKCR, and OPCCR should not be modified in RX210 Group User's Manual: Hardware Rev.1.30.

Before correction

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- The ROM P/E mode entry i bit in the flash P/E mode entry register (FENTRYR.FENTRYi) is 1 (ROM P/E mode) (i = 0, 1)

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- **When** the ROM P/E mode entry i bit in the flash P/E mode entry register (FENTRYR.FENTRYi) is 1 (ROM P/E mode) (i = 0, 1)

Corrections

- The ROM P/E mode entry i bit in the flash P/E mode entry register (FENTRYR.FENTRYi) is 1 (ROM P/E mode, **E2 DataFlash P/E mode**) (i = 0, 1, D)