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RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RX*-A019A/E	Rev.	1.00
Title	The correction of errors in inquiry/selection host command wait state of boot mode		Information Category	Technical Notification		
		Lot No.				
Applicable Product	RX62T Group	All lots	Reference Document	RX62T Group User's Manual: Hard (R01UH0034EJ0130)		lardware

In the applicable products mentioned above, there are some errors in the User's Manual: Hardware, regarding the description, (12) Programming/Erasure State Transition on section 31.9.5.

Correction in the Manuals

The description on section 19.3.5, Inquiry/Selection Host Command Wait State, has been revised. The revision is indicated by the red letters below.

[After correction]

(11) Programming/Erasure State Transition

In response to a programming/erasure state transition command sent from the host, the RX62T determines whether ID code protection is enabled or disabled using the control code and ID code written in the ROM. When ID code protection is enabled, the RX62T returns a response (16h) and waits for the ID code. When ID code protection is disabled, the RX62T erases the entire area of each of the user mat and data mat. After completing erasure, the RX62T returns a response (26h) and waits for a programming/erasure host command. If the RX62T has failed to complete erasure due to an error, it returns an error response (sends C0h and 51h in that order).

Do not issue a programming/erasure state transition command before device selection, clock mode selection, and new bit rate selection commands.

Command	40h	
Response	ACK	
Error response	C0h	51h

ACK (1 byte): ACK code

26h: ID code protection is disabled

16h: ID code protection is enabled

