# SLG7RI

GreenPAK™ Reset IC Standard Designs

## Description

SLG7RI is a collection of Renesas GreenPAK reset IC standard designs. Each design provides a pre-configured implementation for reset generation, reset routing, startup latching, or related system-reset control.

The collection covers simple reset-signal functions as well as reset logic with latches, multiplexing, sequencing, and interface-specific control.

Every SLG7RI standard design can be further customized to meet specific customer requirements, including reset timing, polarity, logic conditions, signal routing, and package selection.

Full datasheets for individual standard designs are available upon request. Contact greenpak@renesas.com.

## Features

- Pre-configured reset and startup-control designs
- Reset generation, routing, latching, and multiplexing options
- Programmable timing and logic behavior
- Compact package options
- Integration of reset with related system-control logic
- Ready for evaluation and further customization
- Ultra-low standby power and compact package options
- Fully customizable to exact customer supervision requirements

## Applications

- Processor and platform reset control
- PCIe and JTAG reset functions
- Power-on reset and startup latching
- Embedded and industrial system reset

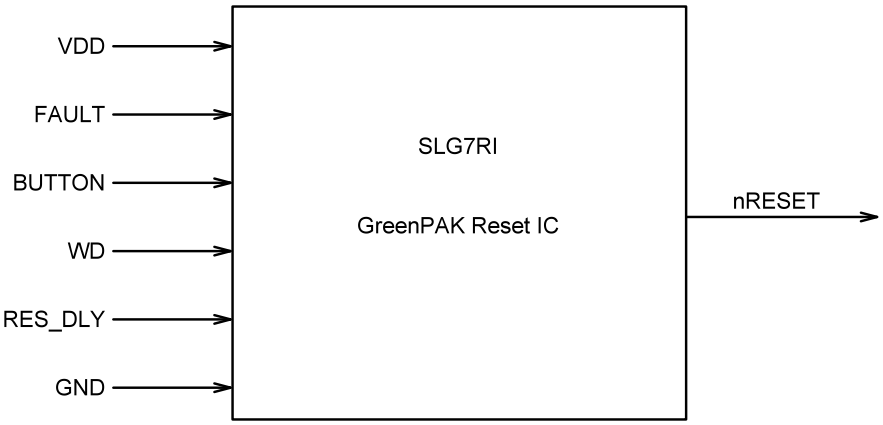


**Figure 1. SLG7RI Reset IC – Typical Application Block Diagram**

The complete, up-to-date list of available standard designs is published in the Product Options section of the SLG7RI product page. Each design is ready for evaluation and can be further customized to meet specific customer requirements. Individual datasheets are available on request via greenpak@renesas.com.