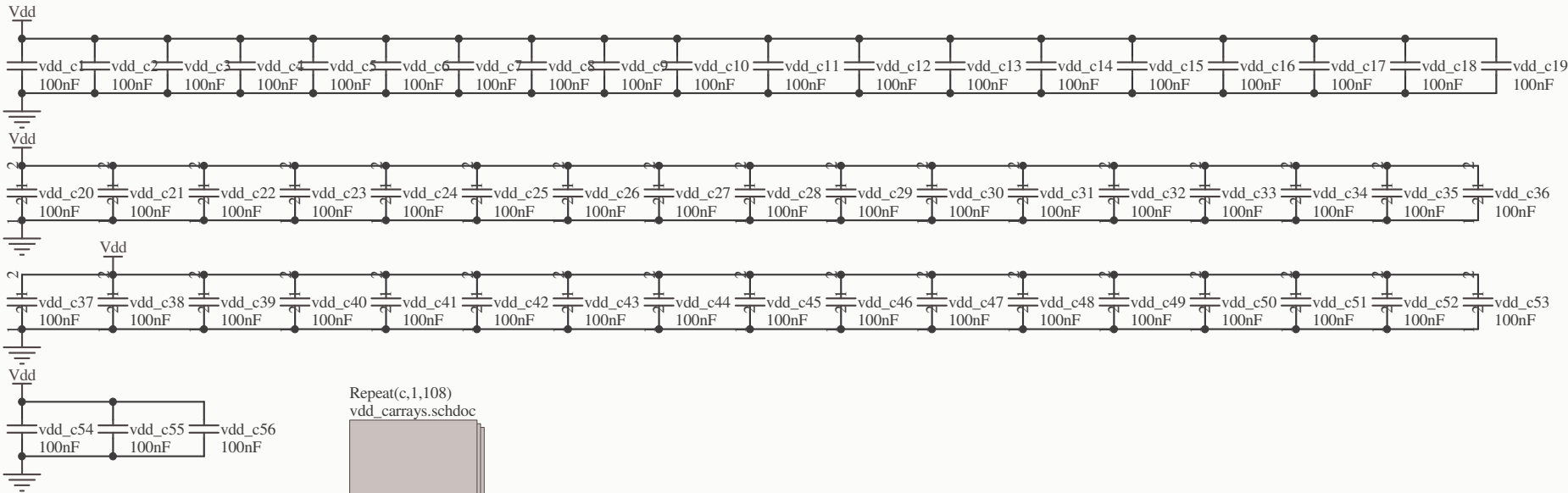


A

A

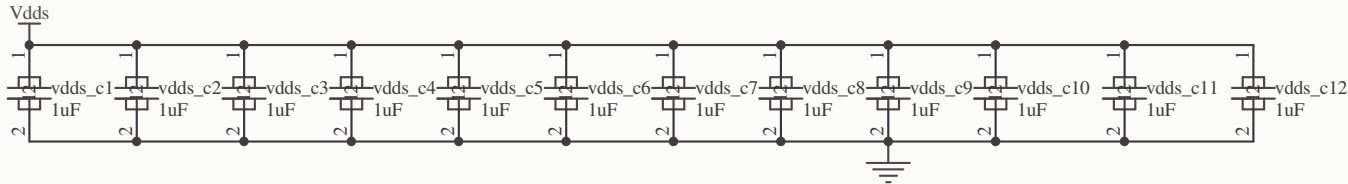


B

B

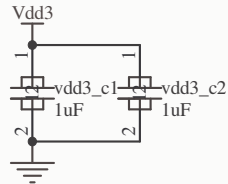
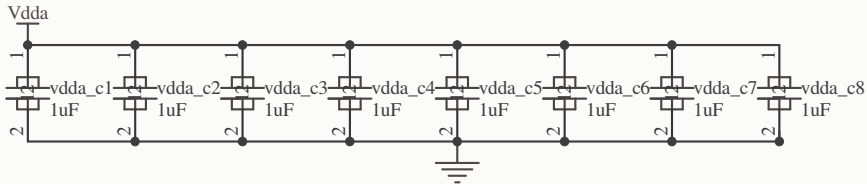
C

C

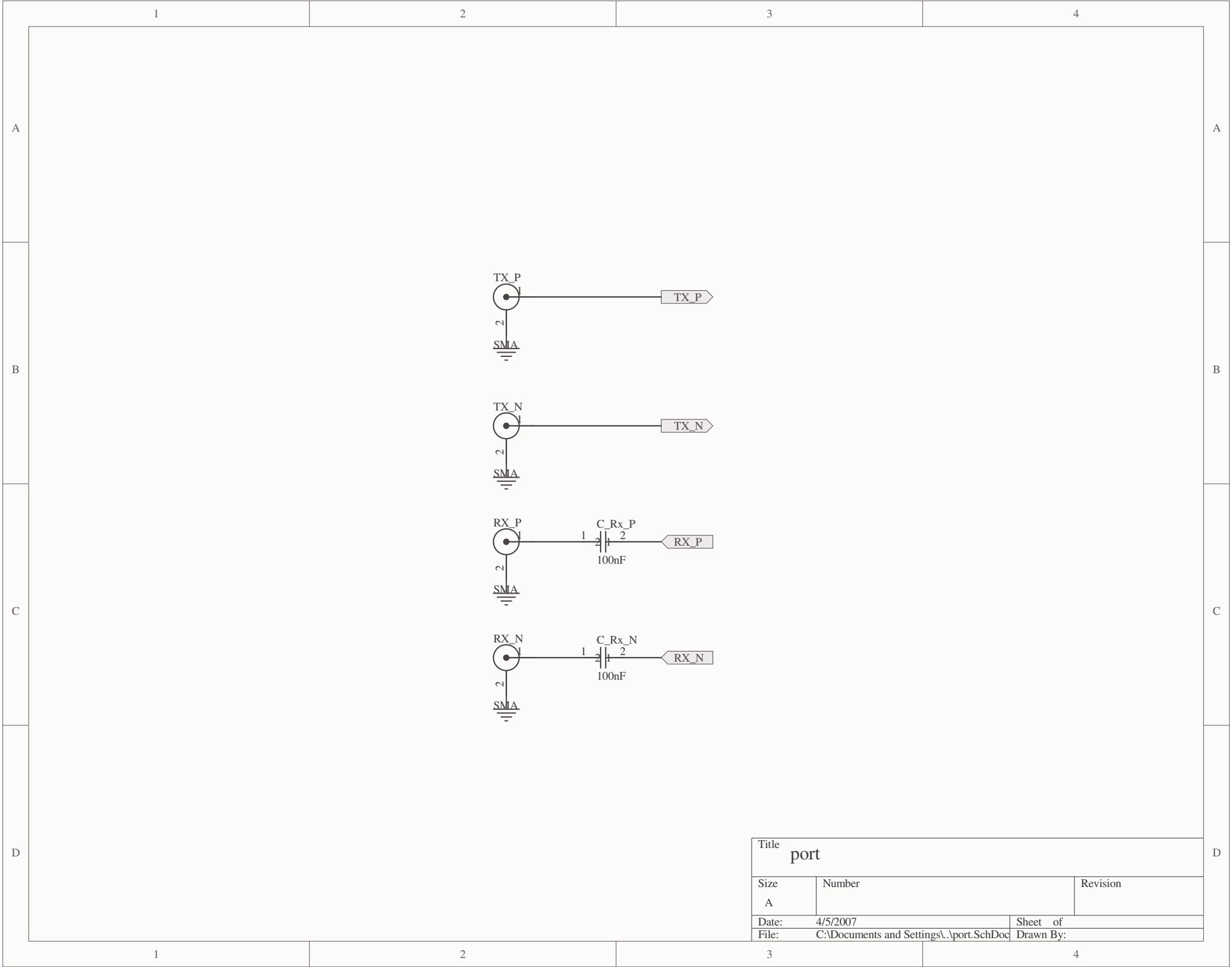


D

D



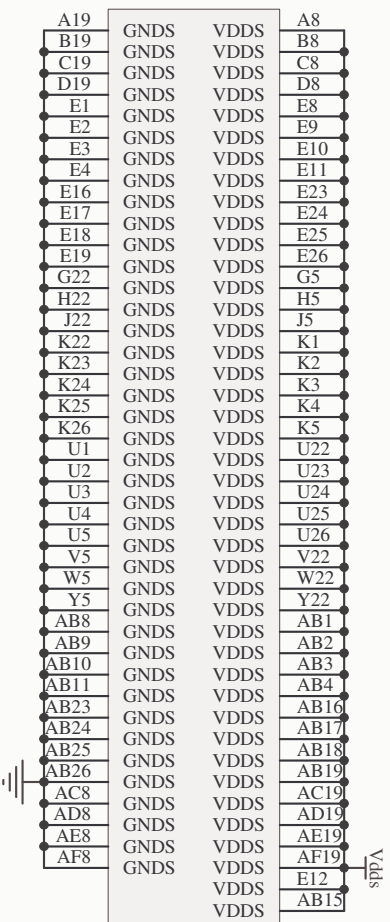
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|------------------------|--|------------|
| Board_Caps_and_Jumpers |  |            |
| Size                   | Number                                   | Revision   |
| A                      |  |            |
| Date:                  | 4/5/2007                                 | Sheet of   |
| File:                  | C:\Documents and Settings\...\Caps_jumps | SDH006 By: |



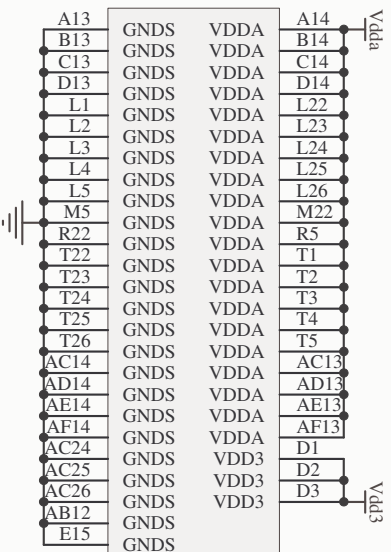
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| Size<br>A  | Number    | Revision |
| Date:<br>4/5/2007                                | Sheet of  |          |
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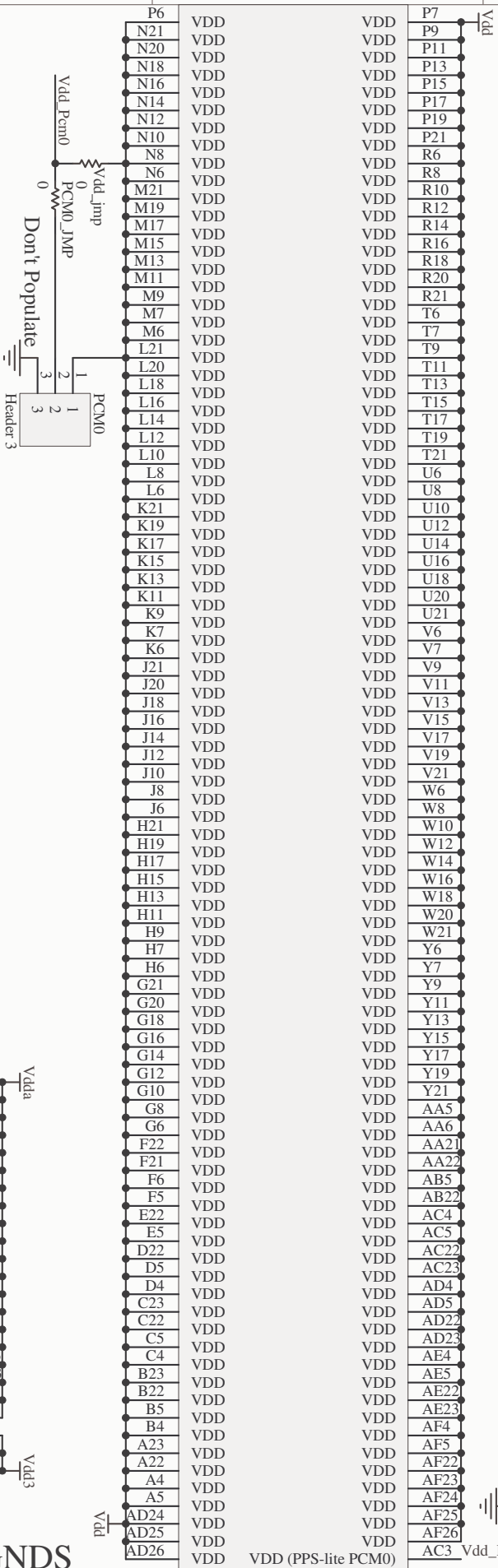




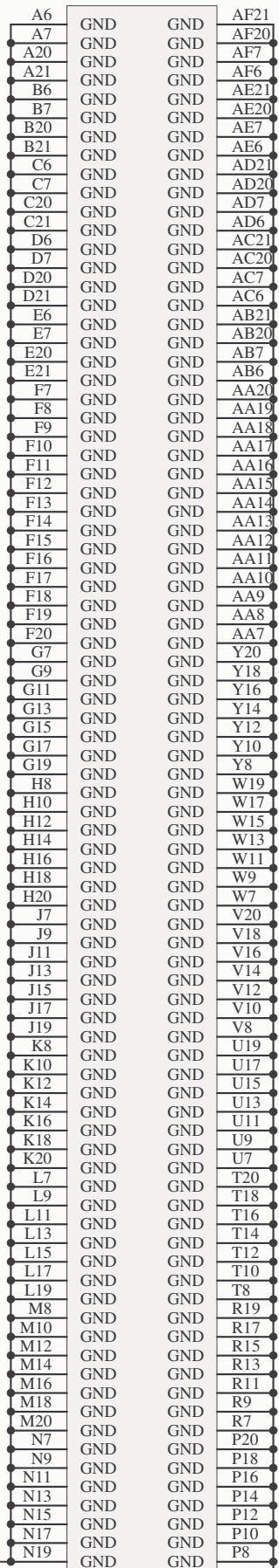
## VDDS & GNDS

U11  
PPS

## VDD3, VDDA, & GNDS

U1J  
PPS

## VDD

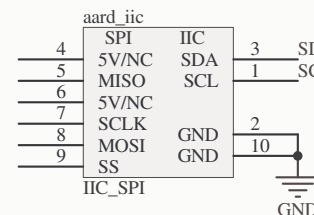
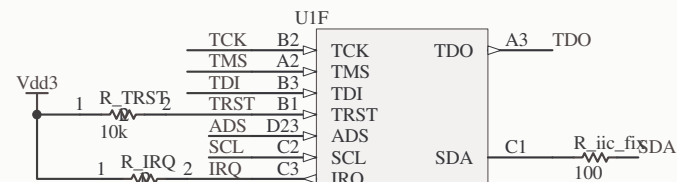
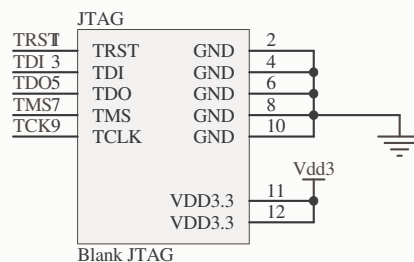
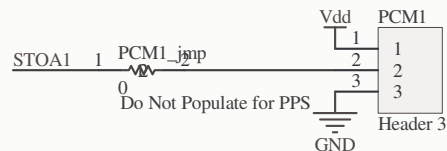
U1K  
PPS

GND

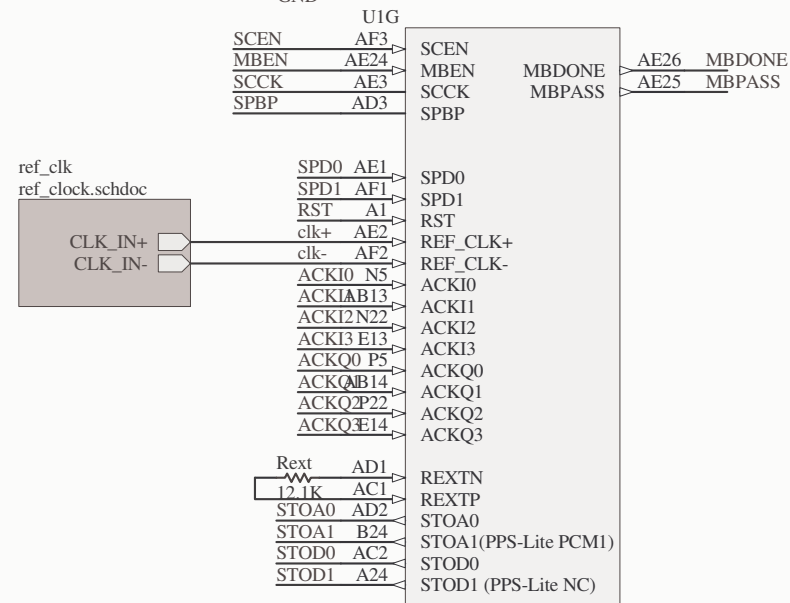
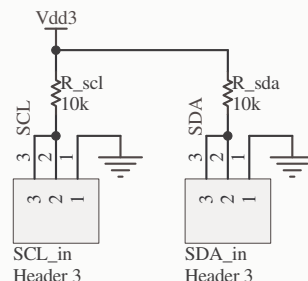
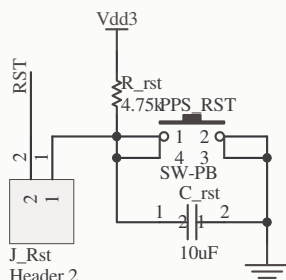
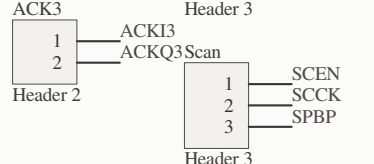
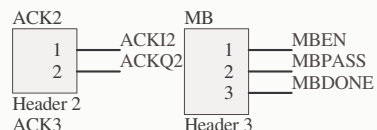
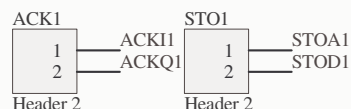
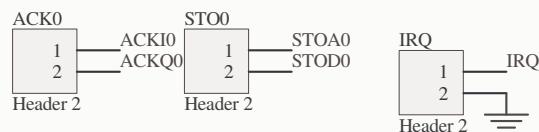
U1H  
PPS

|   |          |          |
|---|----------|----------|
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| Size  | Number   | Revision |
| A   |          |          |
| Date:   | 4/5/2007 | Sheet of |
| File: C:\Documents and Settings\pps_gnd_vdd\BOM.DOC |          |          |

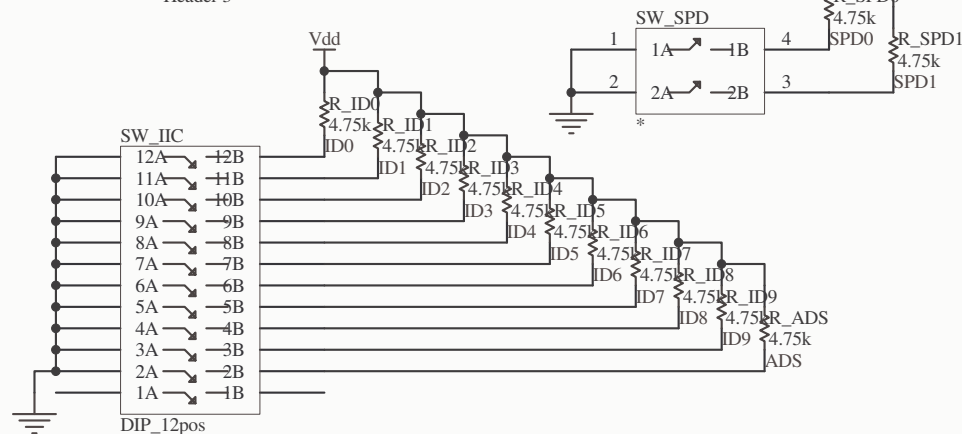
For customer applications, these pins should be connected as specified in the IDT70K2000/IDT80KSW0001 datasheet.



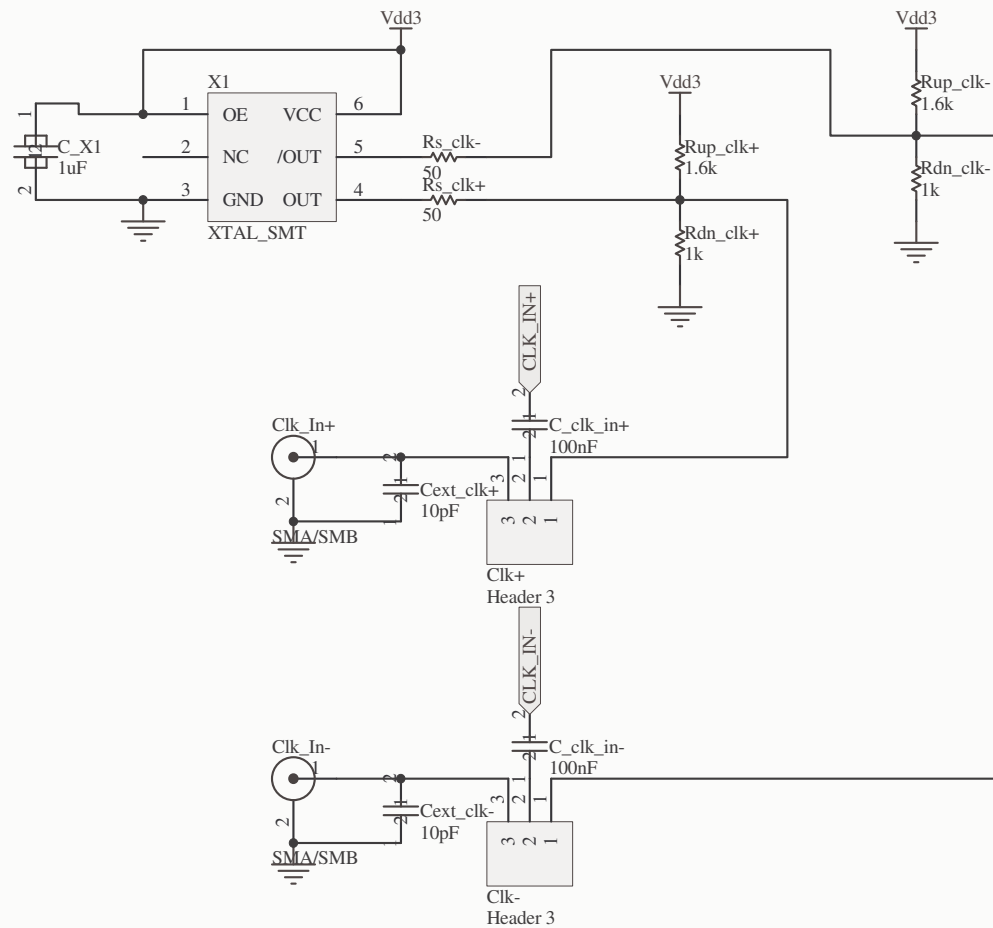
## PPS



## PPS

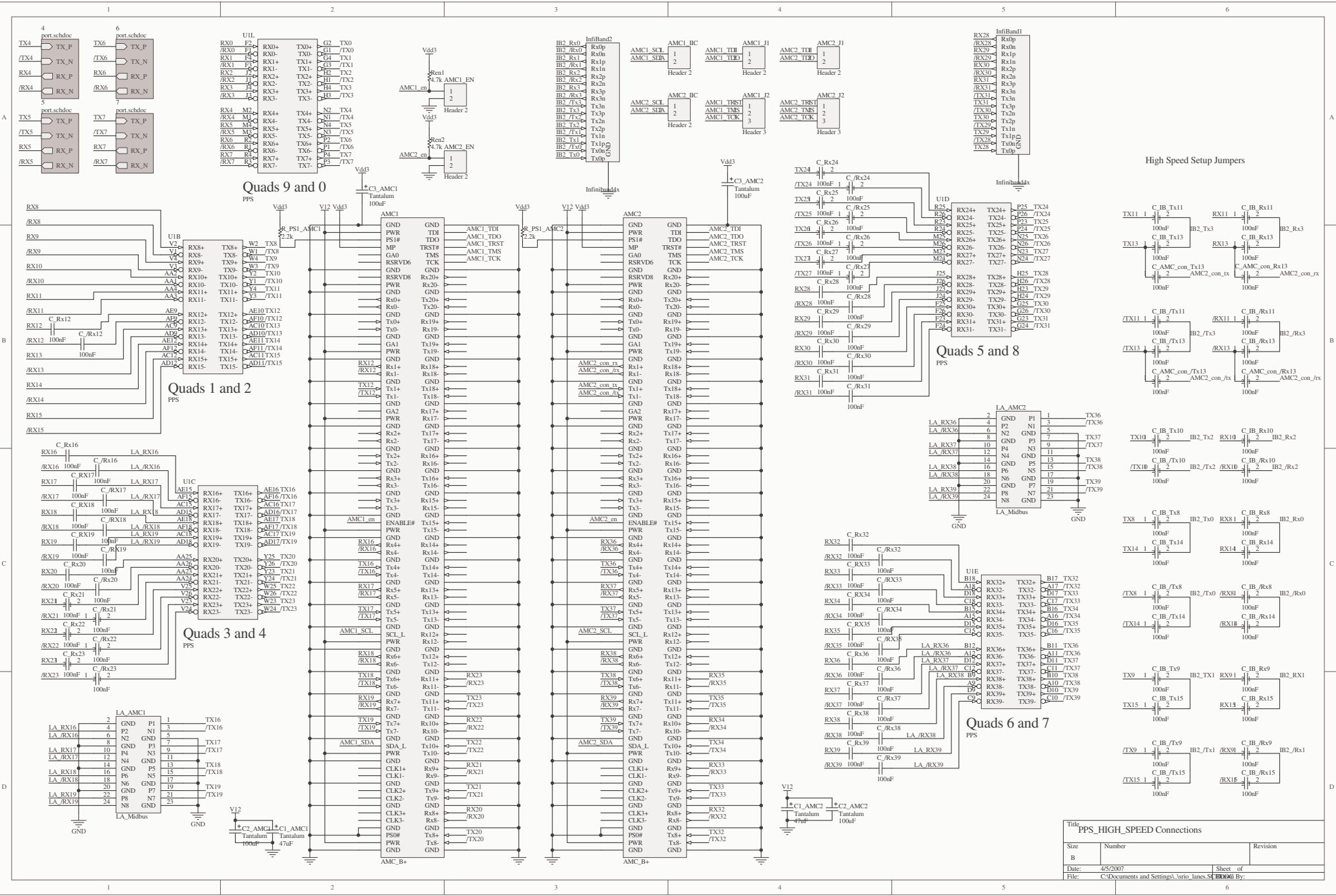


|           |   |          |
|-----------|---|----------|
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| PPS_Other |   |          |
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| Date:     | 4/5/2007  | Sheet of |
| File:     | C:\Documents and Settings\... Apps other.SCHOO... By: |          |



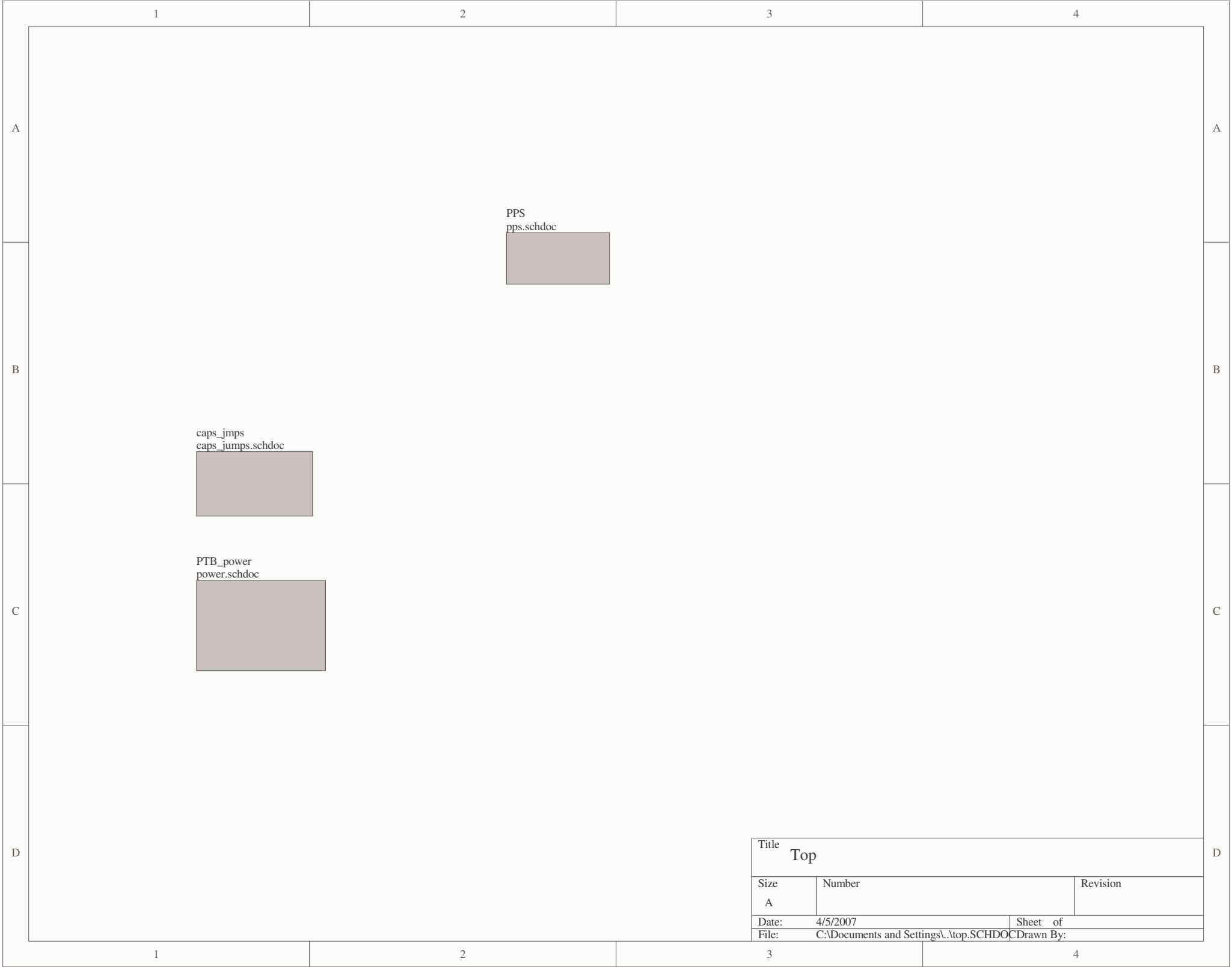
Clk Resistors for PECL oscillator.  
For LVDS oscillator, do not populate Rup/Rdn  
and replace Rs with 0 Ohm jumper

|           |  |           |
|-----------|--|-----------|
| Title     |  |           |
| Ref_Clock |  |           |
| Size      | Number                                       | Revision  |
| A         |  |           |
| Date:     | 4/5/2007                                     | Sheet of  |
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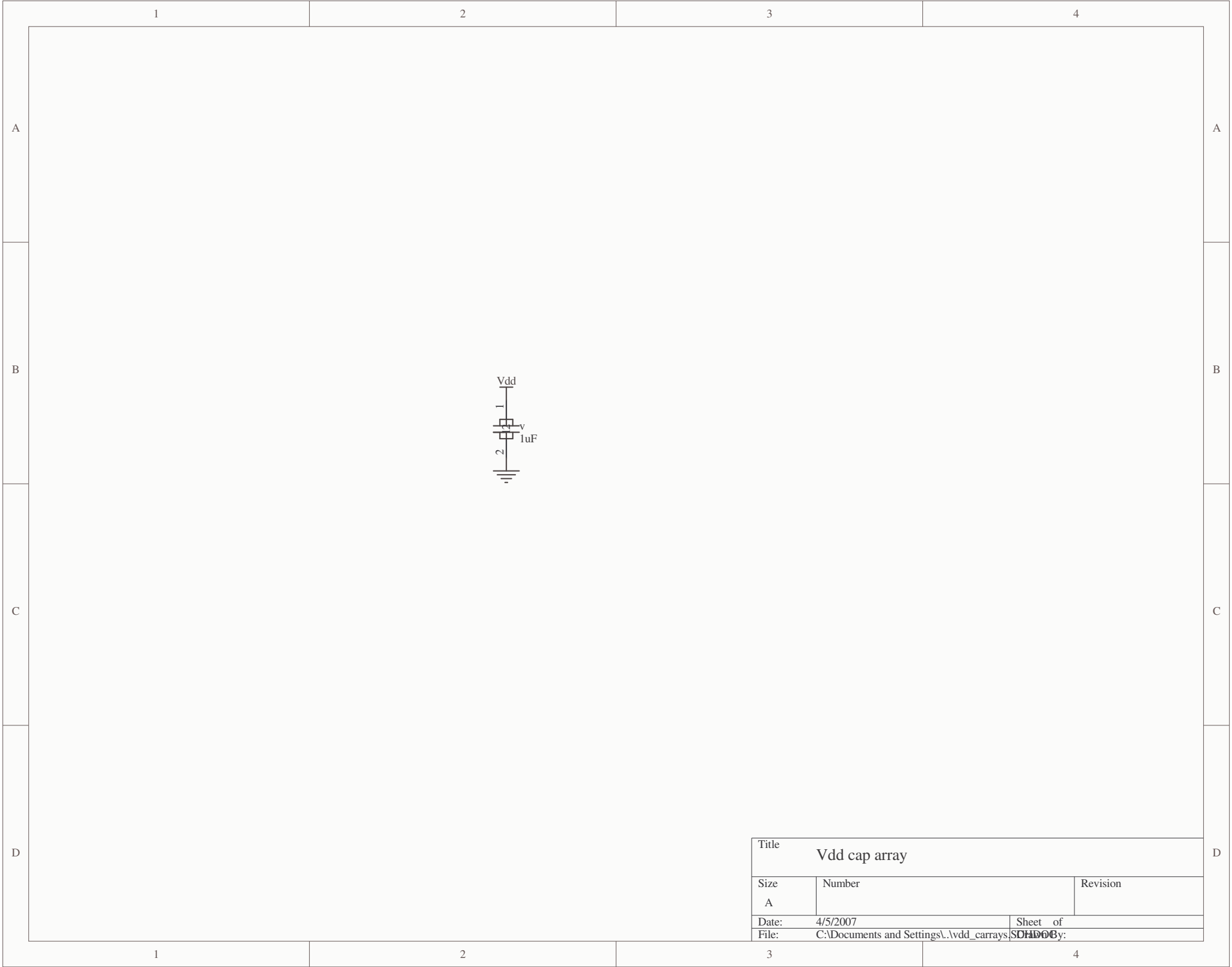


| Title<br>PPS_HIGH_SPEED Connections |   |          |
|-------------------------------------|---|----------|
| Size<br>B                           | Number  | Revision |
| Date:                               | 4/5/2007  | Sheet of |
| File:                               | C:\Documents and Settings\jario_lanes\My Documents\PPS_HIGH_SPEED Connections By: [Signature] |          |





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|-------|--|--|-----------|----|
| Title |  |  | Top       |    |
| Size  | Number                                   |  | Revision  |    |
| A     |  |  |           |    |
| Date: | 4/5/2007                                 |  | Sheet     | of |
| File: | C:\Documents and Settings\...\top.SCHDOC |  | Drawn By: |    |



|       |   |  |               |    |
|-------|---|--|---------------|----|
| Title |   |  | Vdd cap array |    |
| Size  | Number  |  | Revision      |    |
| A     |   |  |               |    |
| Date: | 4/5/2007  |  | Sheet         | of |
| File: | C:\Documents and Settings\..\.vdd_carrrays\SDH003 |  | Drawn By:     |    |