

R8C/LAxA Group

Current Consumption

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Prepared on May 12, 2011

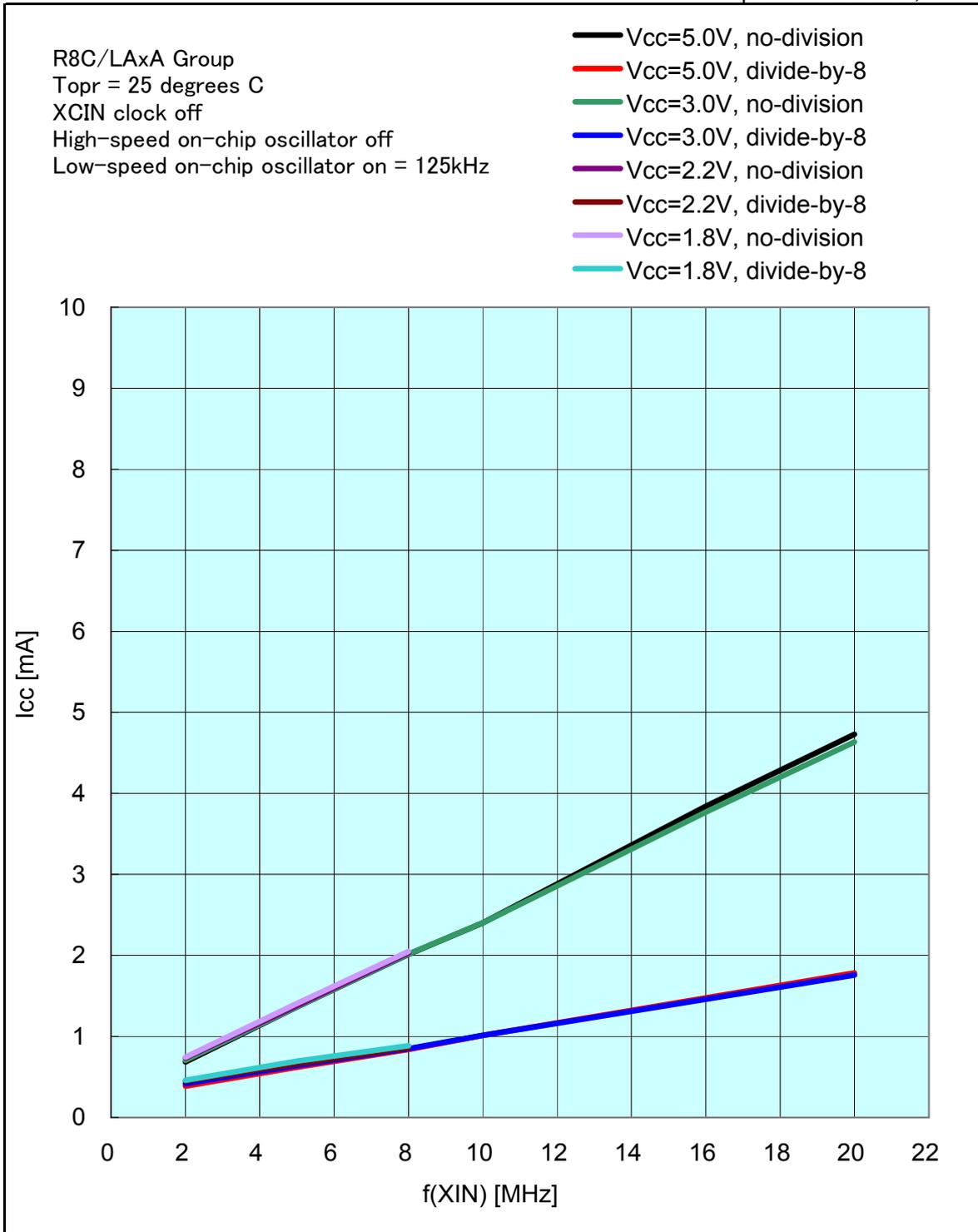
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Icc VS f(XIN)

(High-speed clock mode)

Topr =25 degrees C

Prepared on Nov. 30, 2010



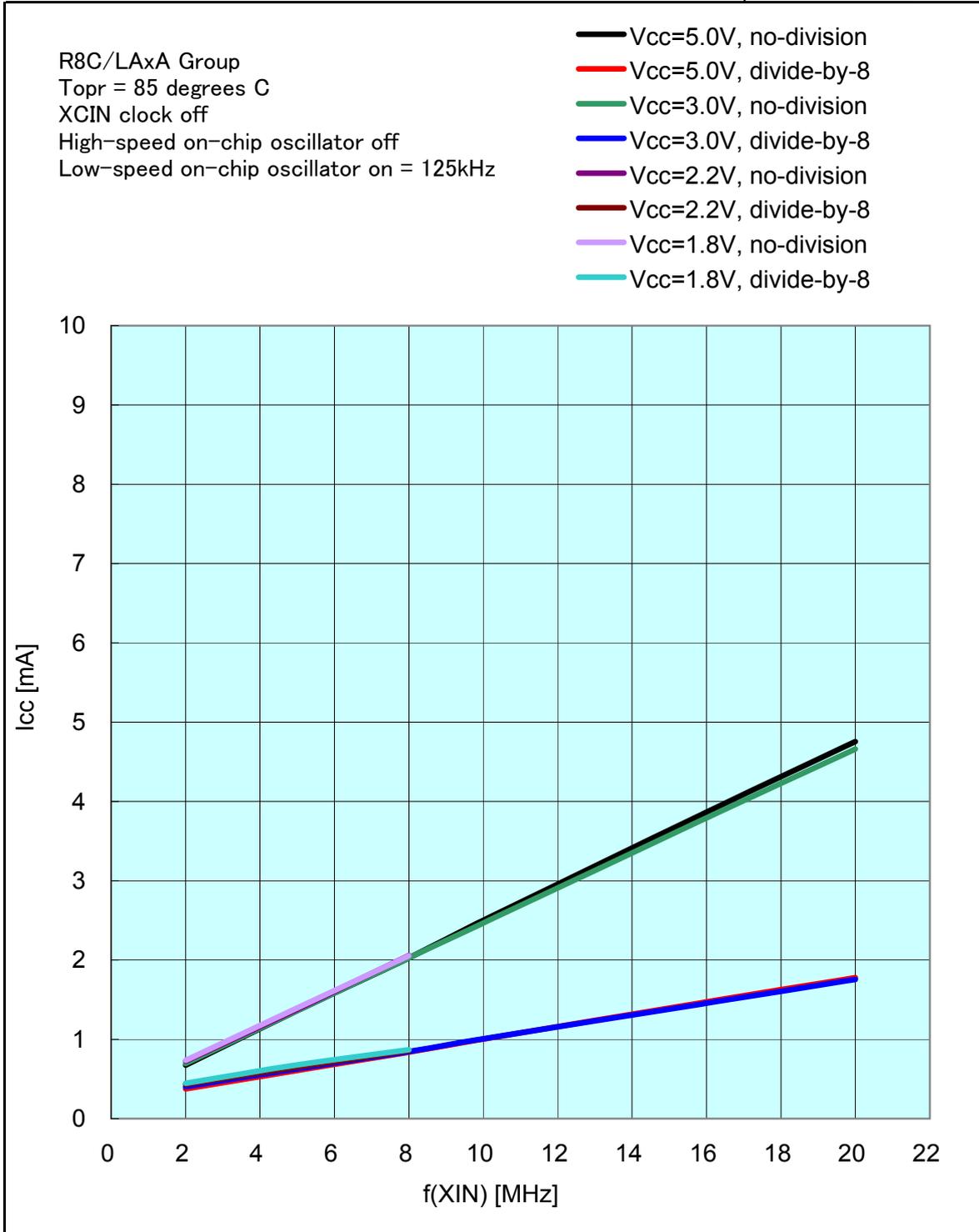
The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics

I_{cc} VS f(XIN)

(High-speed clock mode)

T_{opr} = 85 degrees C

Prepared on Nov. 30, 2010



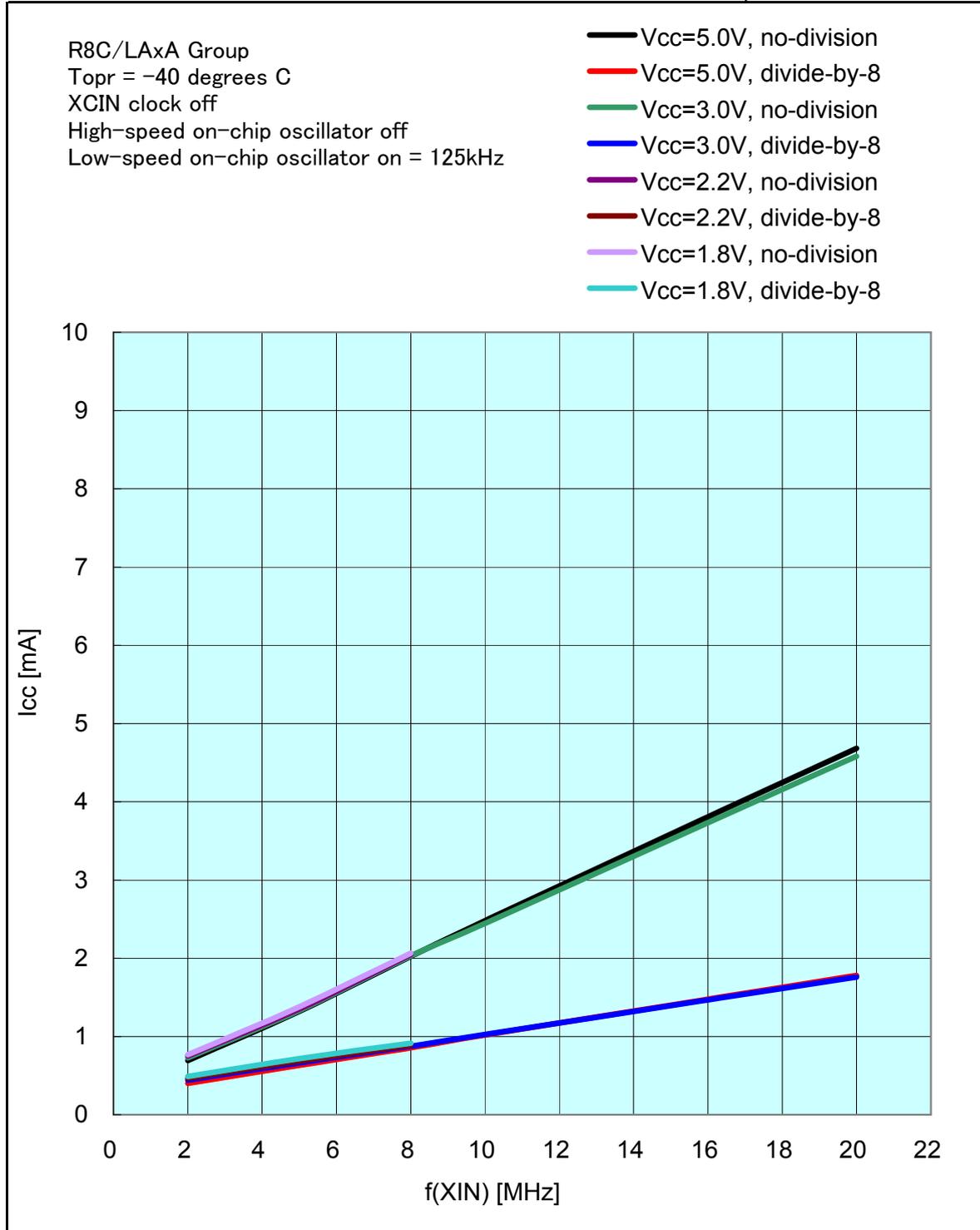
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Icc vs f(XIN)

(High-speed clock mode)

Topr = -40 degrees C

Prepared on Nov. 30, 2010



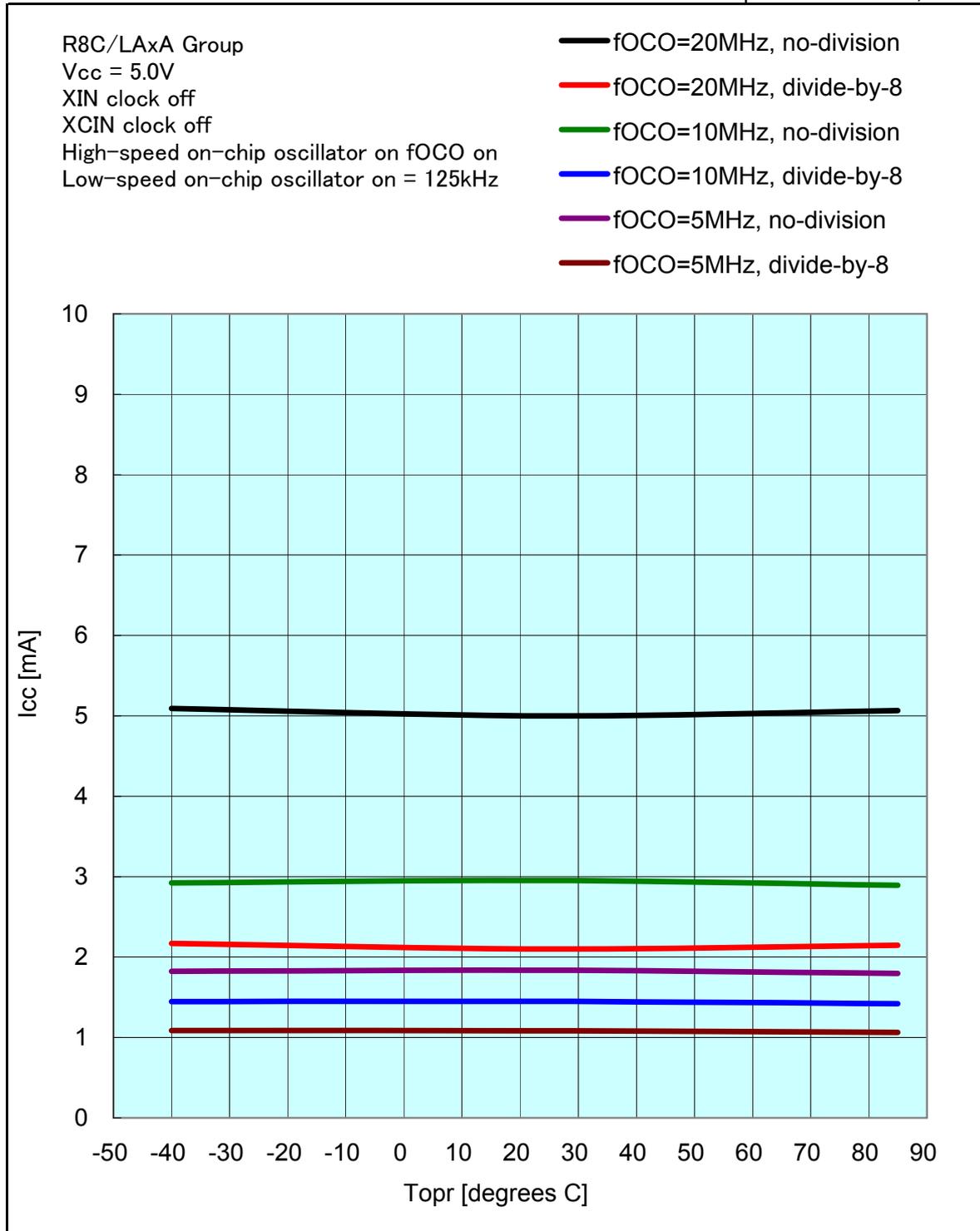
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Icc VS Topr

(High-speed on-chip oscillator mode on fOCO)

Vcc = 5.0V

Prepared on Nov. 30, 2010



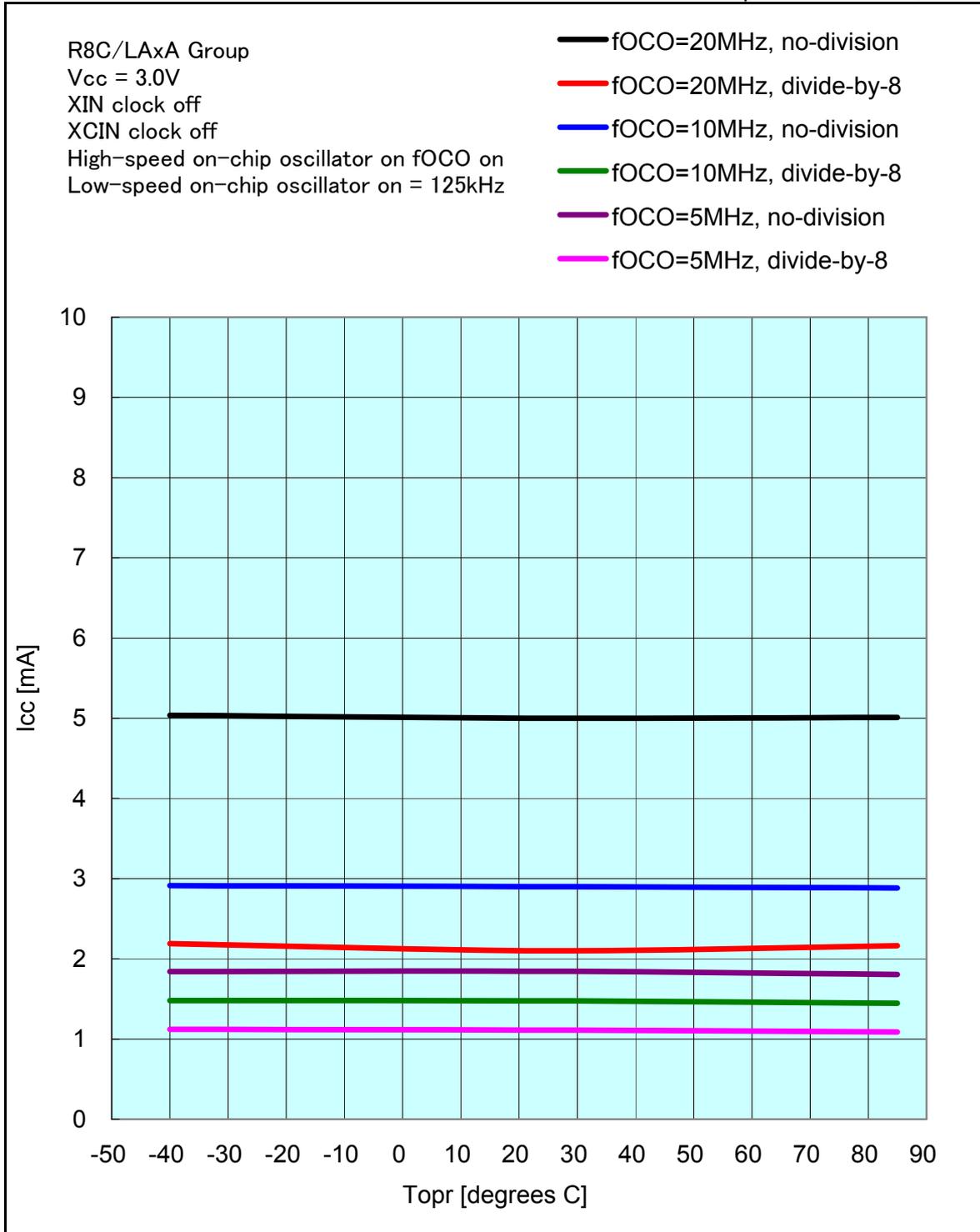
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Icc VS Topr

(High-speed on-chip oscillator mode on fOCO)

V_{cc} = 3.0V

Prepared on Nov. 30, 2010



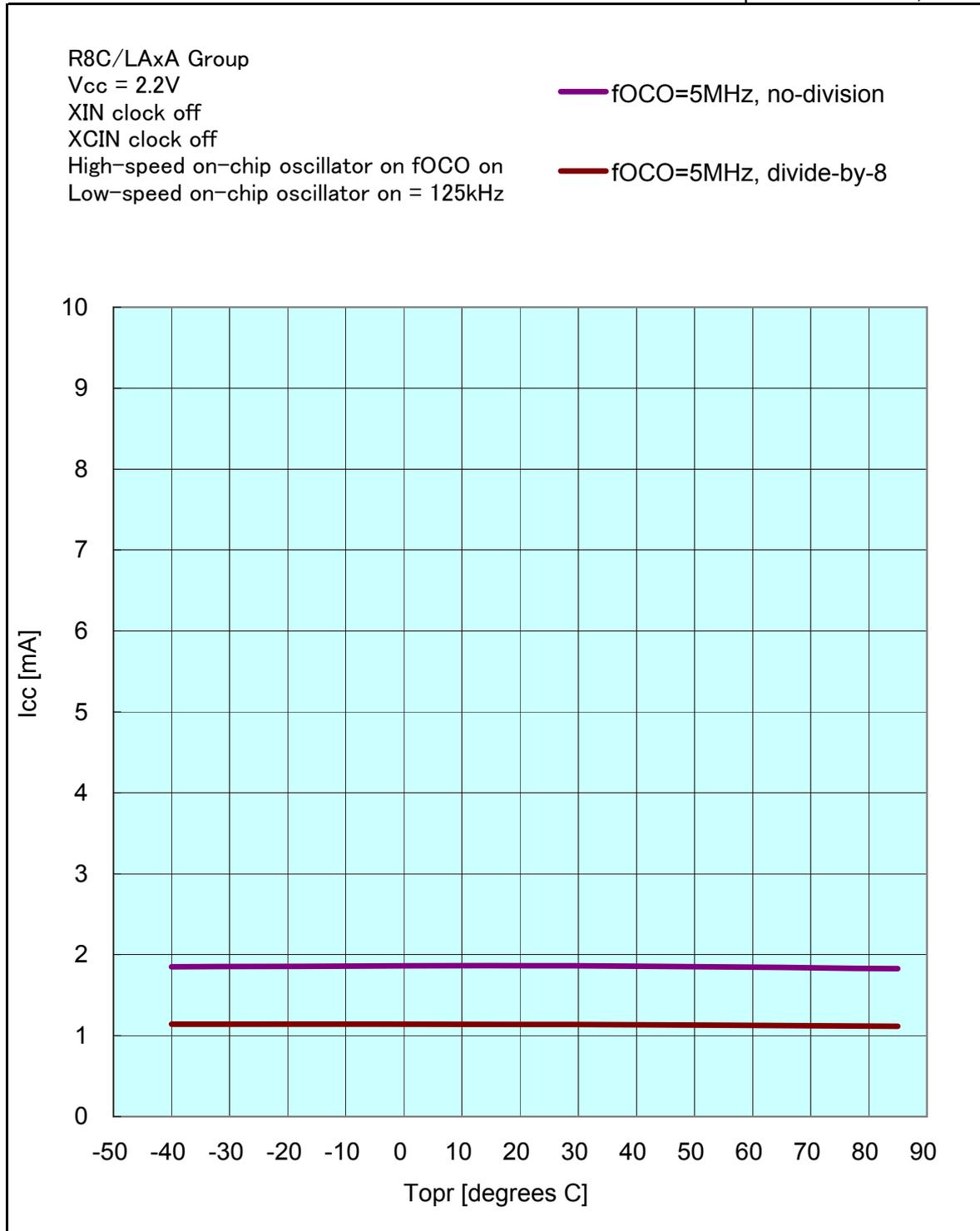
The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics

Icc VS Topr

(High-speed on-chip oscillator mode on fOCO)

Vcc = 2.2V

Prepared on Nov. 30, 2010

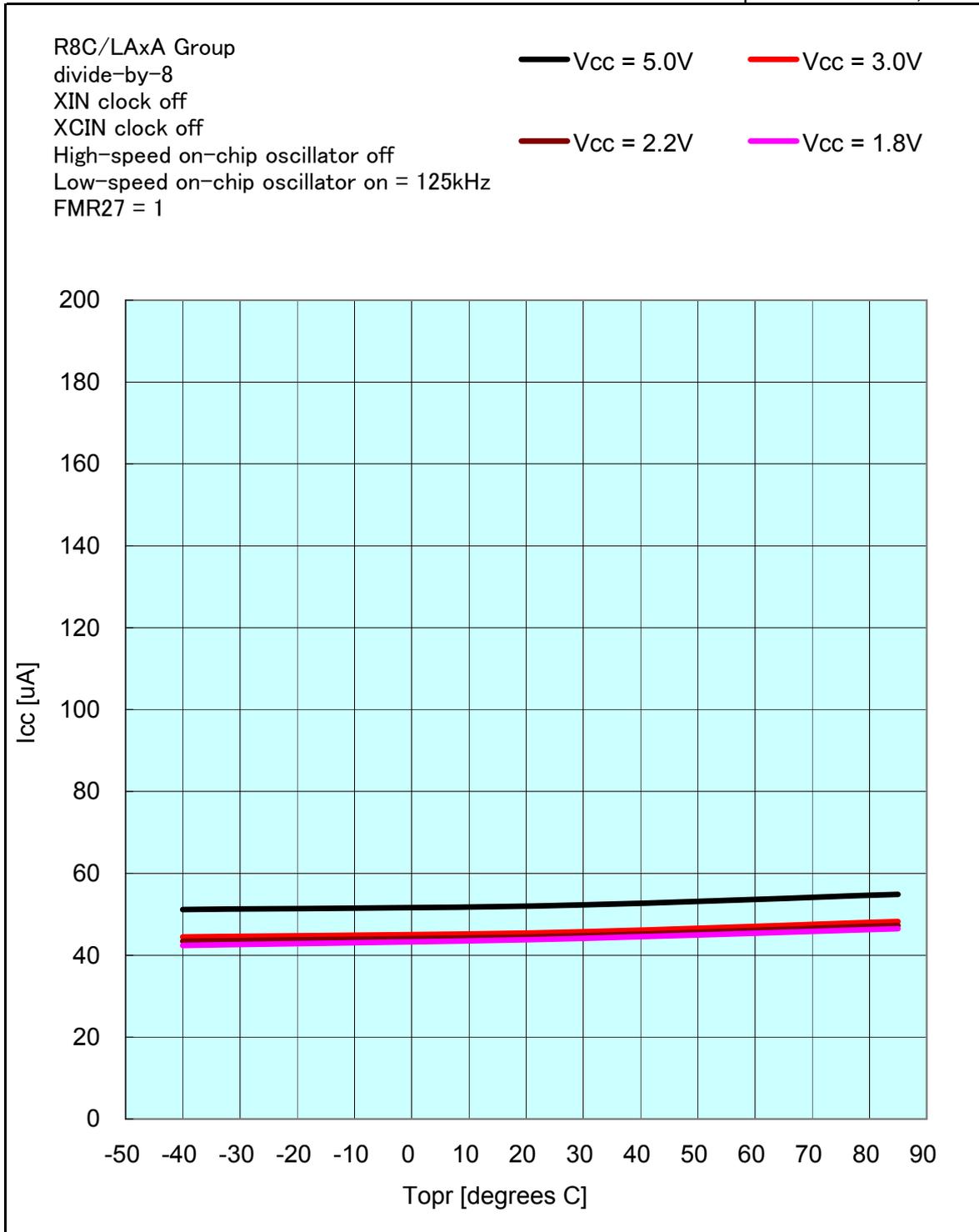


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Icc VS Topr

(Low-speed on-chip oscillator mode)

Prepared on Nov. 30, 2010

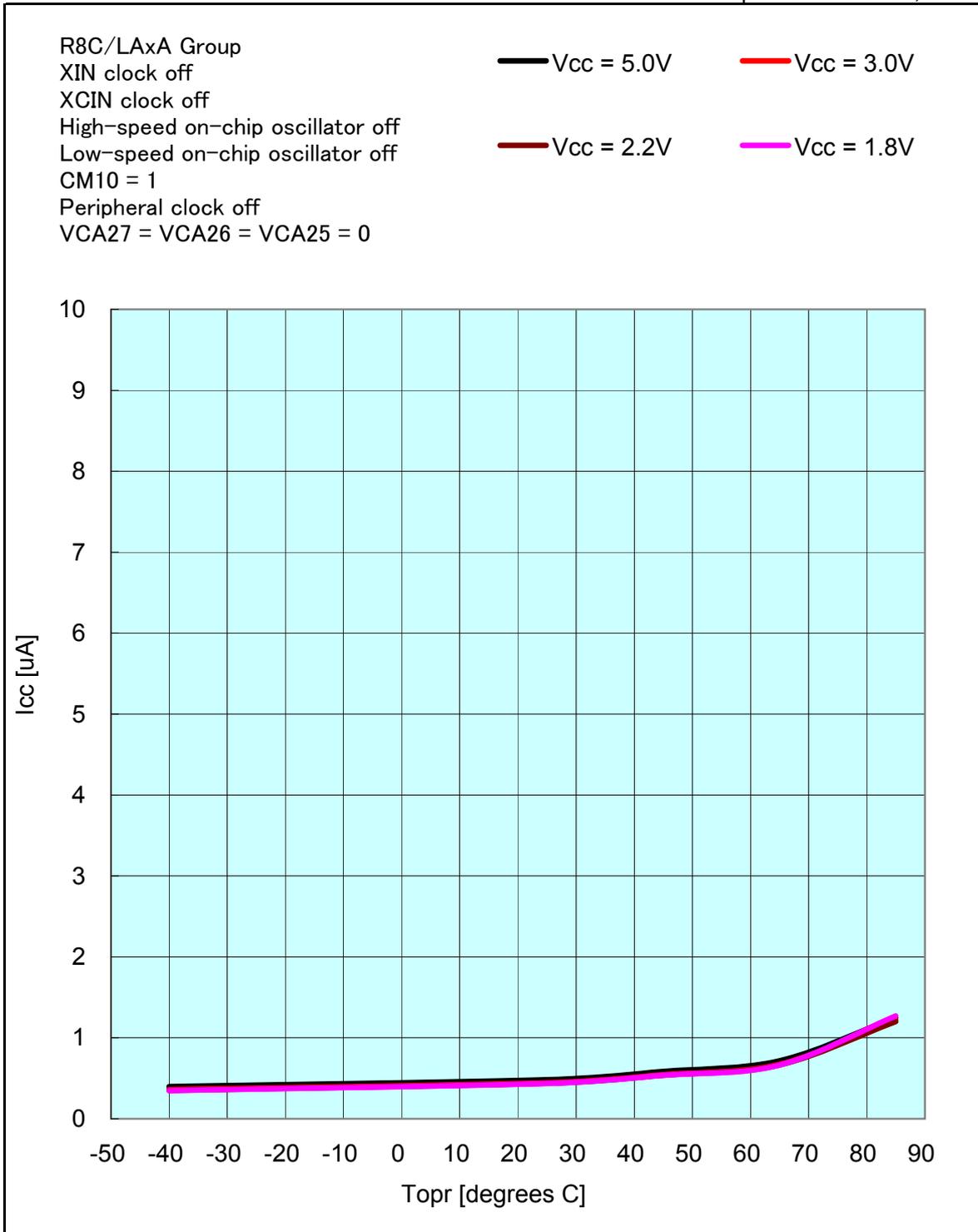


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Icc VS Topr

(Stop mode)

Prepared on Nov. 30, 2010

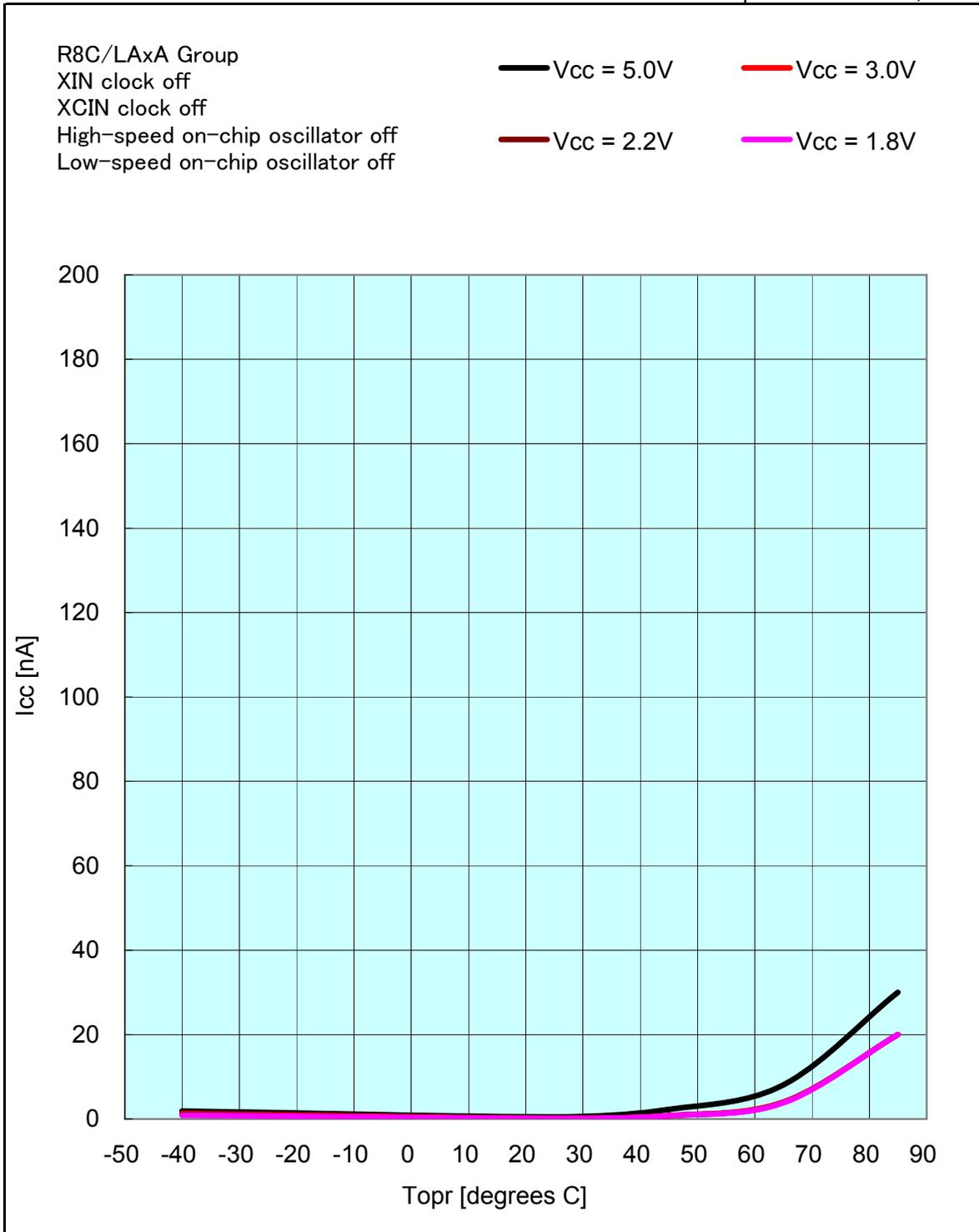


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Icc VS Topr

(Power off mode0)

Prepared on Nov. 30, 2010

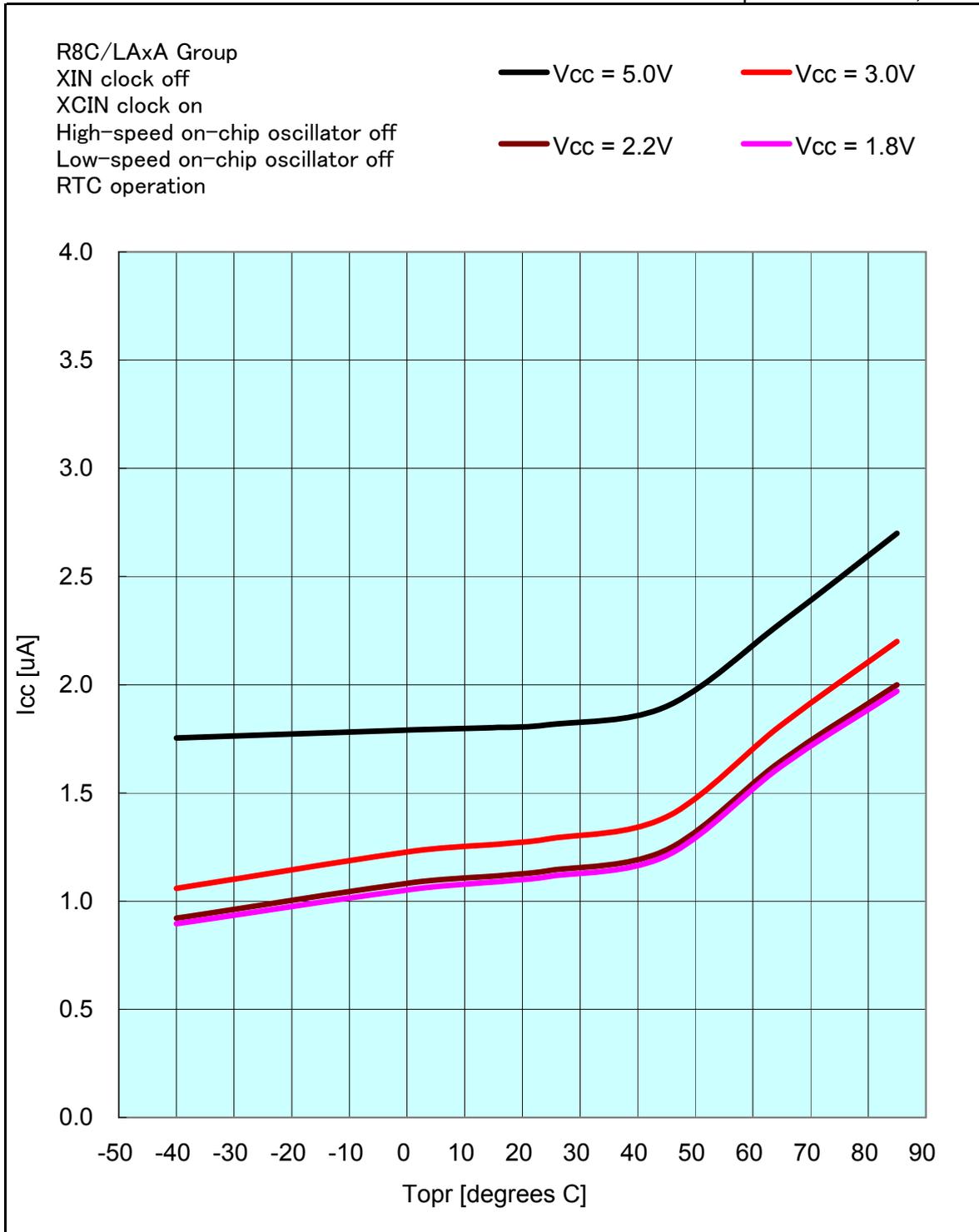


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Icc VS Topr

(Power off mode2)

Prepared on Nov. 30, 2010

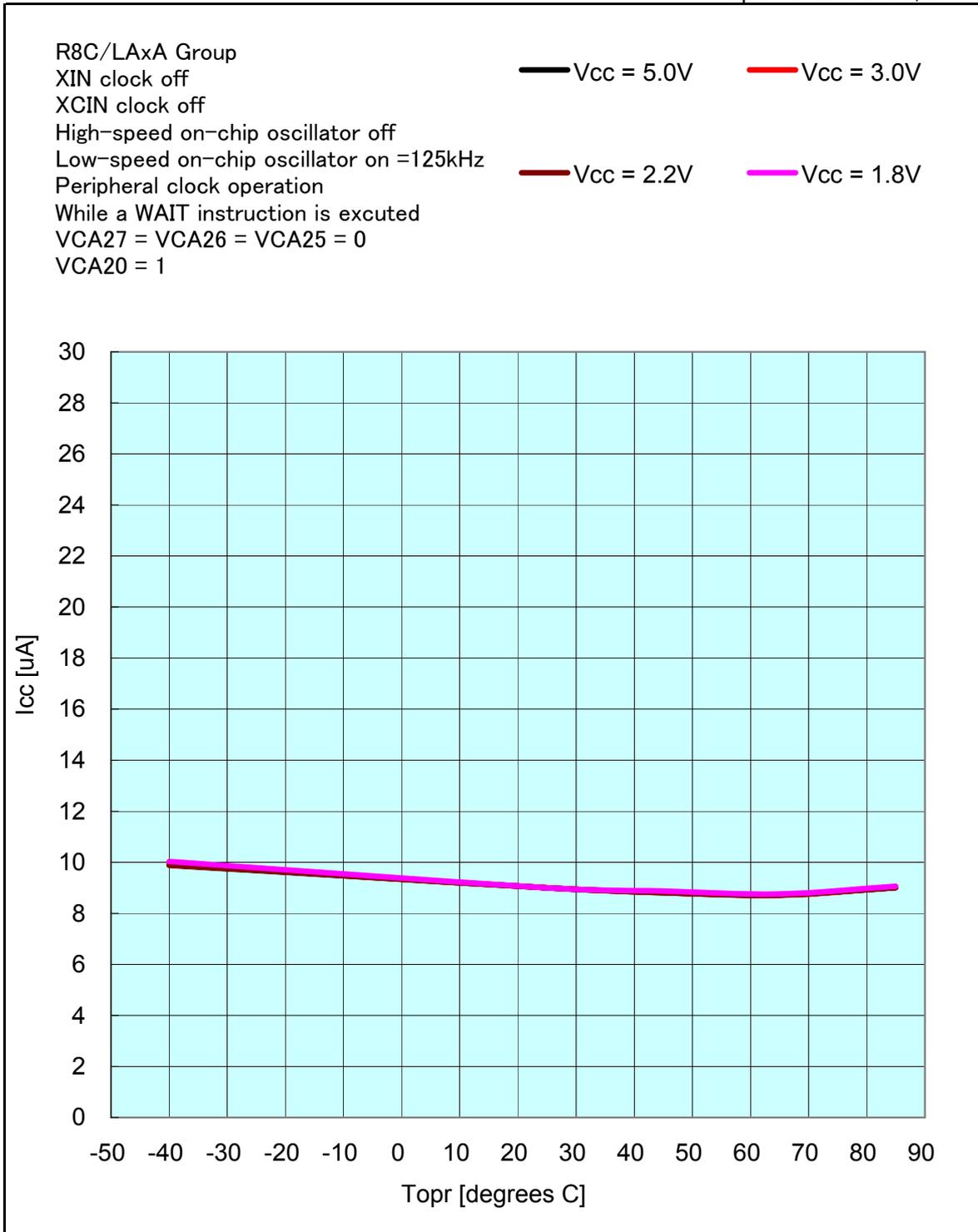


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Icc VS Topr

(Low-speed on-chip oscillator wait mode)
Peripheral clock operation

Prepared on Nov. 30, 2010

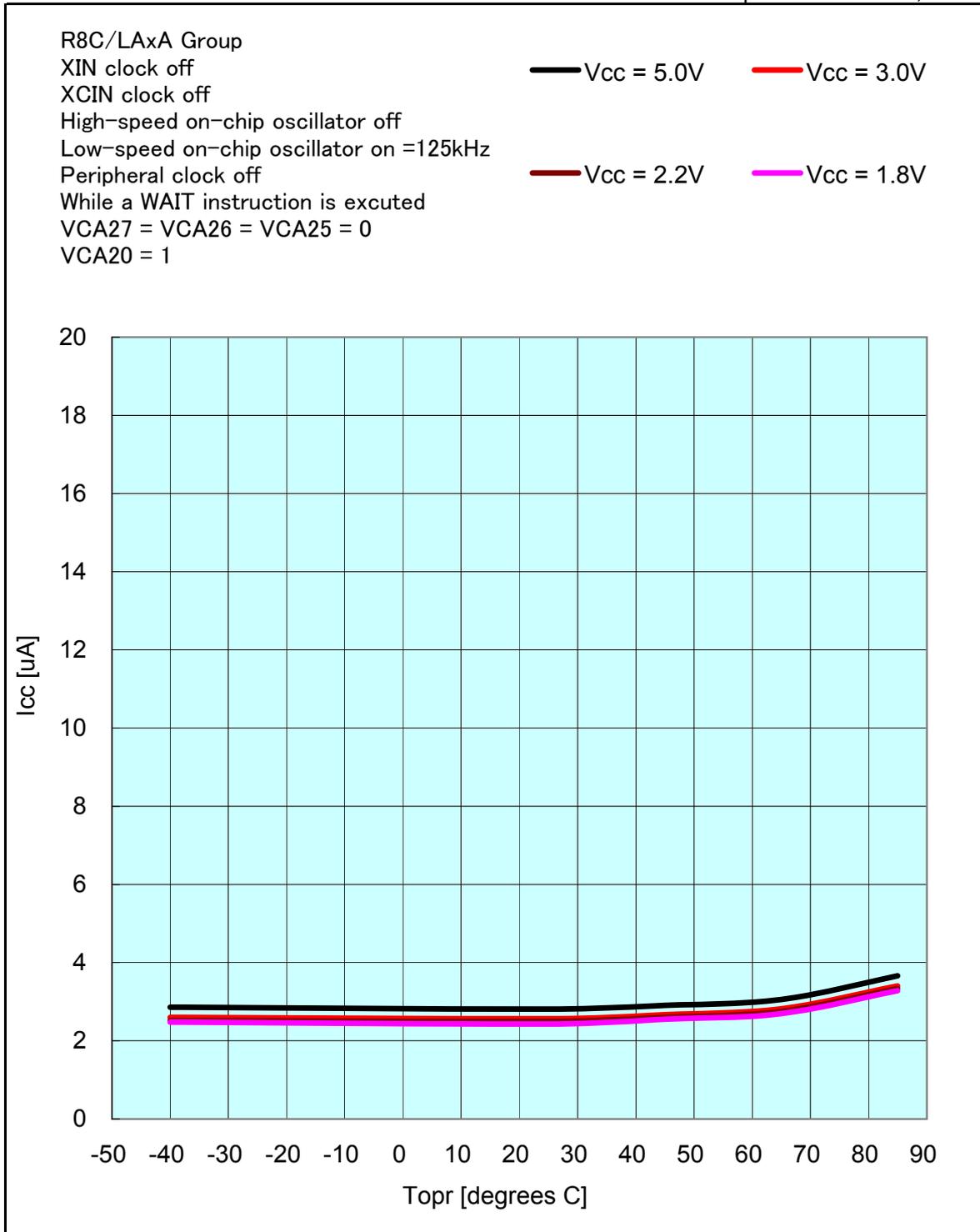


The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics

Icc VS Topr

(Low-speed on-chip oscillator wait mode)
Peripheral clock off

Prepared on Nov. 30, 2010

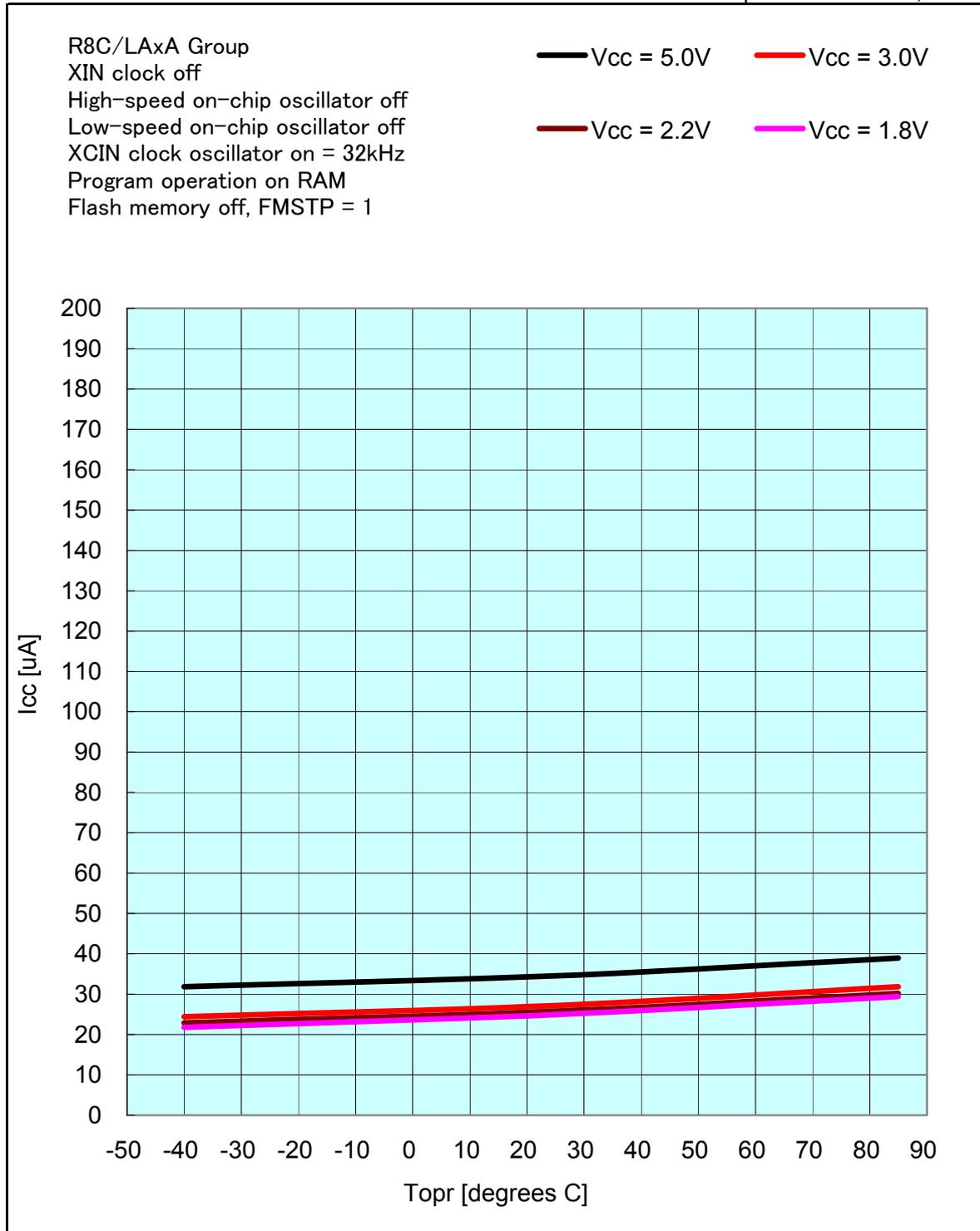


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Icc VS Topr

(Low-speed clock mode)
Program operation on RAM

Prepared on Nov. 30, 2010

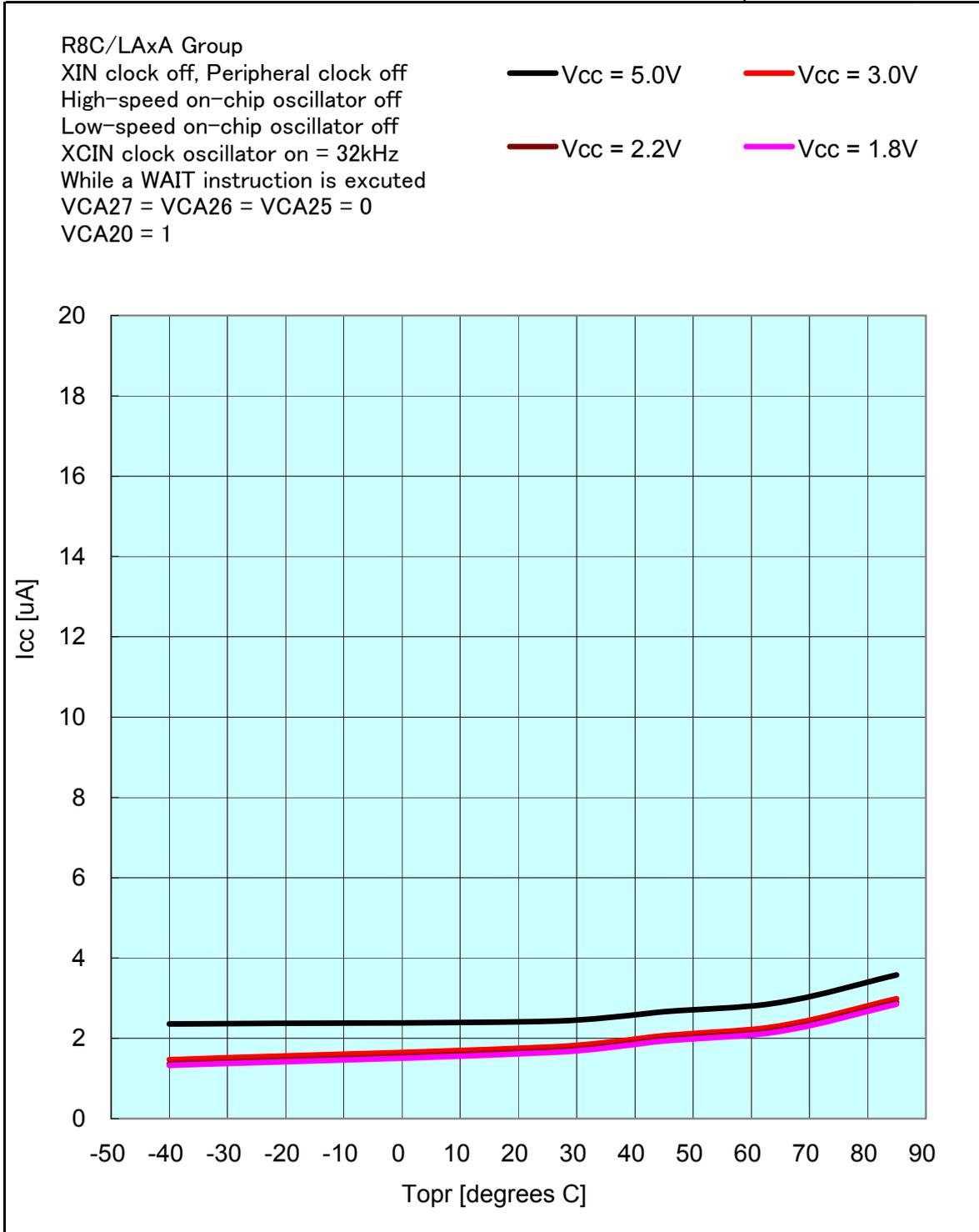


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Icc VS Topr

(Low-speed clock wait mode)

Prepared on Nov. 30, 2010

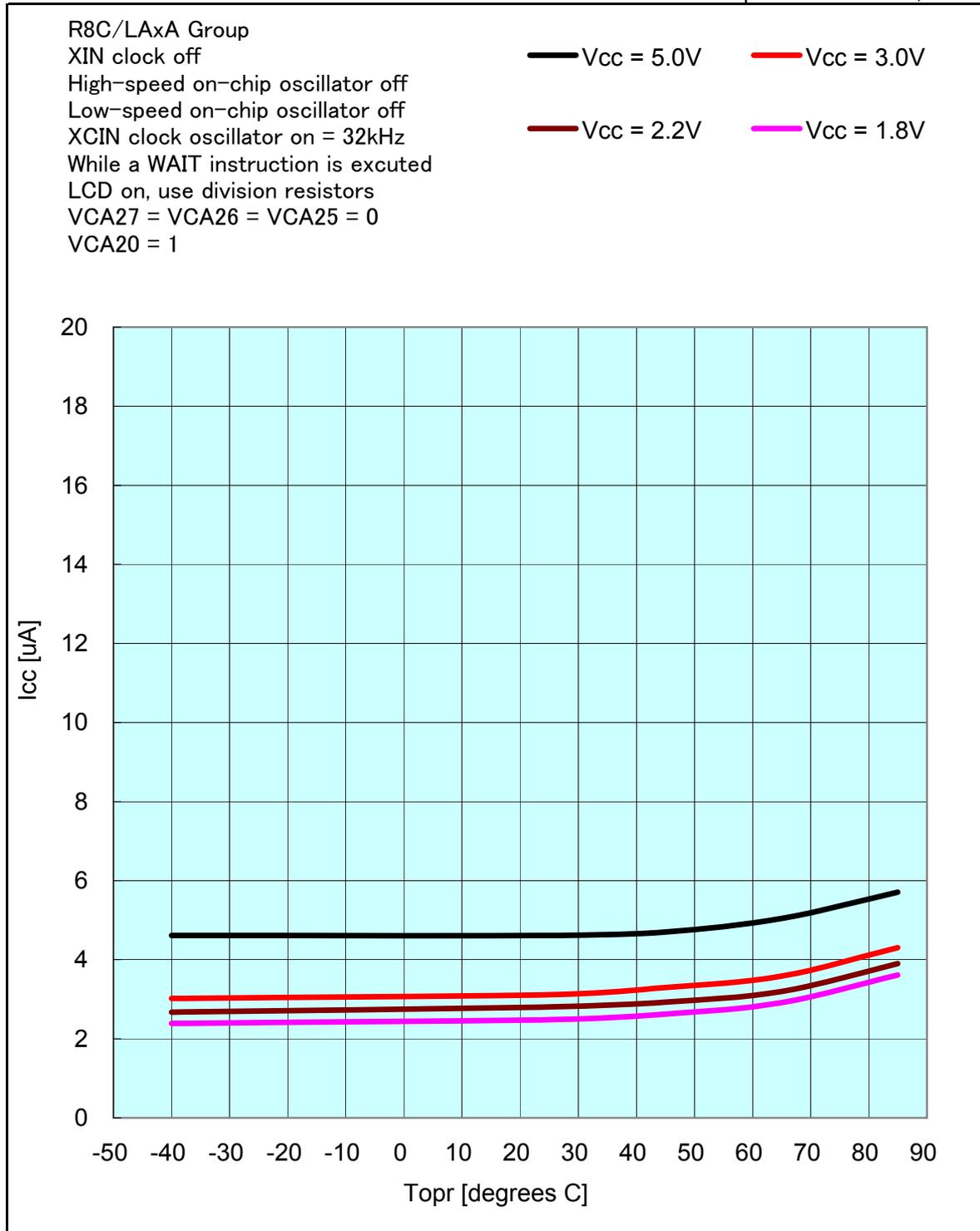


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Icc VS Topr

(Low-speed clock wait mode)
LCD=ON(division resistors)

Prepared on Nov. 30, 2010

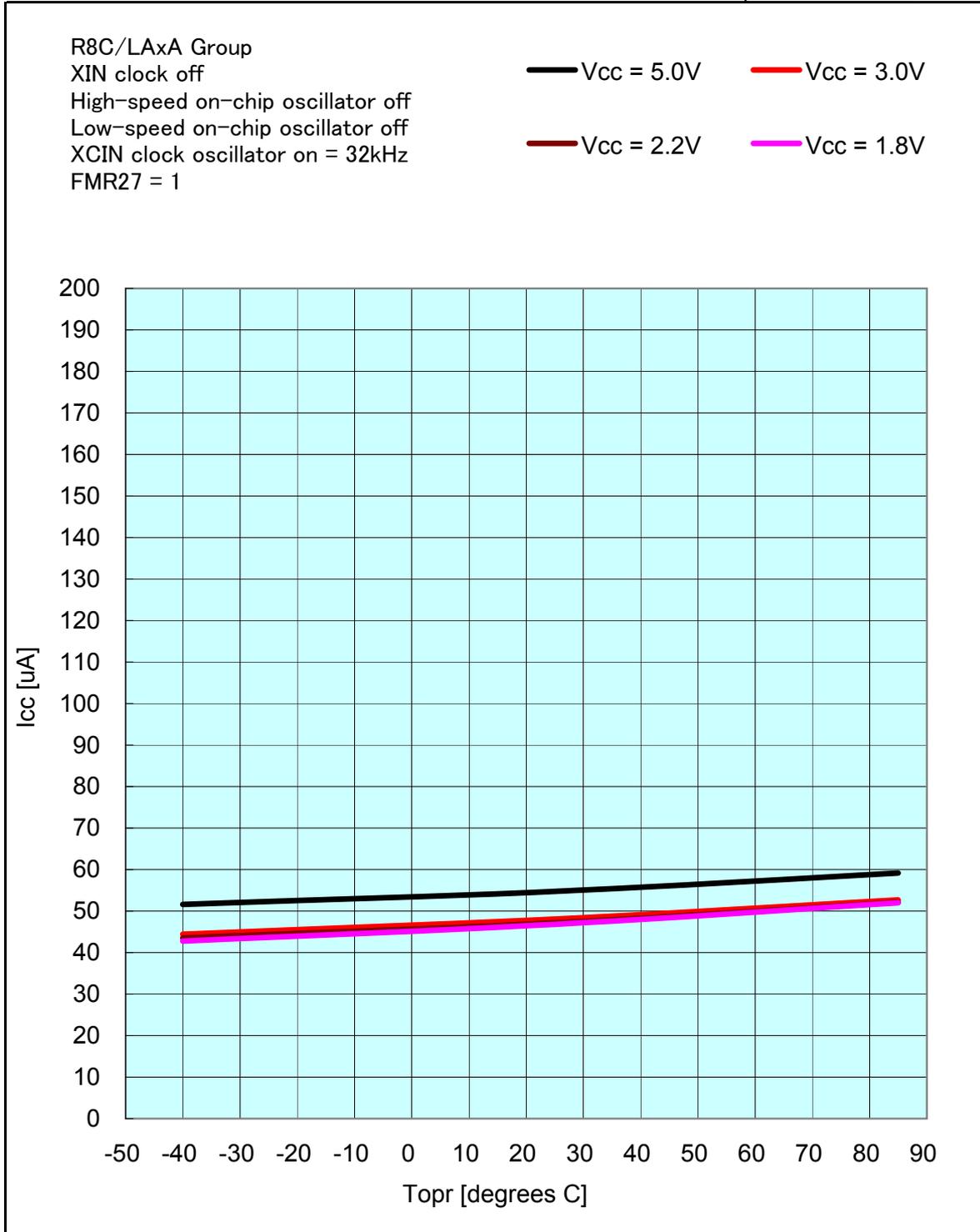


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Icc VS Topr

(Low-speed clock mode)

Prepared on Nov. 30, 2010



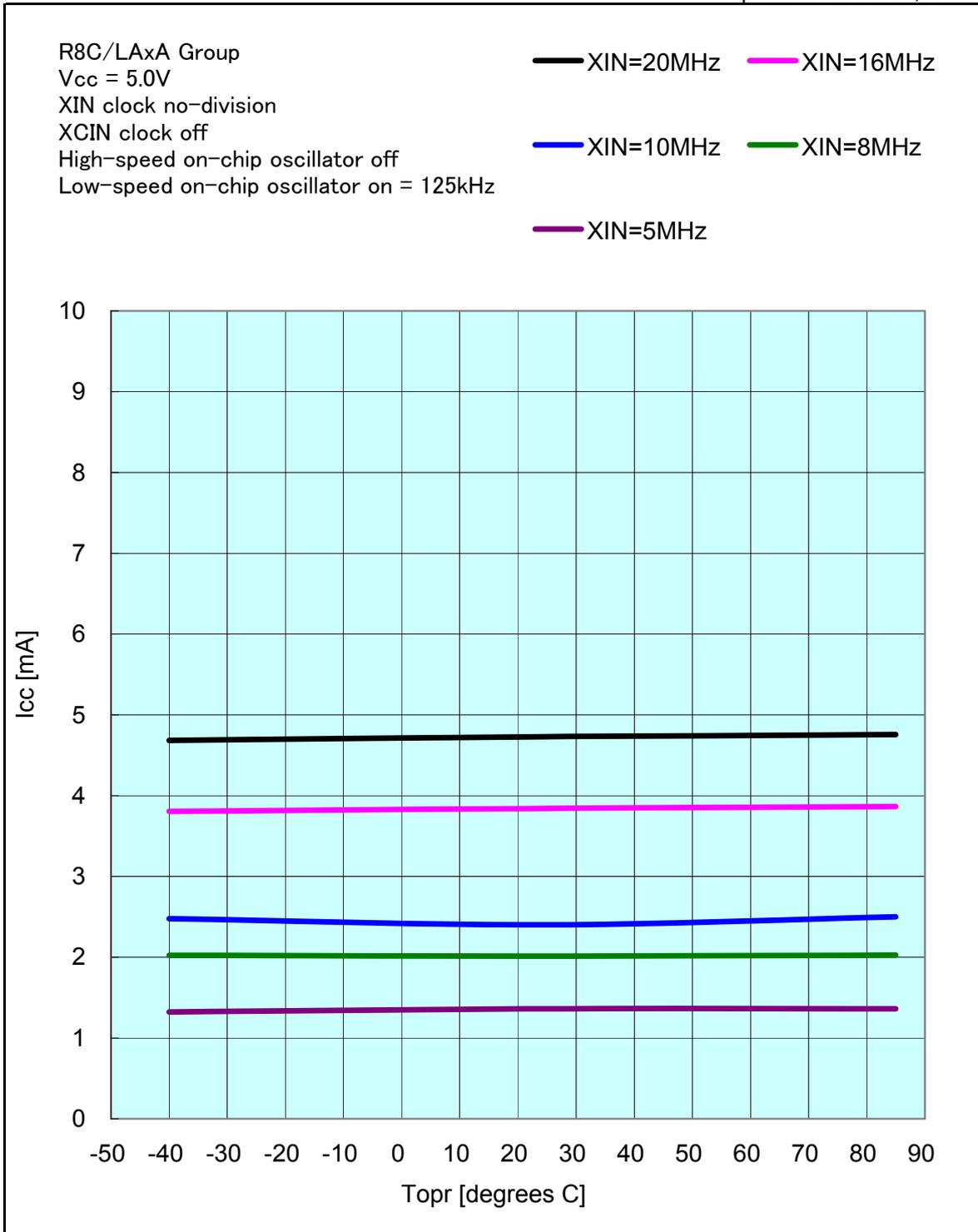
The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics

Icc VS Topr

(High-speed clock mode:no-division)

Vcc = 5.0V

Prepared on Nov. 30, 2010



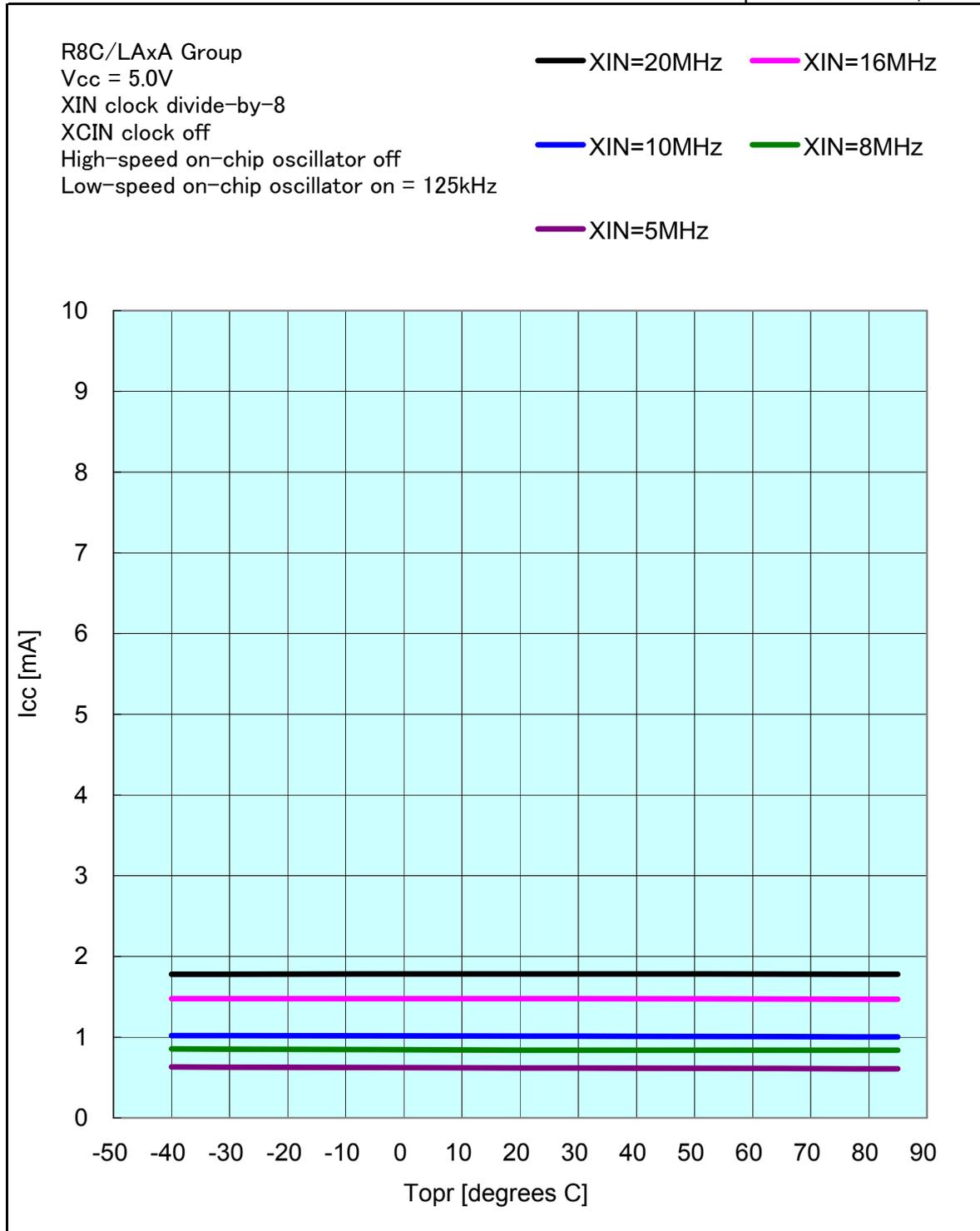
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Icc VS Topr

(High-speed clock mode:divide-by-8)

Vcc = 5.0V

Prepared on Nov. 30, 2010



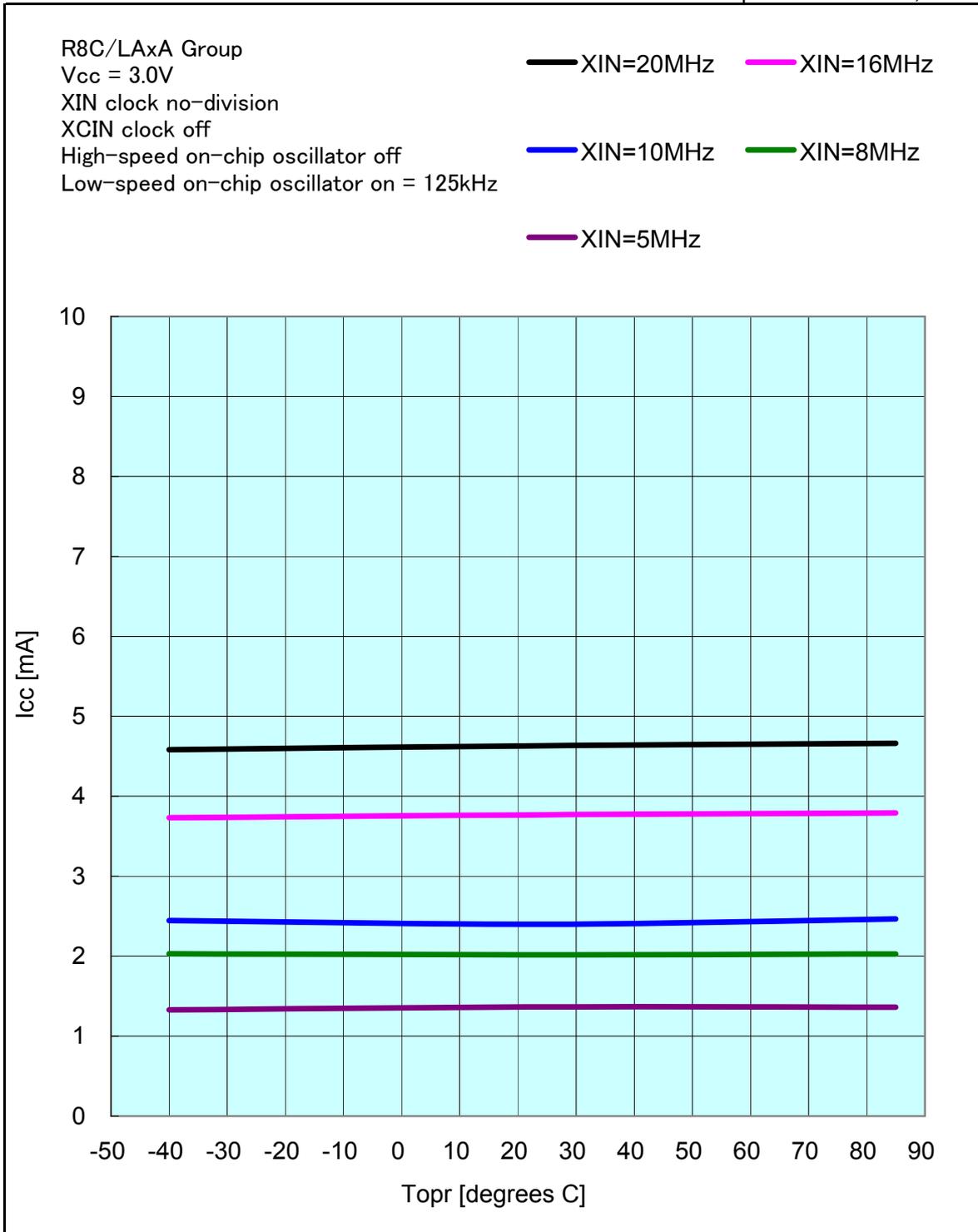
The mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics

Icc VS Topr

(High-speed clock mode:no-division)

Vcc = 3.0V

Prepared on Nov. 30, 2010



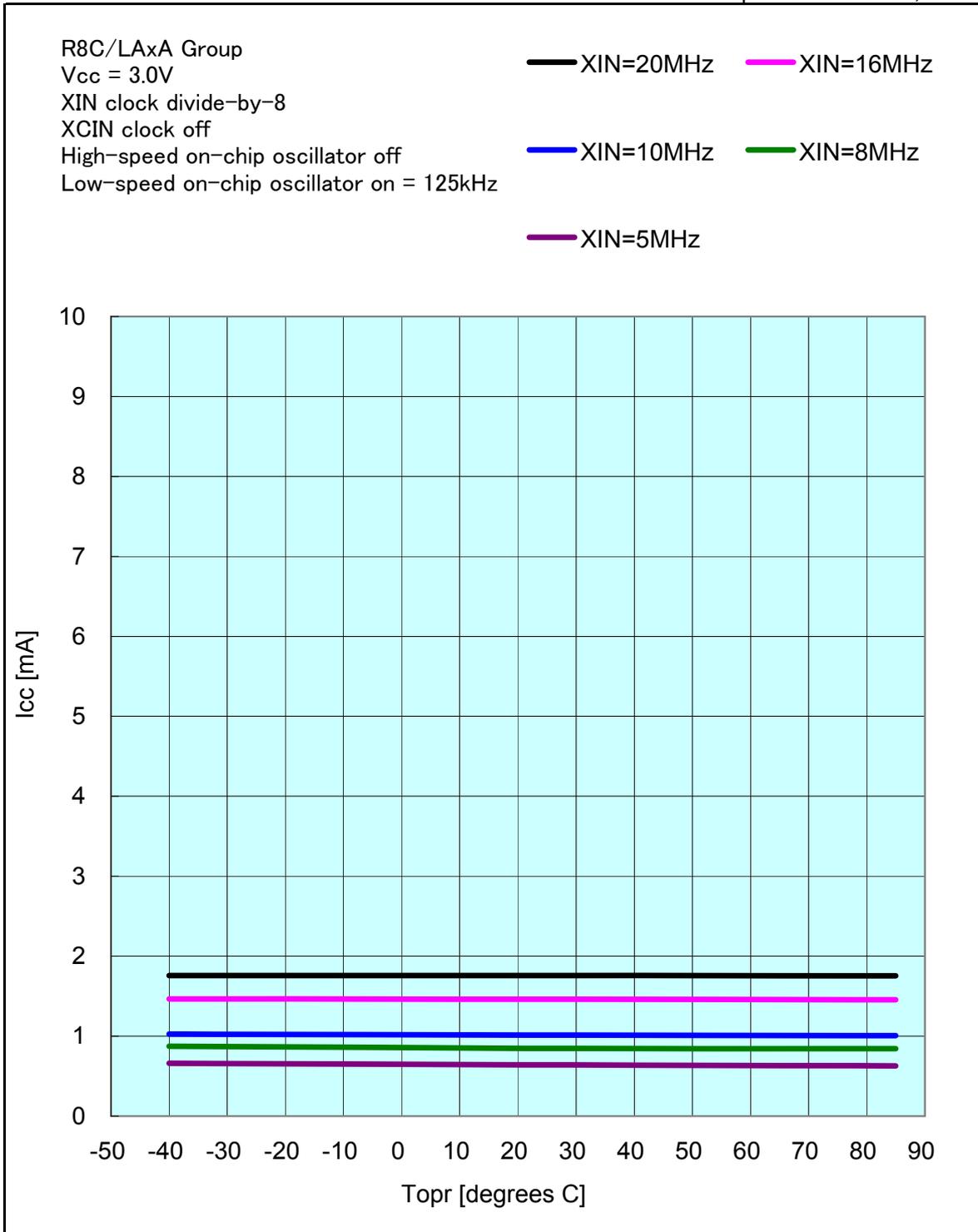
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Icc VS Topr

(High-speed clock mode:divide-by-8)

Vcc = 3.0V

Prepared on Nov. 30, 2010



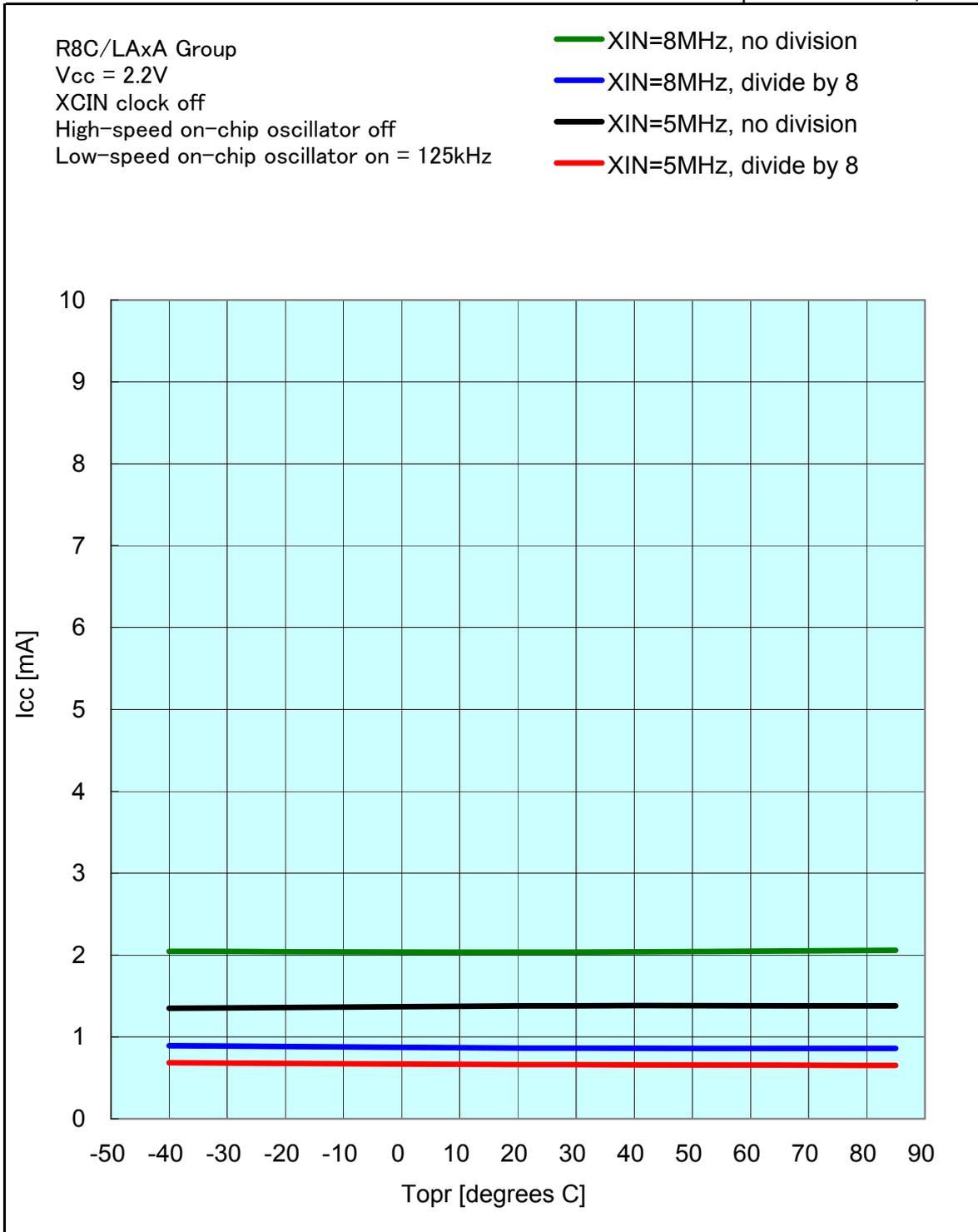
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Icc VS Topr

(High-speed clock mode)

V_{cc} = 2.2V

Prepared on Nov. 30, 2010



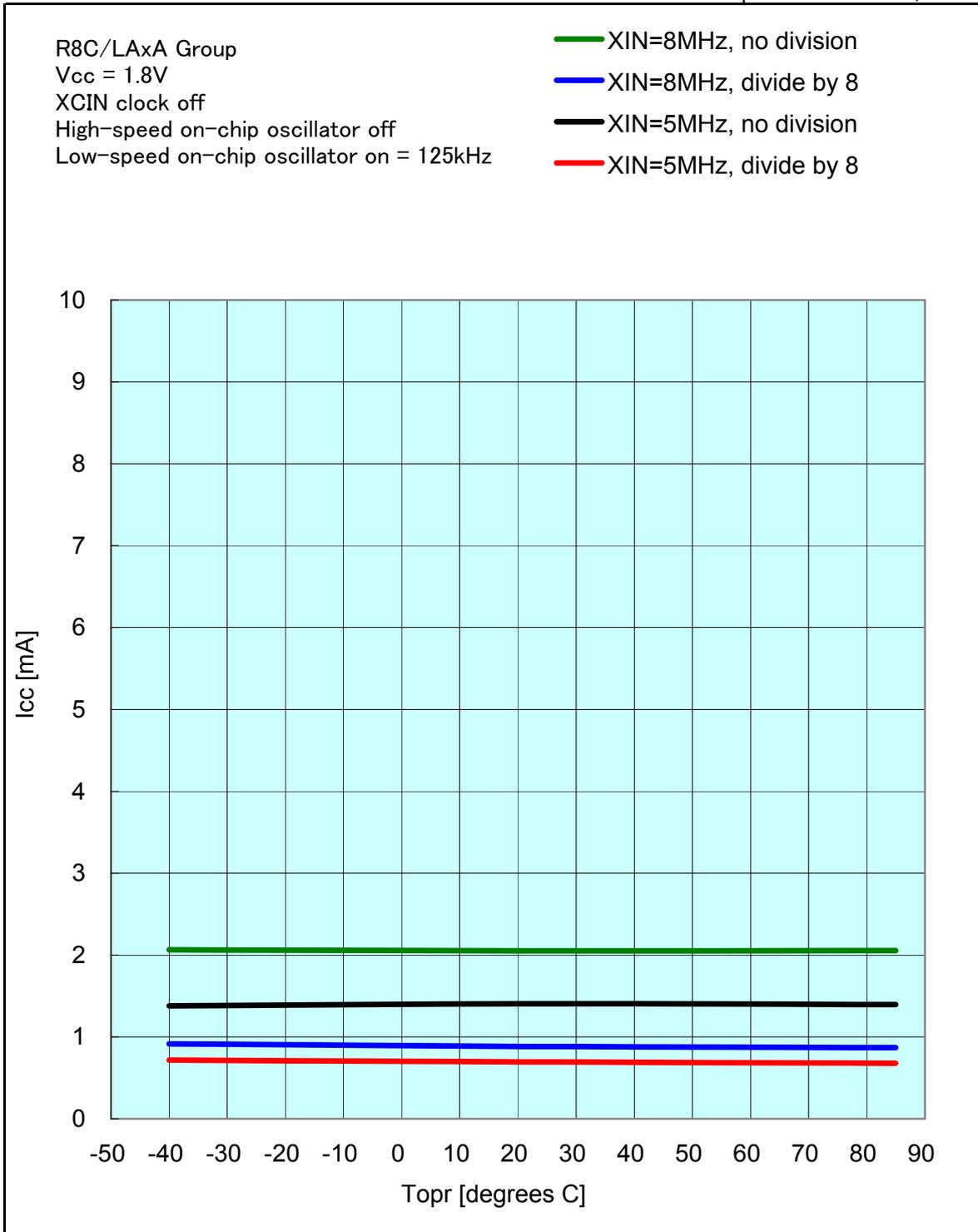
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Icc VS Topr

(High-speed clock mode)

Vcc = 1.8V

Prepared on Nov. 30, 2010



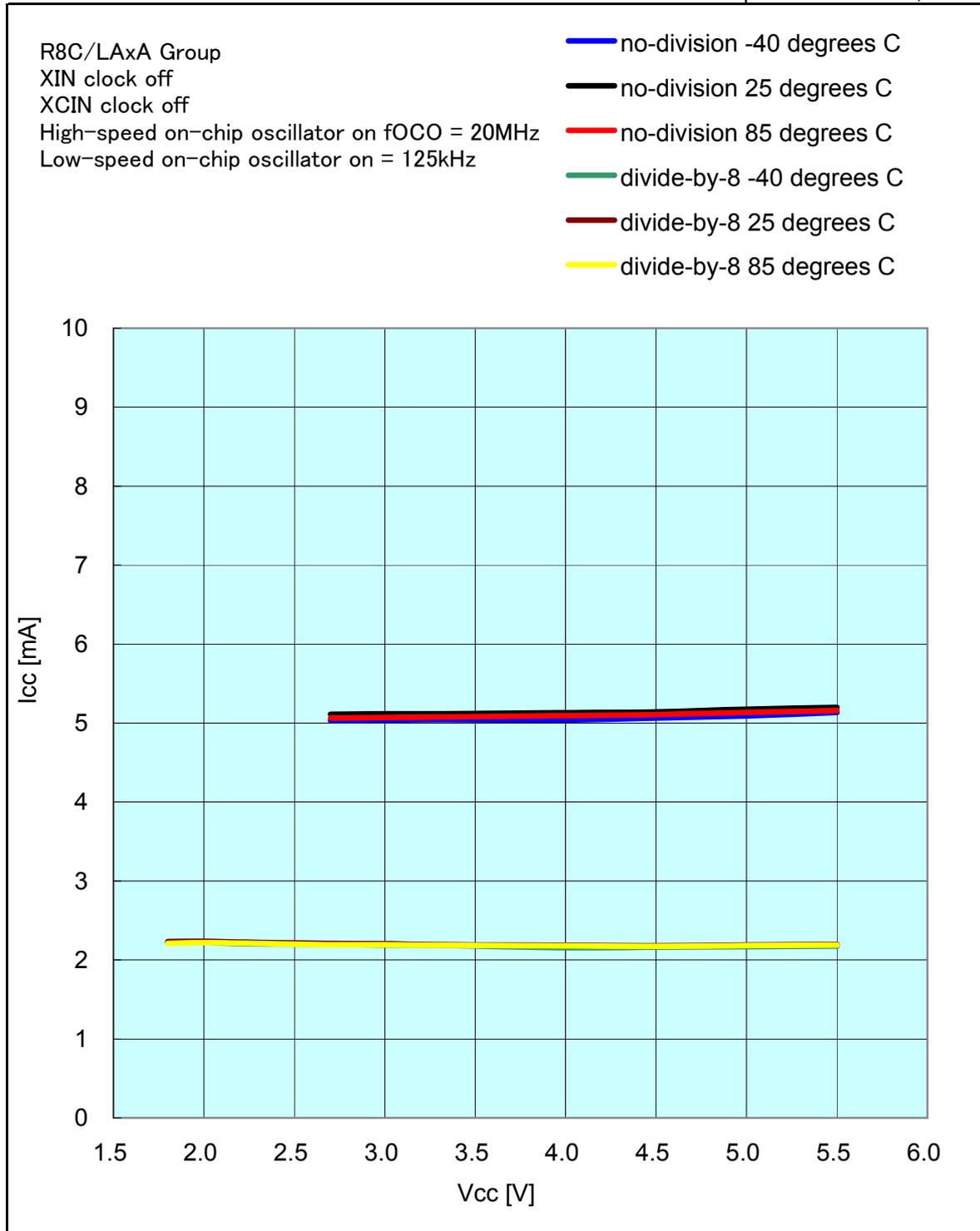
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Icc VS Vcc

(High-speed on-chip oscillator mode)

fOCO = 20MHz

Prepared on Nov. 30, 2010



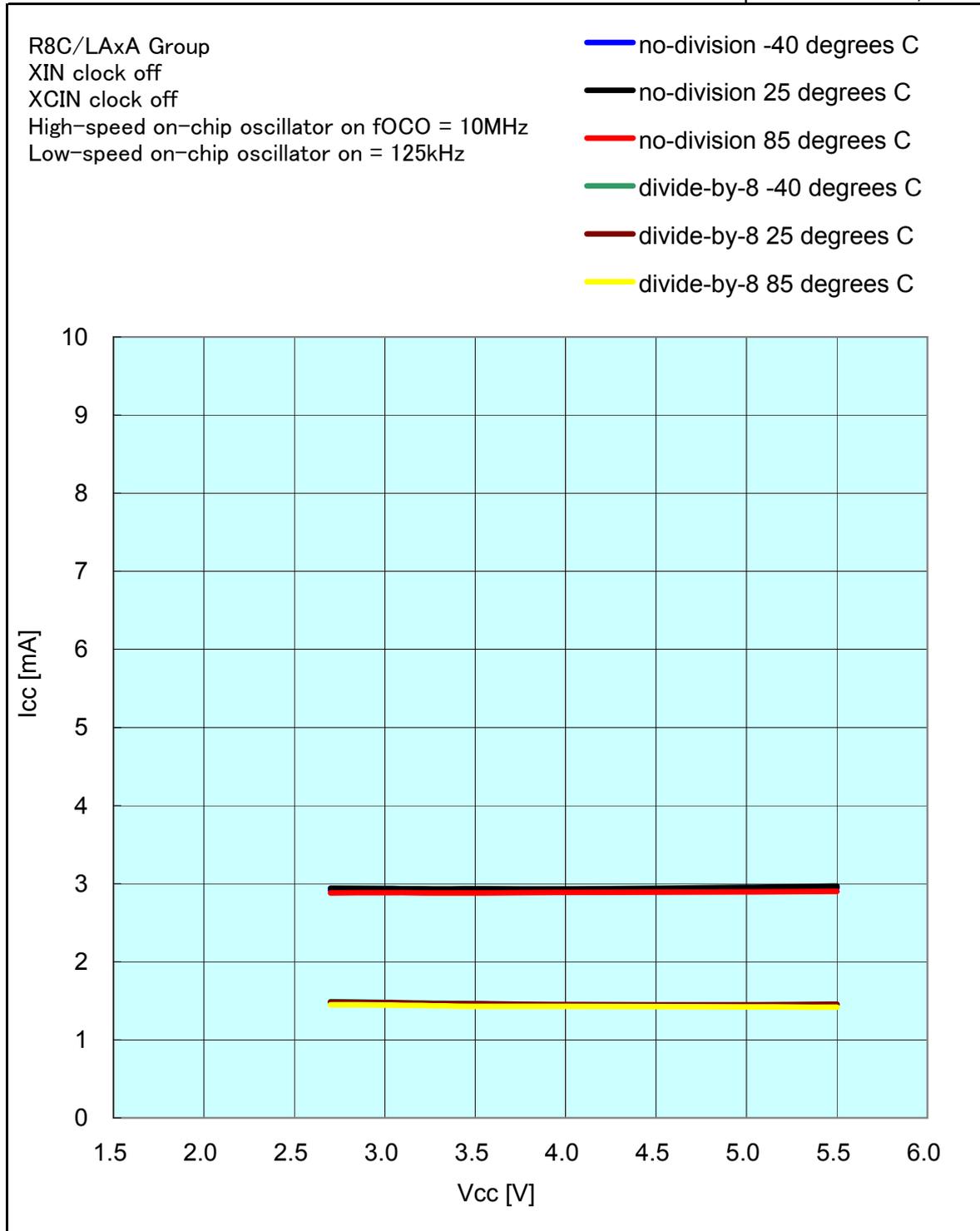
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Icc VS Vcc

(High-speed on-chip oscillator mode)

fOCO = 10MHz

Prepared on Nov. 30, 2010



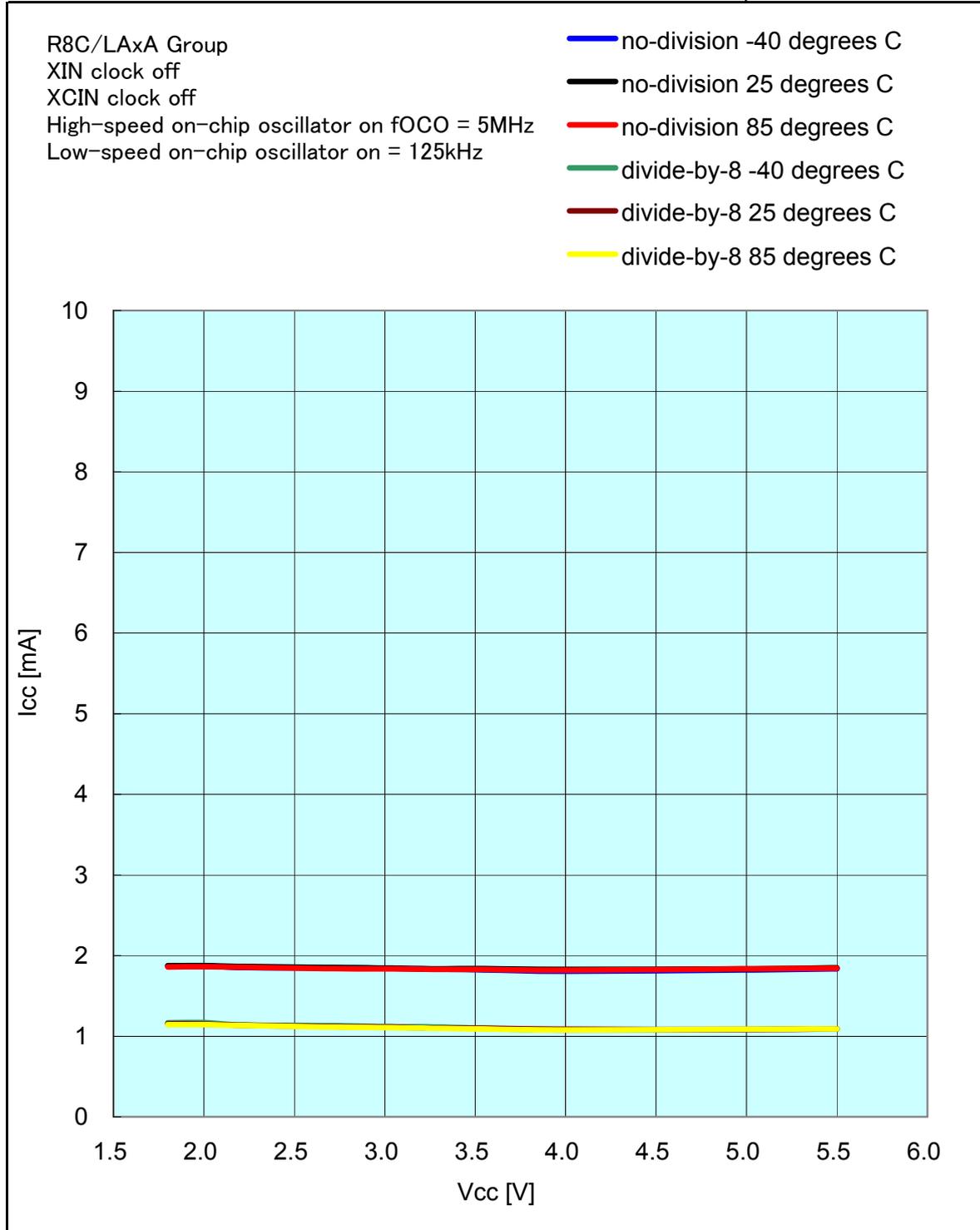
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Icc VS Vcc

(High-speed on-chip oscillator mode)

fOCO = 5MHz

Prepared on Nov. 30, 2010

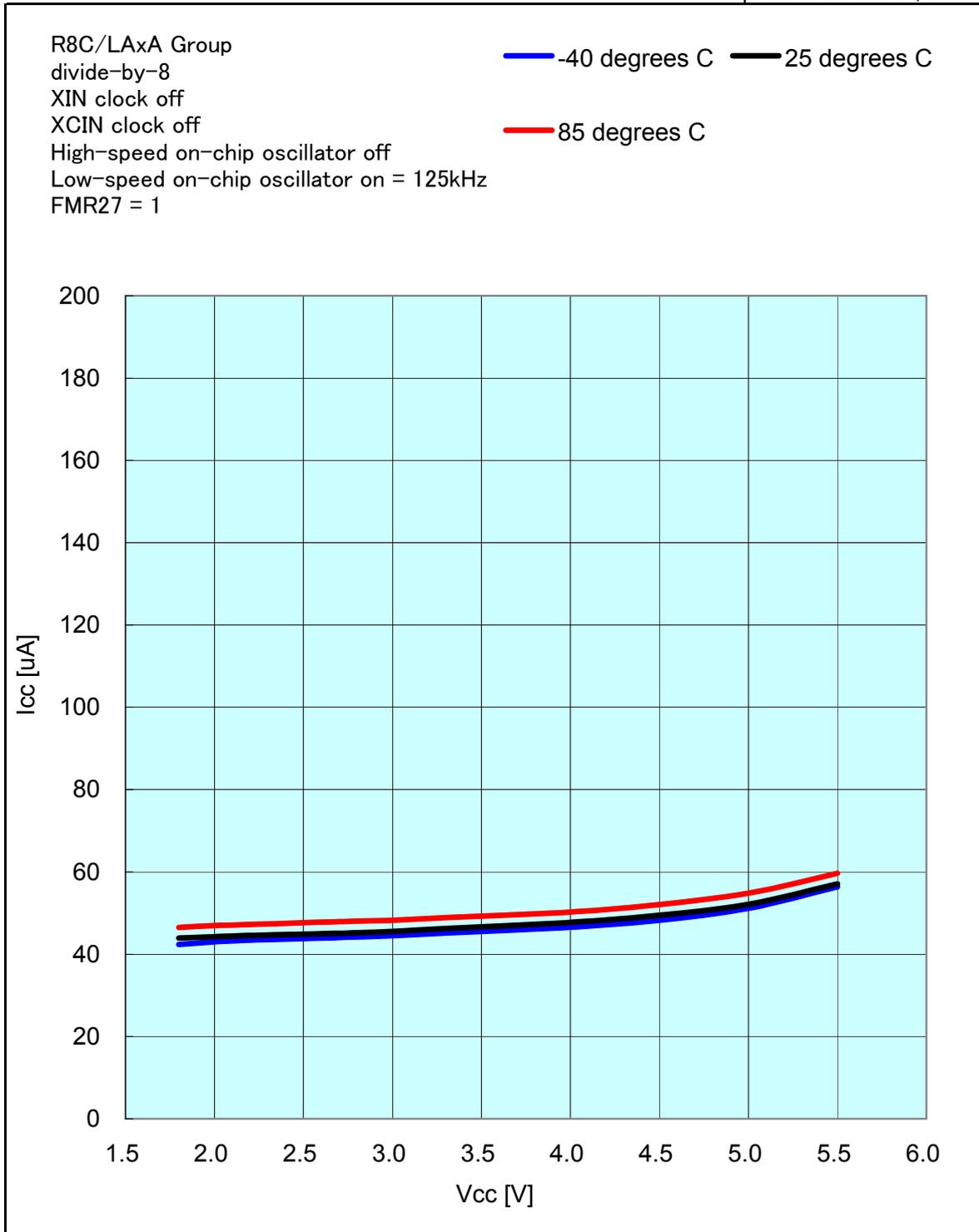


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Icc vs Vcc

(Low-speed on-chip oscillator mode)

Prepared on Nov. 30, 2010

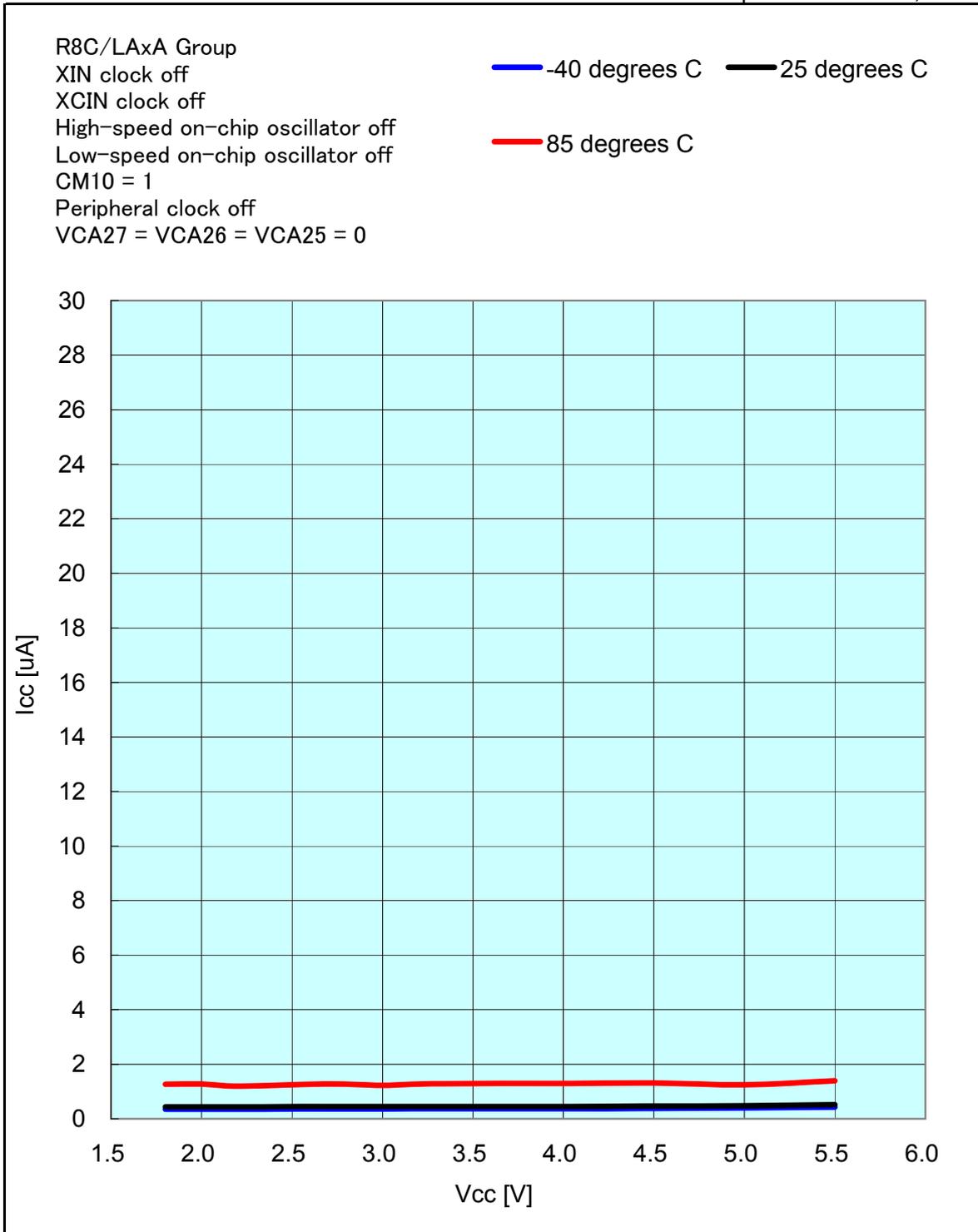


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Icc VS Vcc

(Stop mode)

Prepared on Nov. 30, 2010

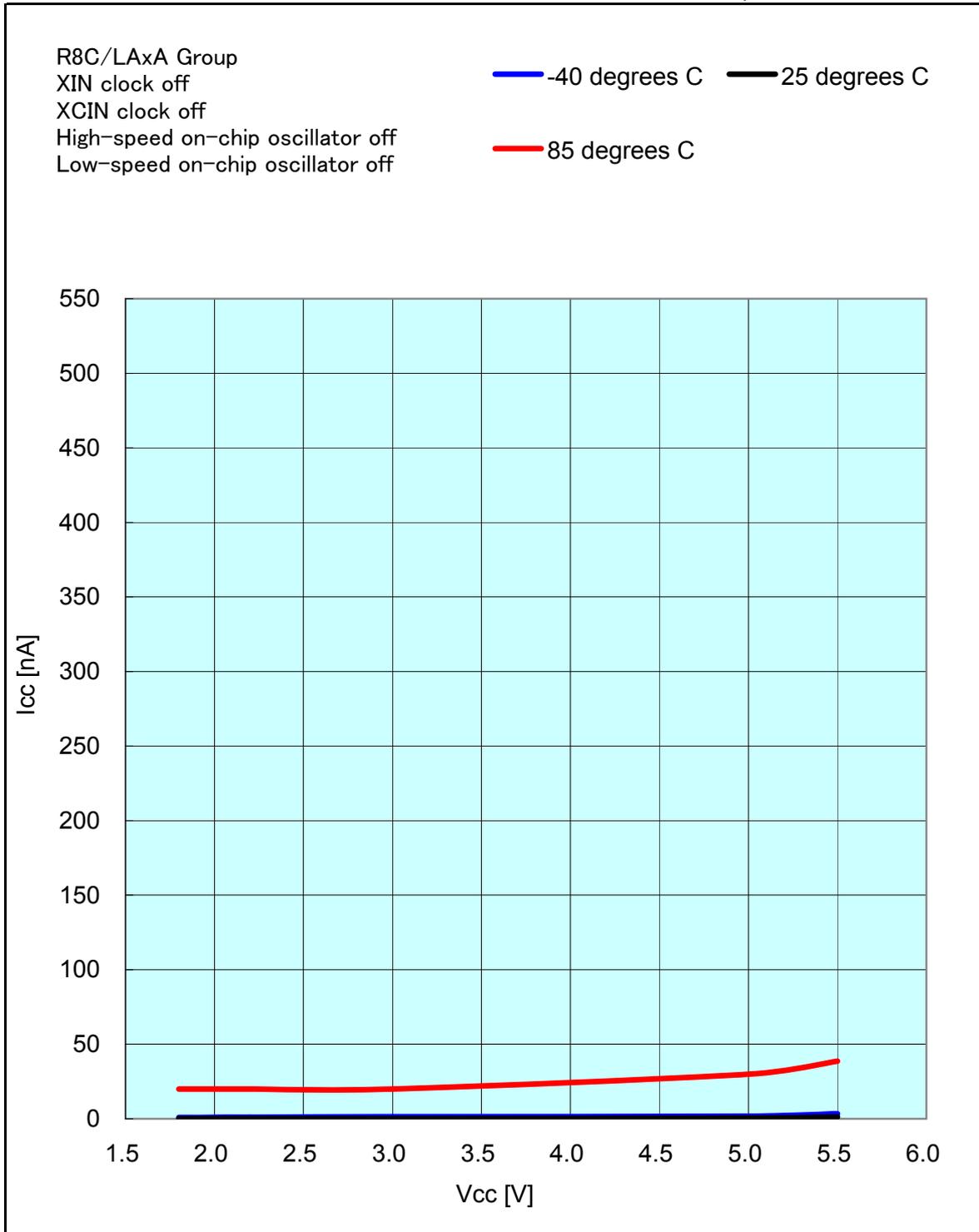


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Icc vs Vcc

(Power off mode0)

Prepared on Nov. 30, 2010

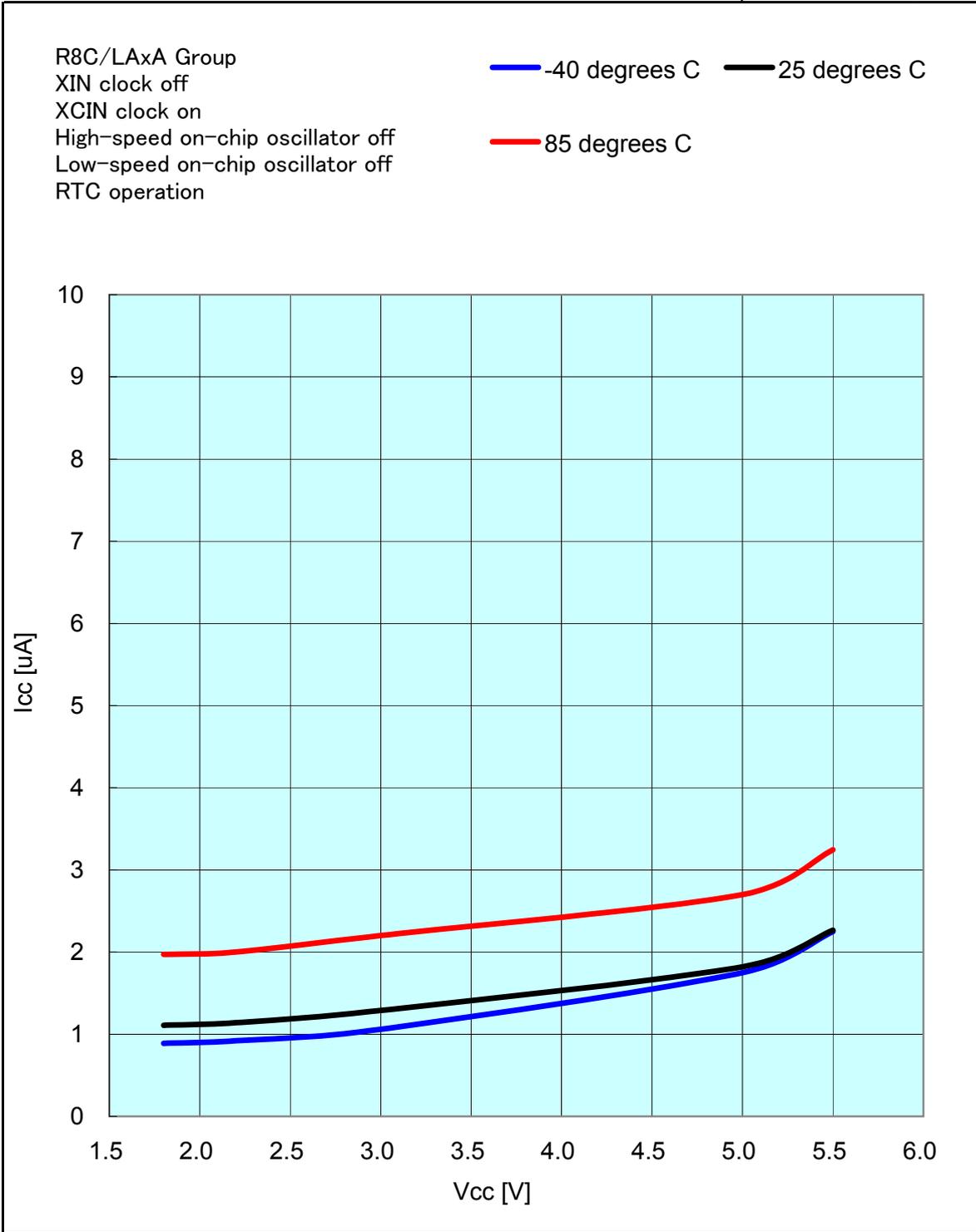


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Icc vs Vcc

(Power off mode2)

Prepared on Nov. 30, 2010

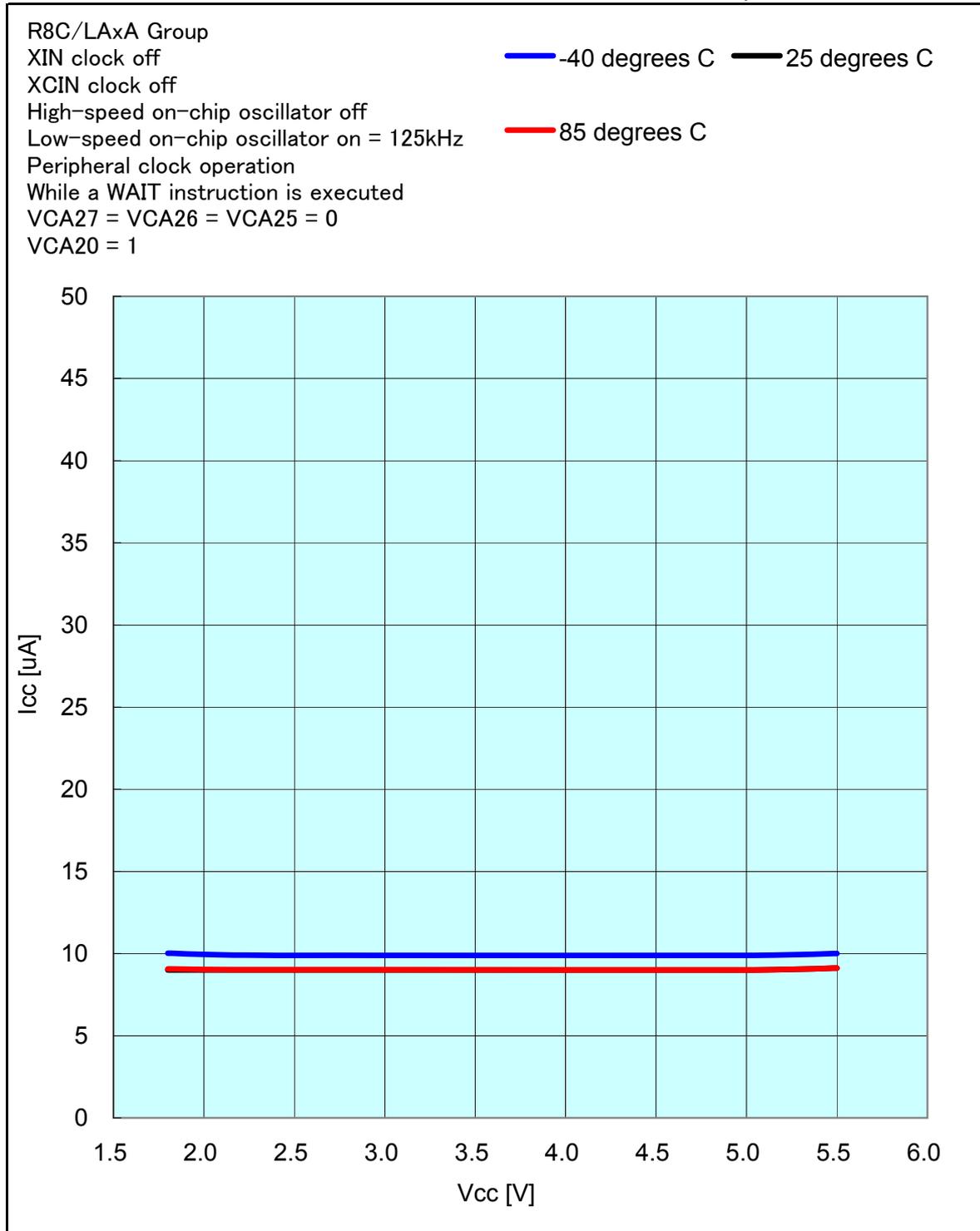


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Icc vs Vcc

(Low-speed on-chip oscillator wait mode)
Peripheral clock operation

Prepared on Nov. 30, 2010



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Icc VS Vcc

(Low-speed on-chip oscillator wait mode)
Peripheral clock off

Prepared on Nov. 30, 2010

R8C/LAxA Group

XIN clock off

XCIN clock off

High-speed on-chip oscillator off

Low-speed on-chip oscillator on = 125kHz

Peripheral clock off

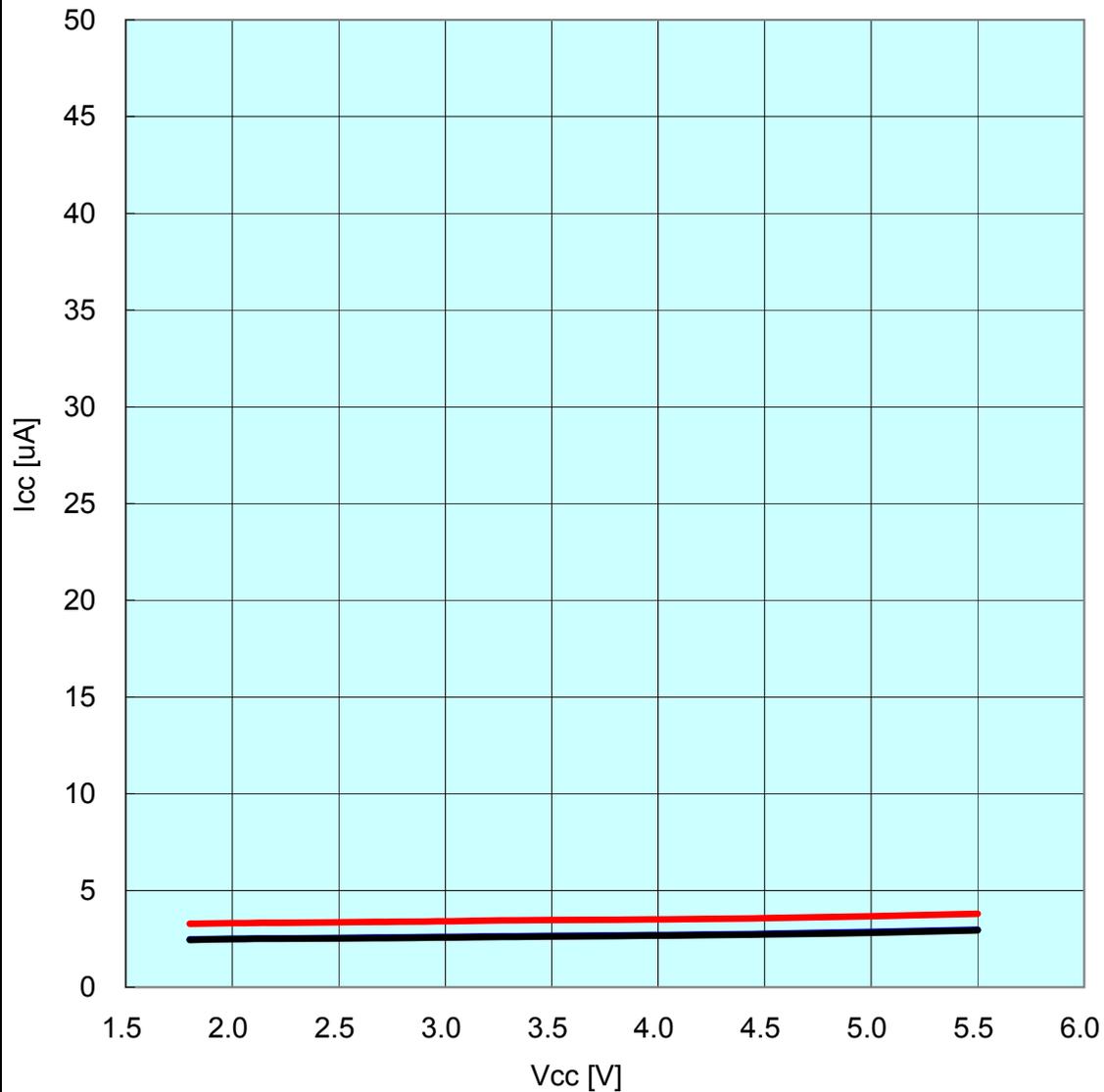
While a WAIT instruction is executed

VCA27 = VCA26 = VCA25 = 0

VCA20 = 1

— -40 degrees C — 25 degrees C

— 85 degrees C

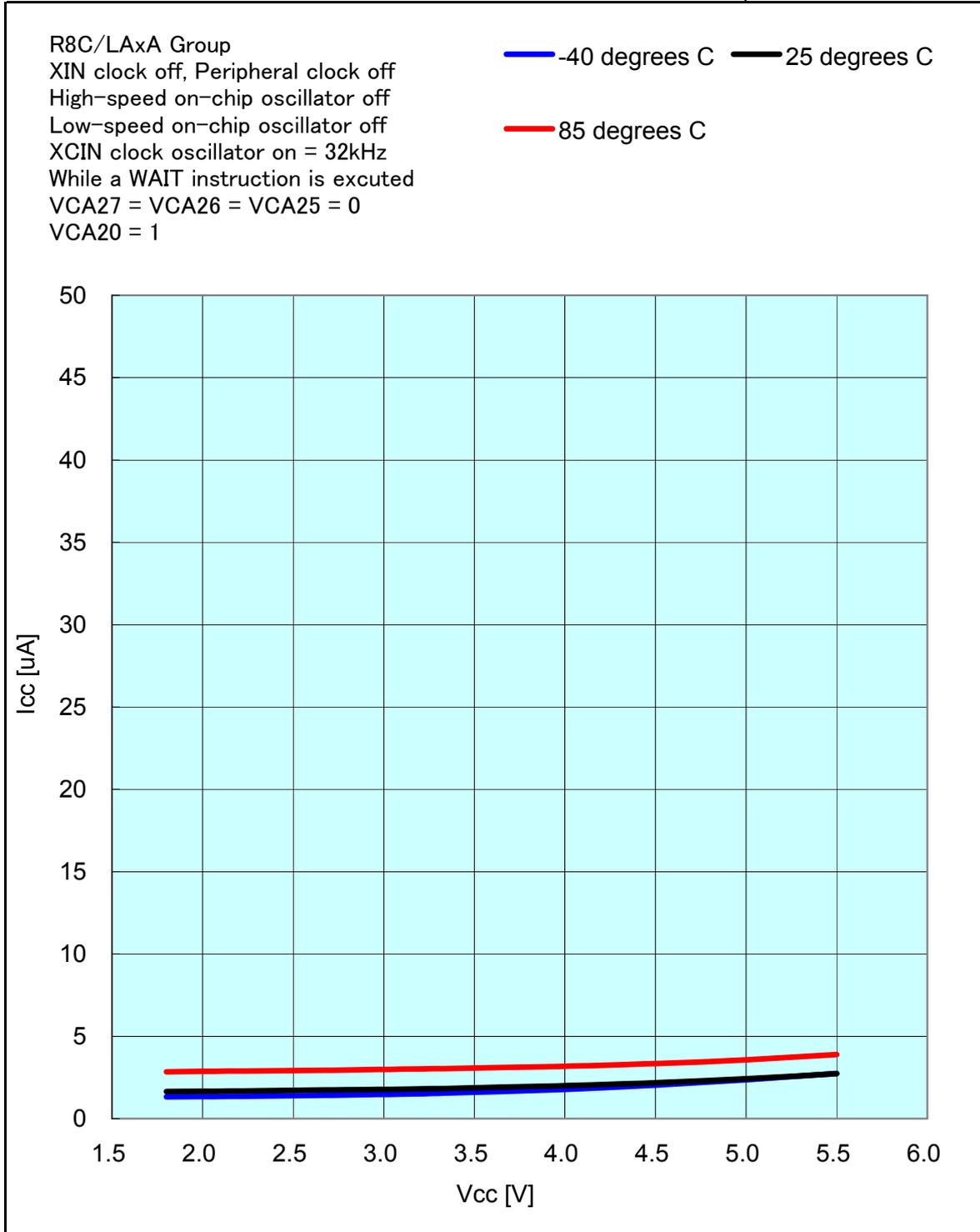


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Icc VS Vcc

(Low-speed clock wait mode)

Prepared on Nov. 30, 2010

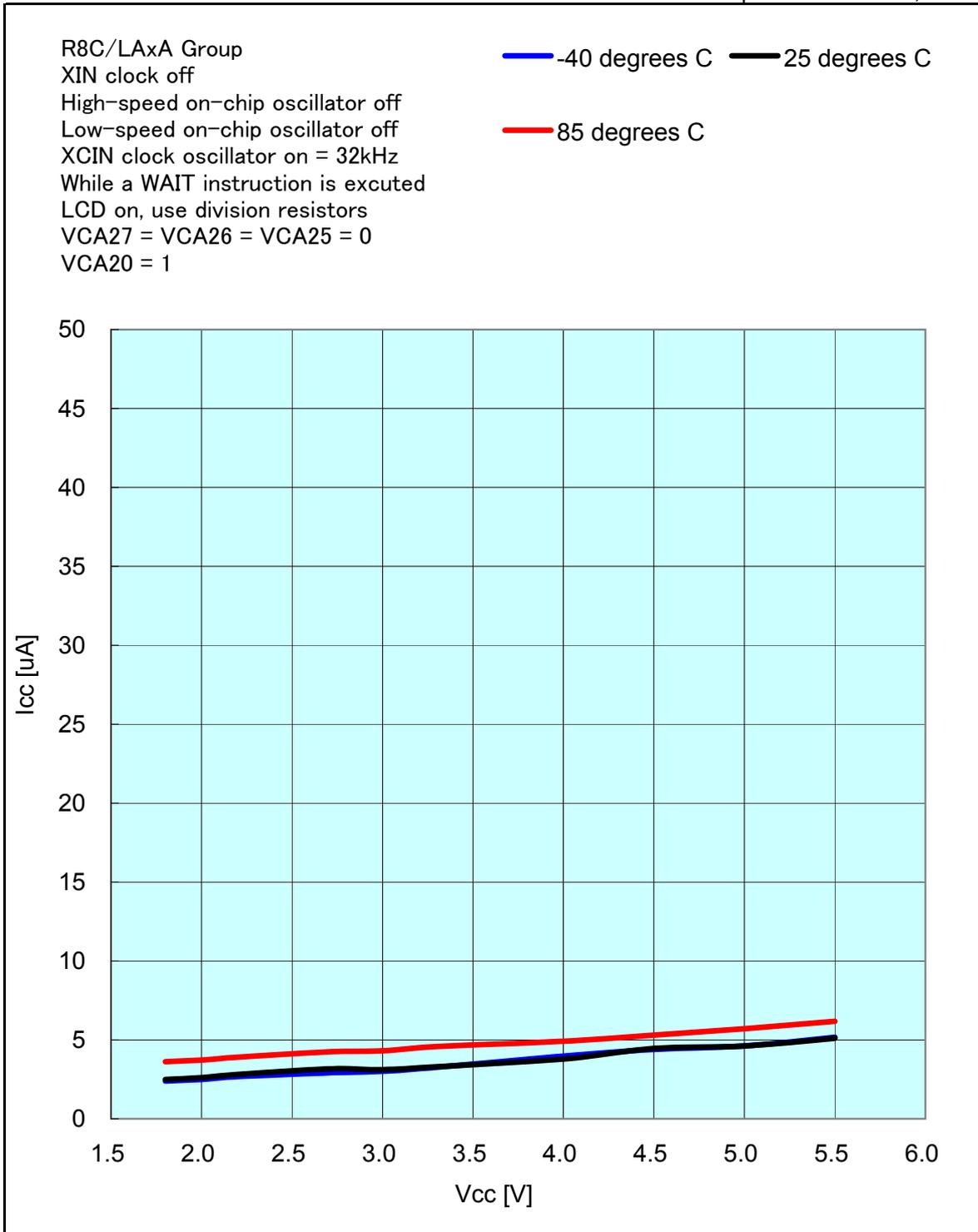


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Icc VS Vcc

(Low-speed clock wait mode)
LCD=ON(division resistors)

Prepared on Nov. 30, 2010

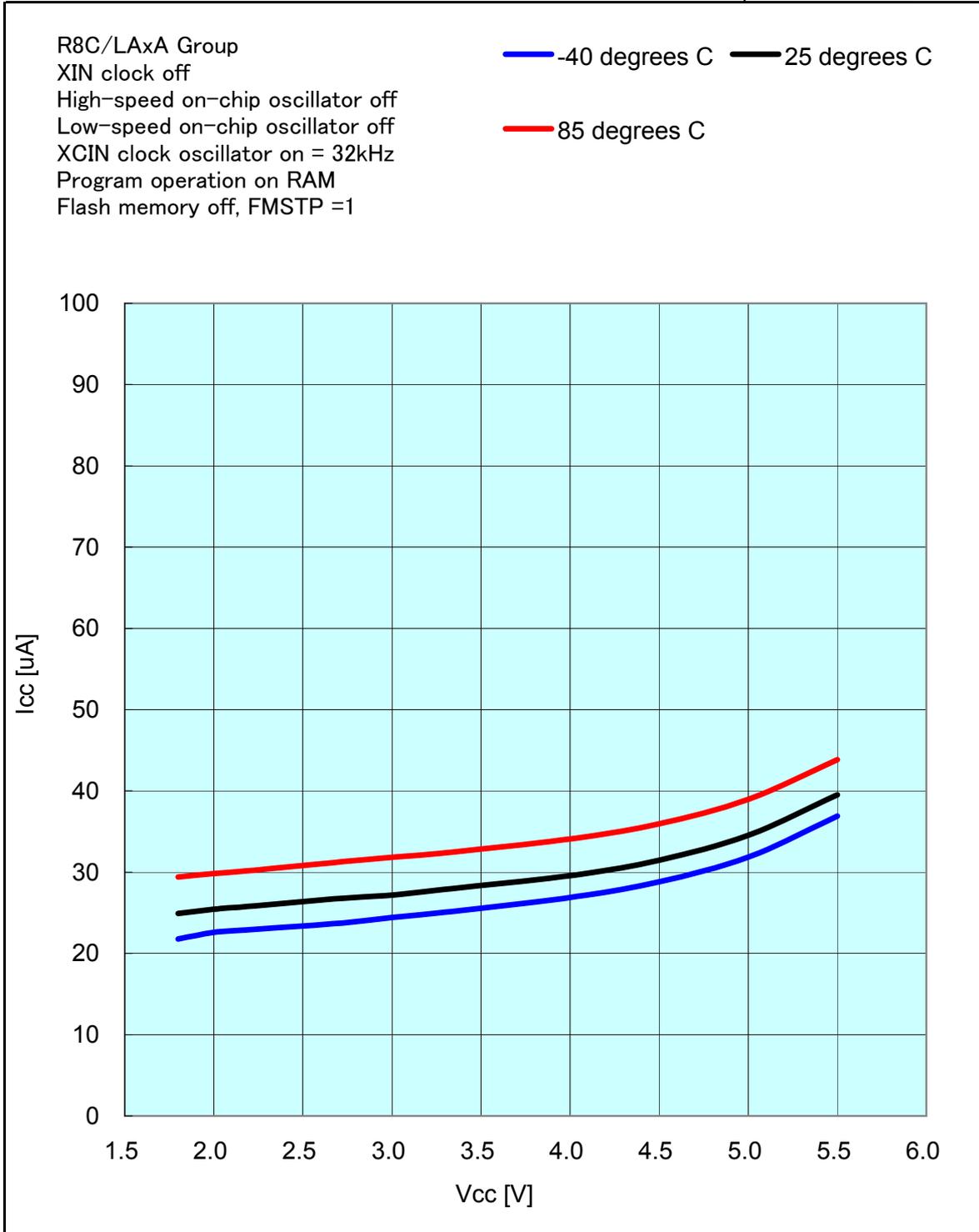


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Icc VS Vcc

(Low-speed clock mode)
Program operation on RAM

Prepared on Nov. 30, 2010

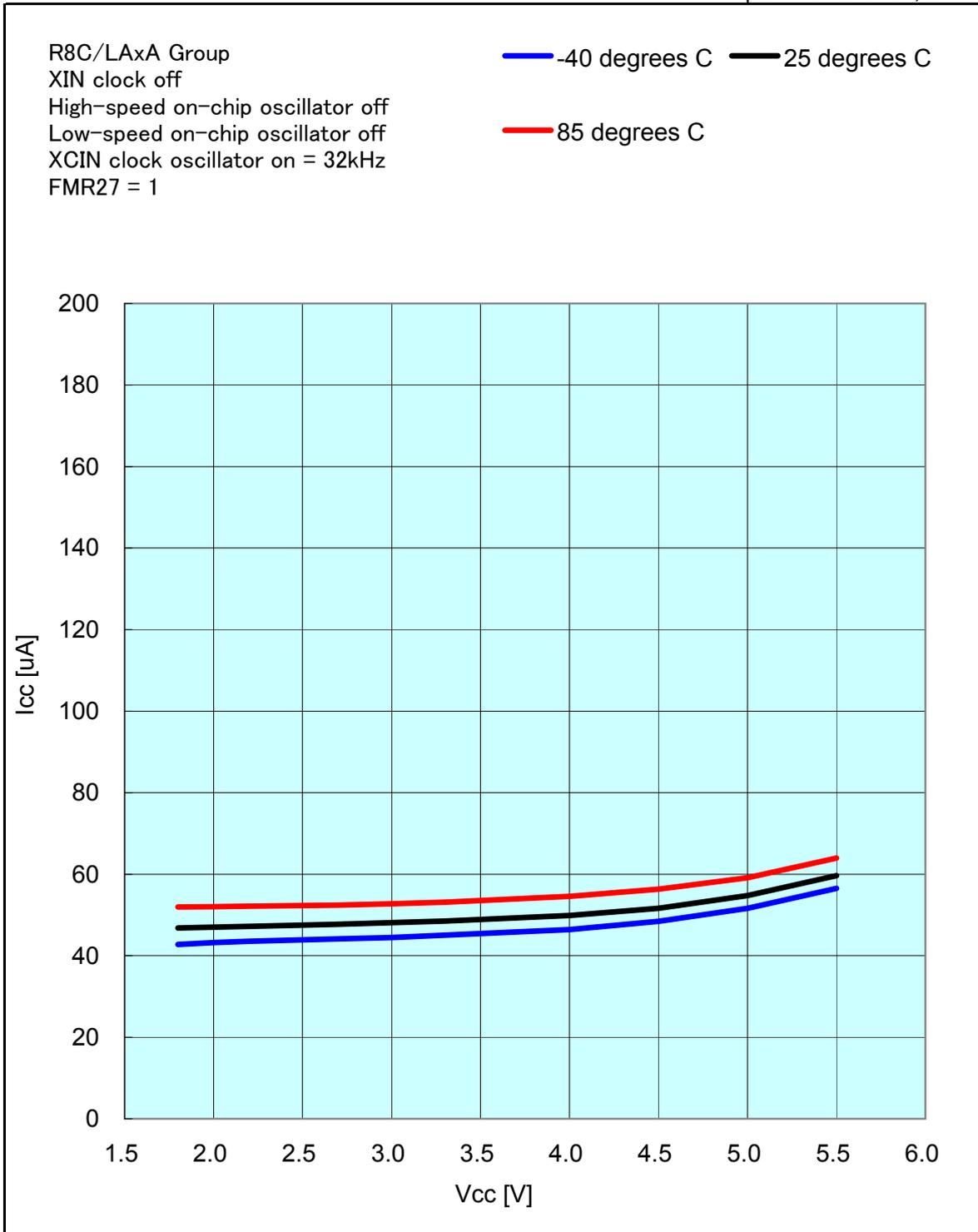


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Icc VS Vcc

(Low-speed clock mode)

Prepared on nov. 30, 2010



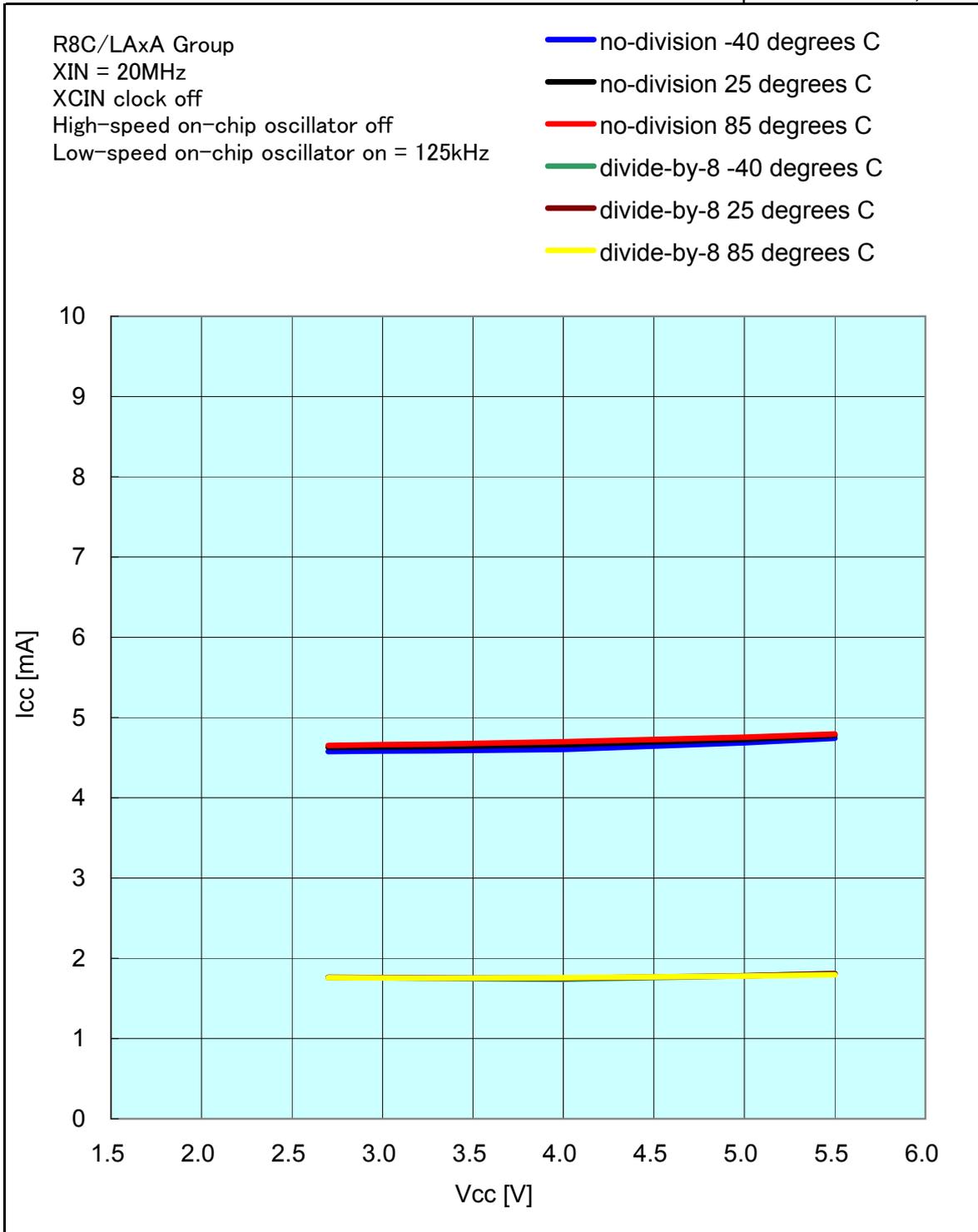
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Icc VS Vcc

(High-speed clock mode)

XIN = 20MHz

Prepared on Nov. 30, 2010



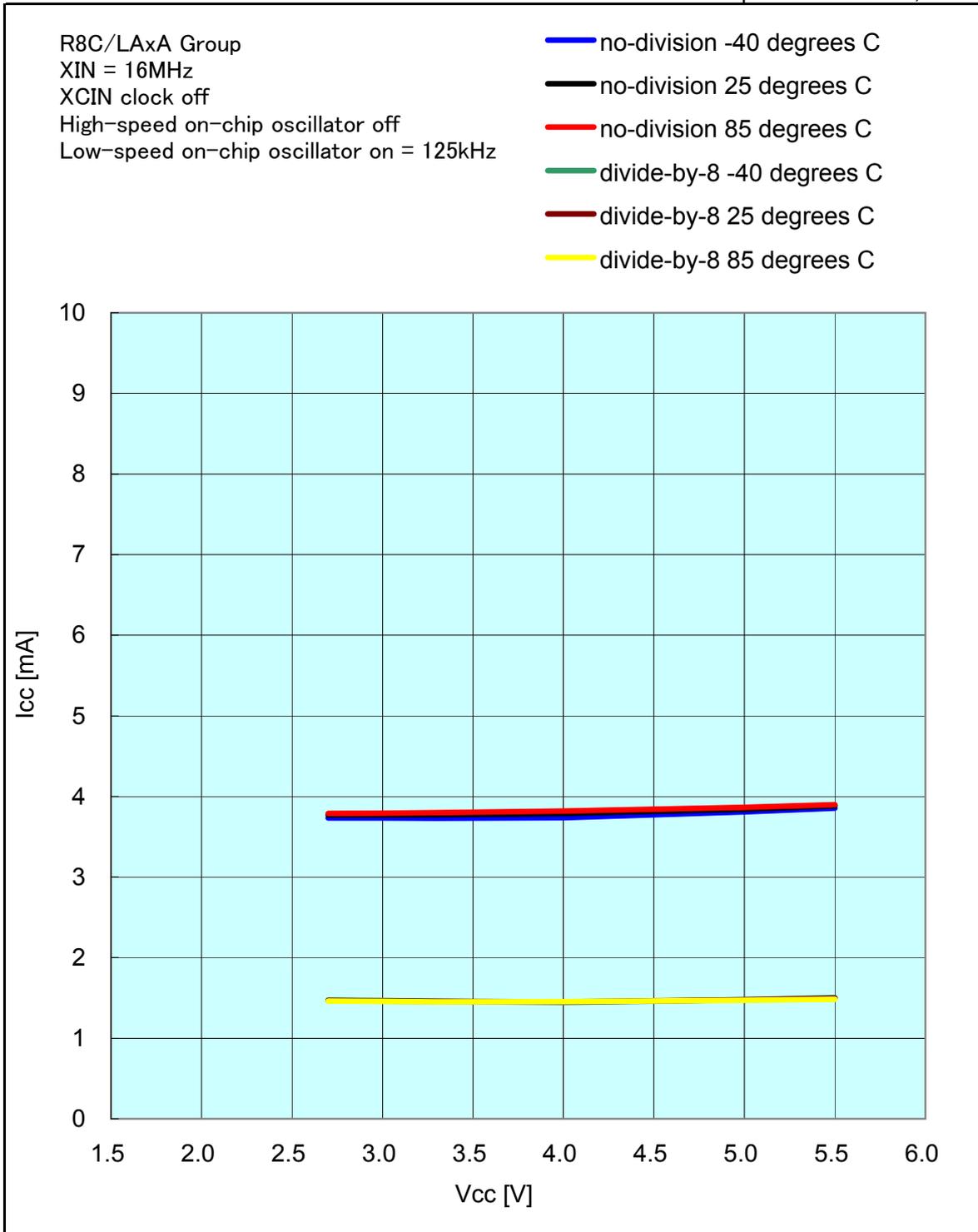
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Icc VS Vcc

(High-speed clock mode)

XIN = 16MHz

Prepared on Nov. 30, 2010



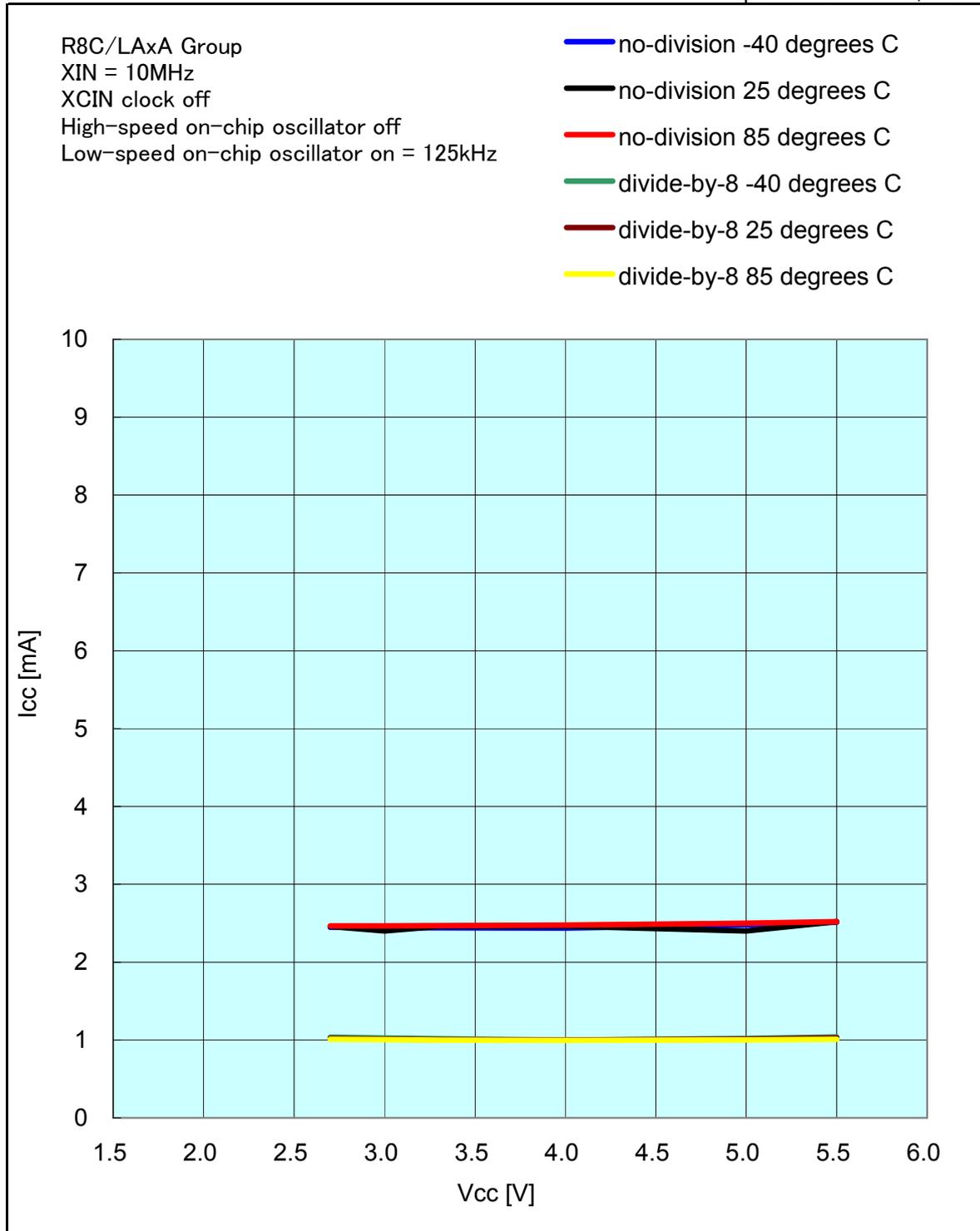
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Icc VS Vcc

(High-speed clock mode)

XIN = 10MHz

Prepared on Nov. 30, 2010



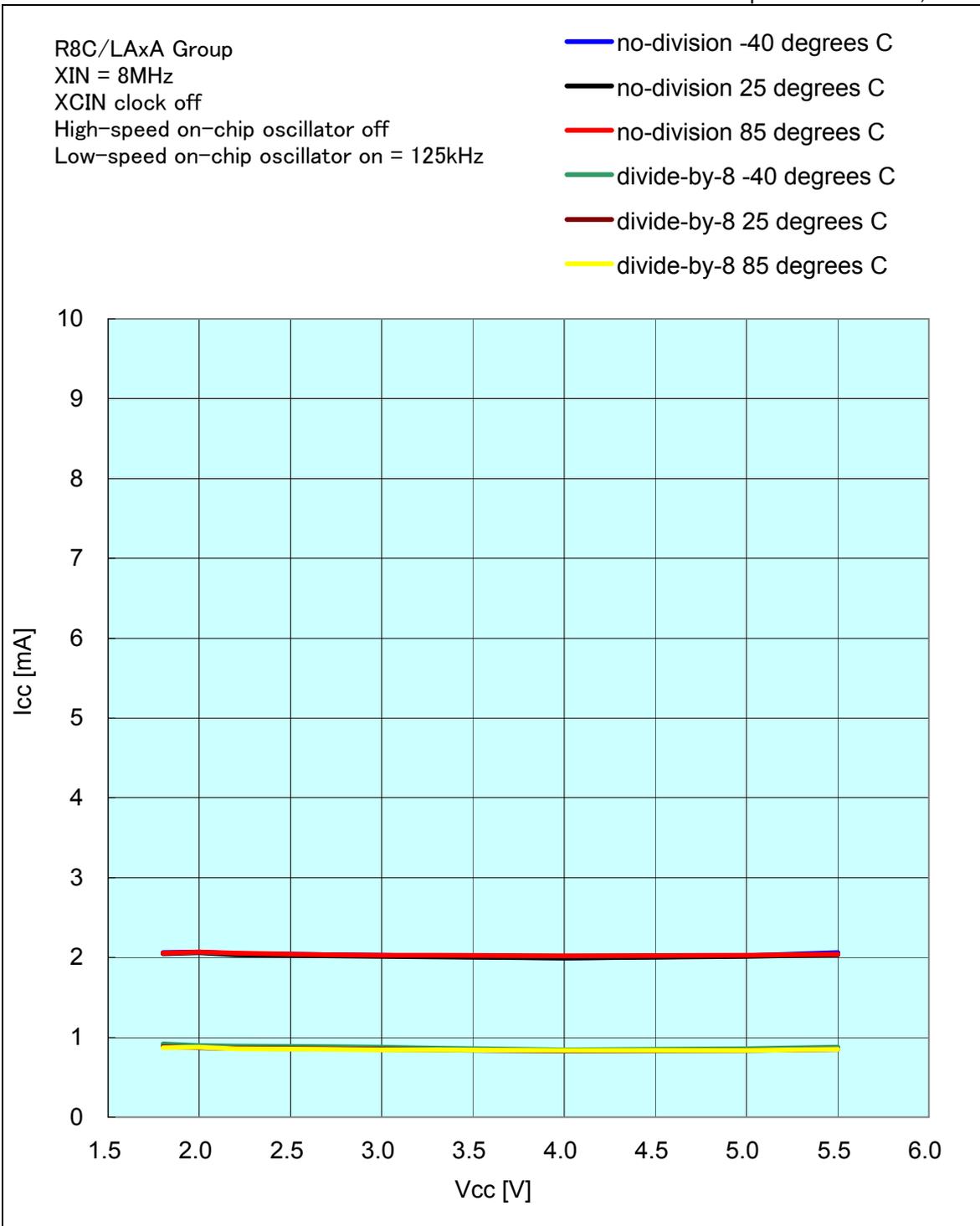
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I_{cc} VS V_{cc}

(High-speed clock mode)

XIN = 8MHz

Prepared on Nov. 30, 2010



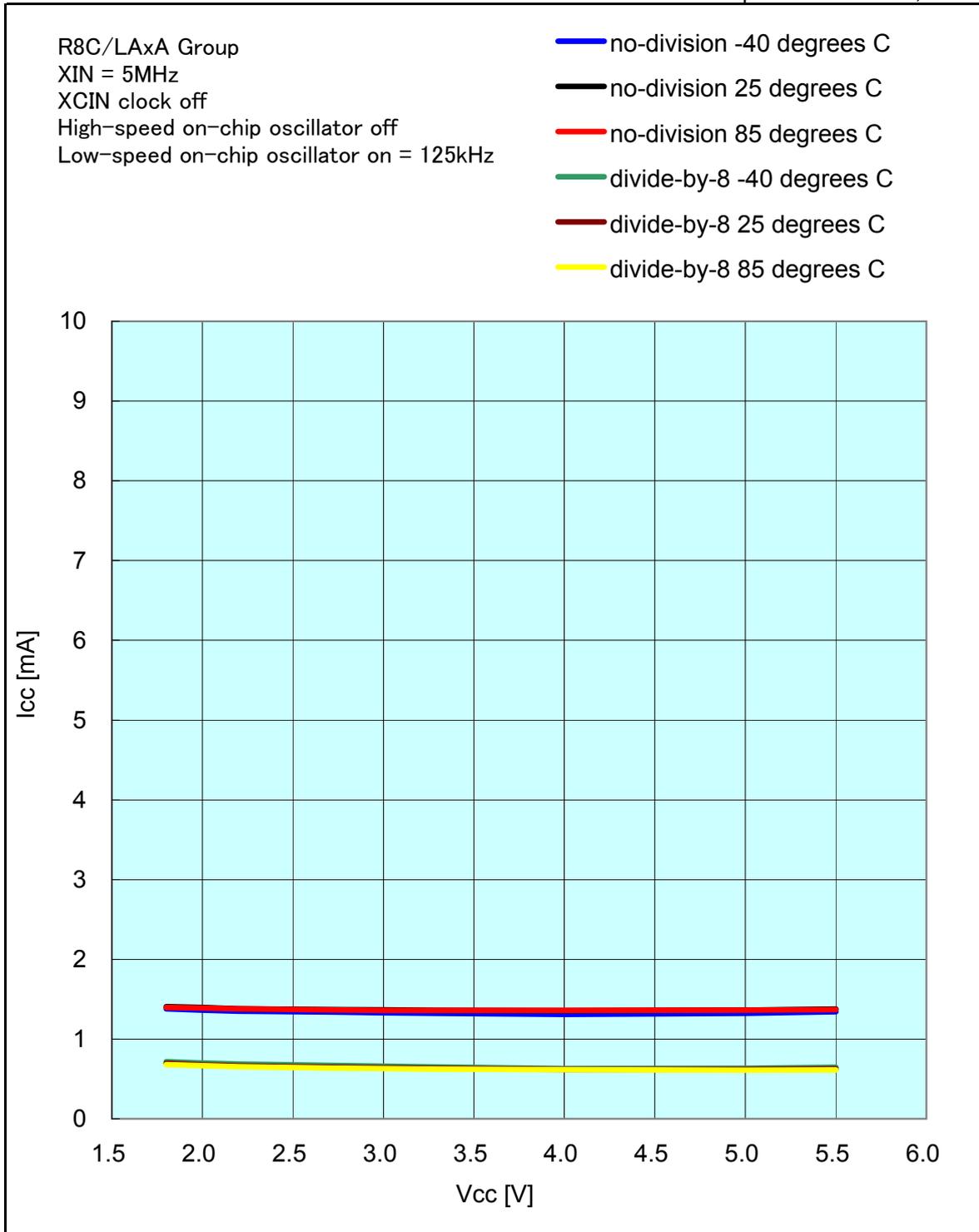
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Icc VS Vcc

(High-speed clock mode)

XIN = 5MHz

Prepared on Nov. 30, 2010



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